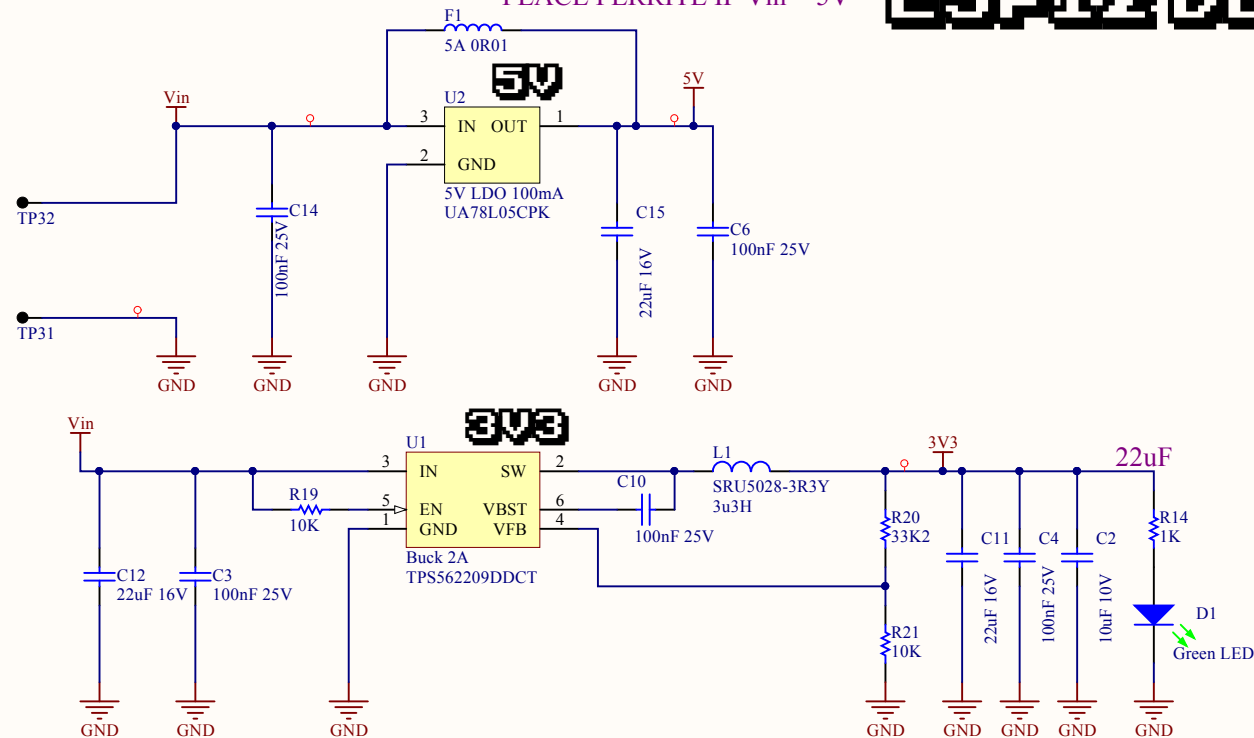


AREA 3001

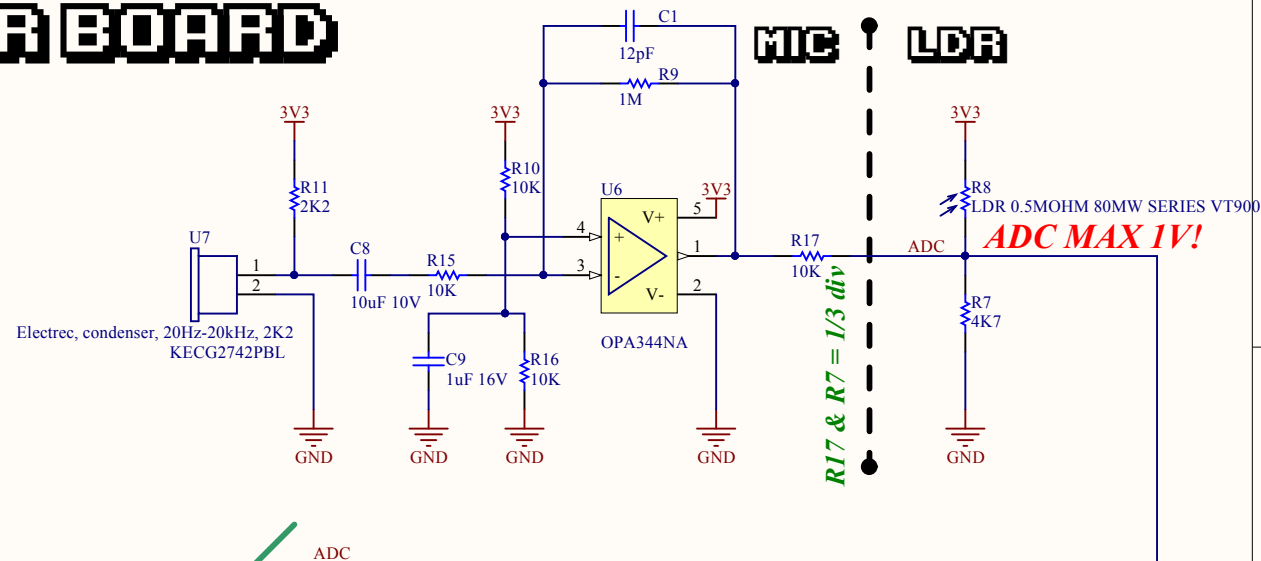
ESP12 DEVELOPER BOARD

POWER

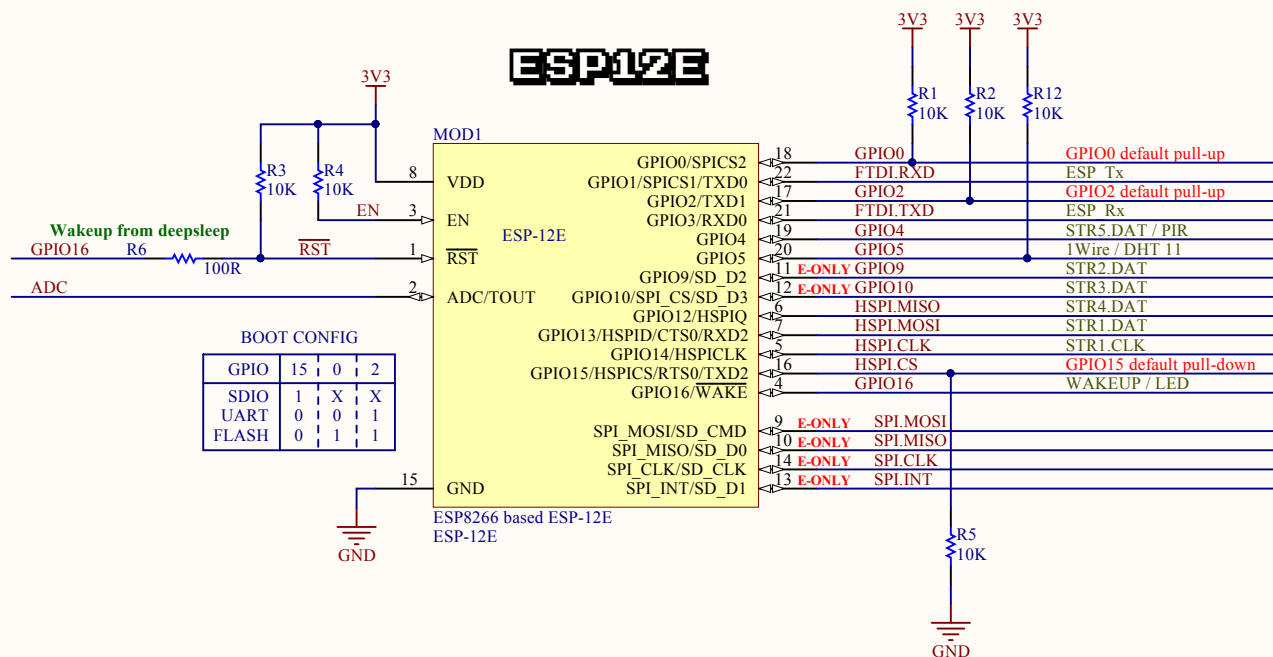
PLACE FERRITE IF Vin = 5V



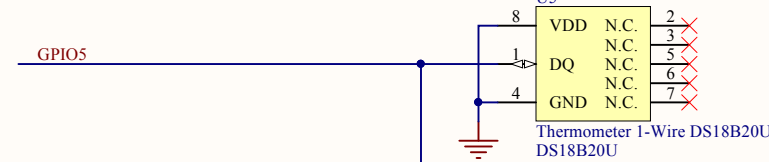
ANALOG



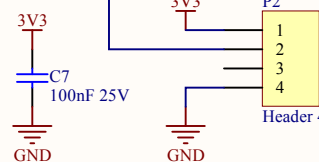
ESP12E



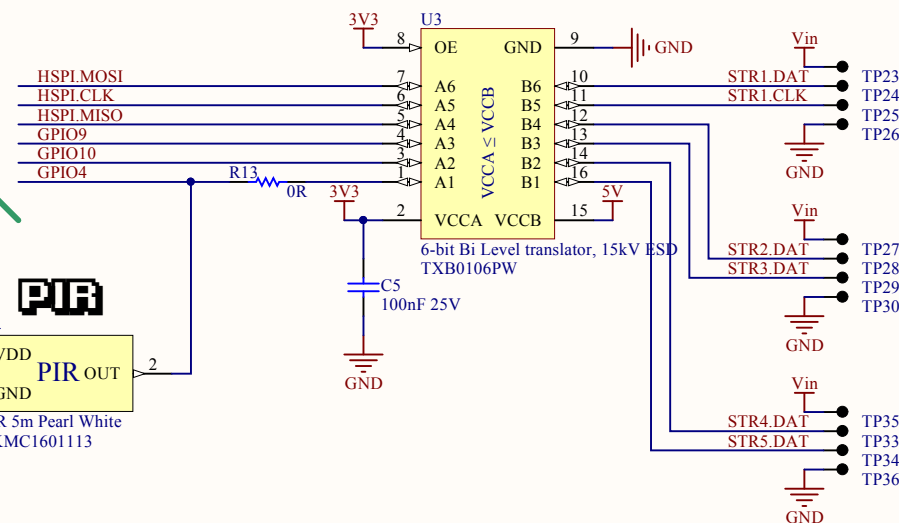
DS18B20



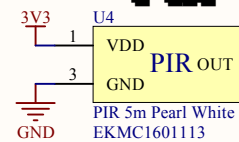
DHT11



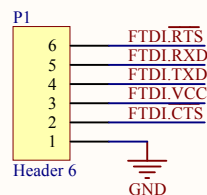
BUFFER



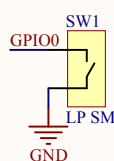
PIR



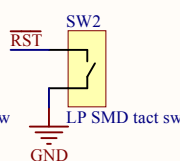
UART



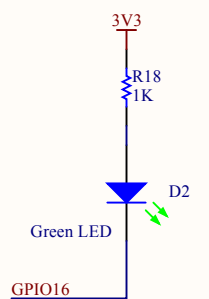
BOOT



RESET



DEBUG LED



Phyx

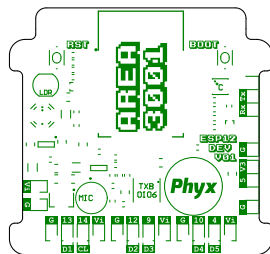
Title
ESP12_DEV

Date
18/06/2015

Drawn By
Wim Van Gool

Size
A3

Sheet 1 of 1



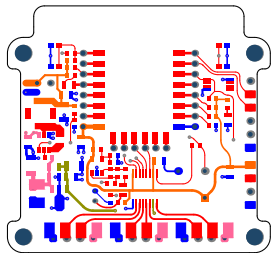
Document
ESP12_DEV

| Layer | Name |
|-------|-------------|
| 1 | Top Overlay |

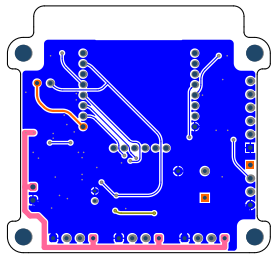
| | |
|------------|-----|
| Date | Rev |
| 29/08/2015 | 00 |

Designer
WimVG

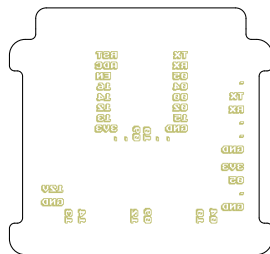
| | |
|----------|---------------------|
| Document | ESP12_DEV_01.PcbDoc |
|----------|---------------------|



| | | |
|-------------------|---------------------------------|-----------|
| Phyx | Document ESP12_DEV | |
| | Layer Name Top Layer | |
| | Date 29/08/2015 | Rev 00 |
| Designer WimVG | Document ESP12_DEV_01_PcbDoc | |



| | | |
|-------------|--------------|---------------------|
| Phyx | Document | |
| | ESP12_DEV | |
| | Layer Name | |
| | Bottom Layer | |
| | Date | Rev |
| | 29/08/2015 | 00 |
| Designer | | Document |
| WimVG | | ESP12_DEV_01.PcbDoc |



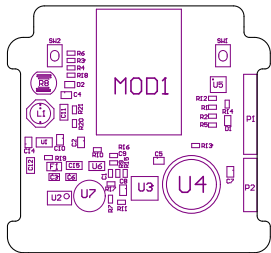
Document
ESP12_DEV

| Layer Name |
|----------------|
| Bottom Overlay |

| | |
|------------|-----|
| Date | Rev |
| 29/08/2015 | 00 |

Designer
WimVG

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|----------|---------------------|
| Document | ESP12_DEV_01.PcbDoc |
|----------|---------------------|



| | | | |
|---------------------------------------------------------------------------------------|------------|-----|---------------------|
|  | Document | | ESP12_DEV |
| | Layer Name | | Assembly Top |
| | Date | Rev | |
| | 29/08/2015 | 00 | |
| Designer | WimVG | | Document |
| | | | ESP12_DEV_01.PcbDoc |