The details on the back page show how the AES algorithm operates for encryption and decryption. The input to each round is a 128-bit data block often represented as a 4x4 matrix, given below. The substitute bytes table, Shift rows and Mix Column matrix details are provided on the back pages.

Using these details show what the input (to Sub Bytes operation) matrix/state below is transformed to after:

i) The Substitute Bytes operation.

ii) The Shift rows operation. (2%)

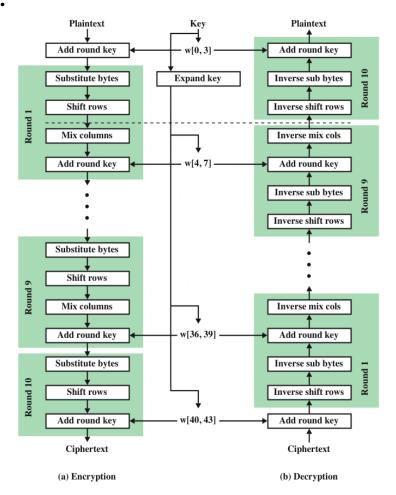
iii) Then show what the output of the Mix Columns operation is for **Column 1** of the resultant matrix from part (ii). (6%)

(2%)

18	0A	B9	В5
64	68	6A	FB
5A	EF	D7	79
8E	B2	10	D4

Input State

Details for Q2.c.



		у															
		0	1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F
x	0	63	7C	77	7B	F2	6B	6F	C5	30	01	67	2B	FE	D7	AB	76
	1	CA	82	C9	7D	FA	59	47	F0	AD	D4	A2	AF	9C	A4	72	C0
	2	В7	FD	93	26	36	3F	F7	CC	34	A5	E5	F1	71	D8	31	15
	3	04	C7	23	C3	18	96	05	9A	07	12	80	E2	EB	27	B2	75
	4	09	83	2C	1A	1B	6E	5A	Α0	52	3B	D6	В3	29	E3	2F	84
	5	53	D1	00	ED	20	FC	B1	5B	6A	СВ	BE	39	4A	4C	58	CF
	6	D0	EF	AA	FB	43	4D	33	85	45	F9	02	7F	50	3C	9F	A8
	7	51	А3	40	8F	92	9D	38	F5	ВС	В6	DA	21	10	FF	F3	D2
	8	CD	0C	13	EC	5F	97	44	17	C4	A7	7E	3D	64	5D	19	73
	9	60	81	4F	DC	22	2A	90	88	46	EE	B8	14	DE	5E	0B	DB
	Α	E0	32	3A	0A	49	06	24	5C	C2	D3	AC	62	91	95	E4	79
	В	E7	C8	37	6D	8D	D5	4E	A9	6C	56	F4	EA	65	7A	AE	08
	С	BA	78	25	2E	1C	A6	B4	C6	E8	DD	74	1F	4B	BD	8B	8A
	D	70	3E	B5	66	48	03	F6	0E	61	35	57	В9	86	C1	1D	9E
	E	E1	F8	98	11	69	D9	8E	94	9B	1E	87	E9	CE	55	28	DF
	F	8C	A1	89	0D	BF	E6	42	68	41	99	2D	0F	B0	54	BB	16
nis e	examp	ole sh	ows h	now th	ie Sul	ostitut	e Byte	es op	eratio	n wor	ks						

51

63

F8

68

04

BA

32

79

E1

Α1

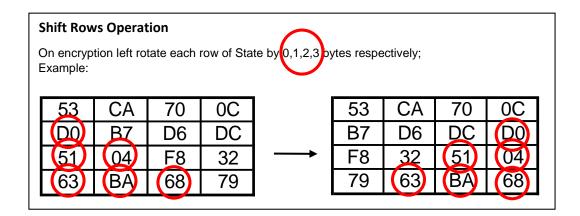
30

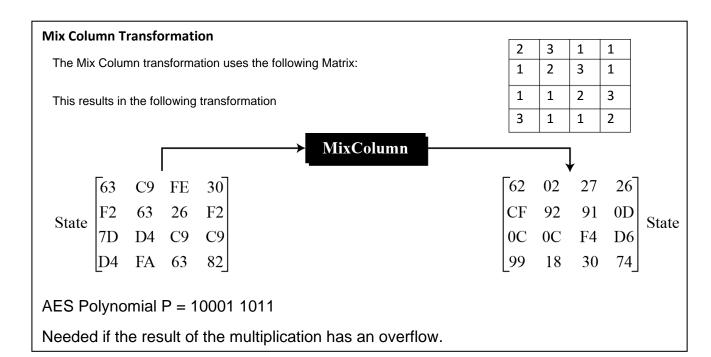
C0

70

00

Details for Q2.c. continued.





14 OA B9 64 68 6A SA EF D7

14 OA B9 B5
64 68 6A FB
5A EF D7 79
8E B2 10 D4

AD 67 56 D5
43 45 03 05

43 45 02 OF BE OF OE B6 19 37 CA 48

67 56 AO 05 45 02 OF 43 OE 136 BE DF 48 37 CA 19

This is After Round Key Addition

After Substitute bytes

After Shift Rows

No shift
shift left by I (Rotate)
Rotate " by Z
" 3

Mix Columns

Replace AD with 02 * AD () 03 * 45 () 01 * 06 () 01 * 48

Replace 45 with 01 * AD () 02 * 45 () 03 * 06 () 01 * 48

Peplace OE. with 01 * AD () 01 * 45 () 02 * 06 () 03 * 48

Peplace 48 with 03 * AD () 01 * 45 () 01 * 06 () 02 * 48

AD + 02 AD = 1010 1101 $402 \Rightarrow |0101 1010$ 40R P = 10001 1011bit 9 set so perform XOR with P (10001 1011) 0100 0001 = 41 45 * 03 = (45 * 2) XOR 45 $45 = 0100 \ 0101$ $*02 \Rightarrow 1000 \ 1010$ $*08 \ 45 = 0100 \ 0101$ $1100 \ 1111 = CF$ [OE * 01 [48 ×01 Now Perfor XOR 0100 0001 41 (F) 1100 1111 CF 1000/110 10000000 OE 1100 1000 48

Replace AD with C8

C 8

Now XOR Them.

$$OE # 02$$
 $OE = 0000 | 1110$
 $*02 = 0001 | 1100 = 10$

NOW YOR Then

$$\begin{array}{rcl}
\text{AD} &=& 1010 & 1101 \\
\text{45} &=& 0100 & 0101 \\
\hline
1110 & 1000 \\
\text{1} &=& 0001 & 1100 \\
\hline
\text{1} &=& 0001 & 1100 \\
\hline
\text{1} &=& 0000 & 1100 \\
\hline
\text{010} &=& 1000 & 1100 \\
\hline
\end{array}$$

$$AD * 03 = (A0 * 02) \times 0R AD$$

$$AD = |0|0||0|0|$$

$$402 = |0|0||0|0|$$

$$1000 |0|0|$$

$$0 |0|0 |0|0|$$

$$10|0 |1|0|$$

$$10|0 |1|0|$$

$$10|0 |1|0|$$

$$10|0 |1|0|$$

$$10|0 |1|0|$$

$$10|0 |1|0|$$

$$10|0 |1|0|$$

$$10|0 |1|0|$$

$$10|0 |1|0|$$

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$$10|0 |0|1|$$

$$10|0 |0|1|$$

$$10|0 |0|1|$$

$$10|0 |0|1|$$

$$10|0 |0|1|$$

$$10|0 |0|1|$$

$$10|0 |0|1|$$

$$10|0 |0|1|$$

column I becomes

45 0E 48 C8 7D 2C 37