

麦克风选型,外围电路 及 Layout 指南

文档履历

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1. 目标概述

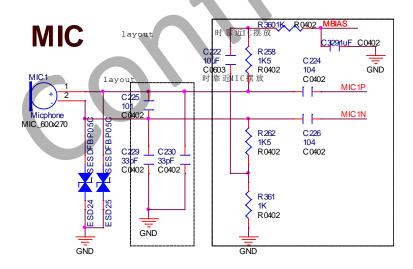
本指南用于指导应用者根据不同应用需求,选择麦克风类型及其外围电路,以及 PCB LAYOUT 注意点,达到高质量的录音效果。

2. 麦克风选型

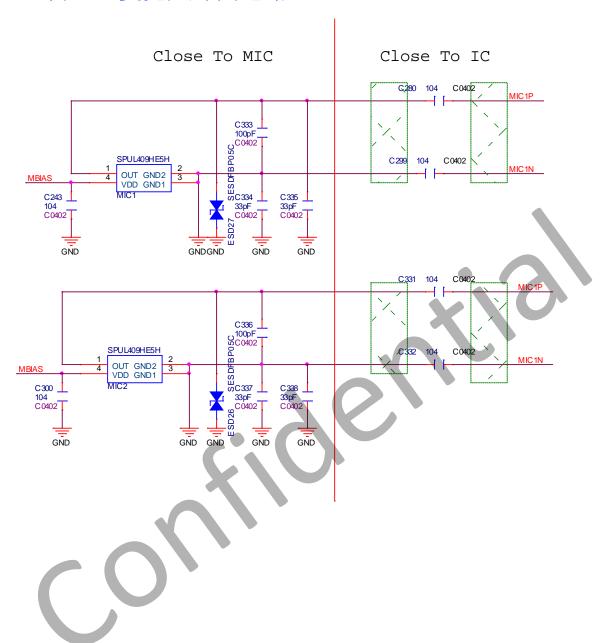
序号	应用场景	麦克风类型	灵敏度
1	普通录音(单 MIC)	驻极体	$-42dB \sim -45dB(\pm 3dB)$
2	普通录音(双 MIC 降噪)	硅麦克风	$-42dB \sim -45dB(\pm 1dB)$
3	视频通话(单 MIC)	驻极体	>= -38dB(±3dB)
4	视频通话(双 MIC 降噪)	硅麦克风	\Rightarrow = -38dB(±1dB)

3. 硬件电路

3.1. 内置驻极体麦克风外围电路

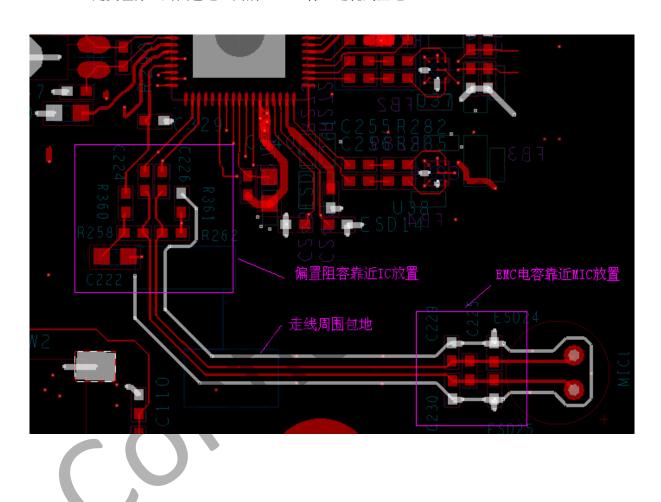


3.2. 内置硅麦克风外围电路



4. PCB LAYOUT

- 1.偏置阻容器件靠近 IC 放置;
- 2.EMC 电容靠近 MIC 放置;
- 3.MICP/N 走类差分,周围包地,间隔 100mil 打一过孔到主地。



5. Declaration

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