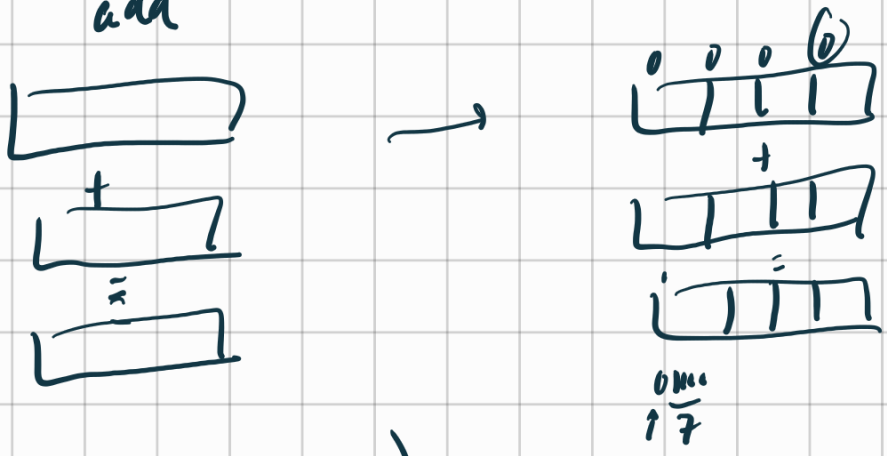
SIMD

Single - Instruction Multiple Data

32 bits add



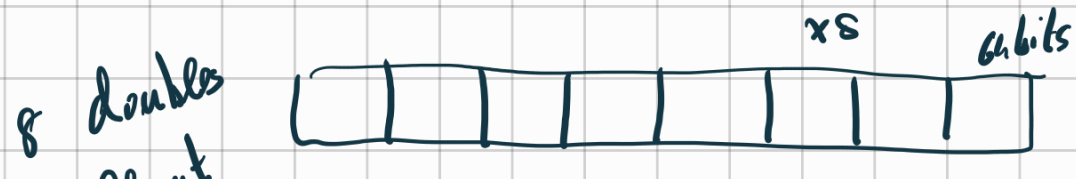
(a & 0b0100011)

a | 0b0100011

Intel/AMD

mmx 64 bits

xmm 128 bits
ymm 256 bits
zmm 512 bits



CUDA (Nvidia, AMD)