• 04/05	days Mon 04/05/1Mon 04/05/1	Development comple0 days	ų.	35
	10 days Mon 25/05/1Fri 05/06/15	Developer testing 10	,	34
	25 days Mon 04/05/1Fri 05/06/15	Develop code 25		33
- 0 %	days Thu 07/05/15Fri 08/05/15	Identify modular desi,2 days		32
0%	Fri 01/05/15	Review specifications 5 days	.,	<u>3</u>
1	26 days Mon Mon 04/05/15 08/06/15	Implementation and 26	*	30
→ 31/03	0 days Tue 31/03/15Tue 31/03/15	Design complete 0	"	29
0%	26 days Mon 30/03/1Sun 03/05/15	Documentation 26	*	28
	21 days Mon 30/03/1Sat 25/04/15	Database design 21	*	23
}	9 days Sat 11/04/15Wed 22/04/114	Component design 9	*	20
0%	5 days Tue 31/03/15Mon 06/04/1	Interface design 5	,	19
0%	4 days Wed 01/04/1Sat 04/04/15	Activity diagrams 4	*	18
0%	Sat 28/03/15 Fri 03/04/15	Sequence diagram:7 days	* 7000	17
April April 1990 1990	Condition State Initial Incoressors resource realities (Fig. 2000) 145			7