ID	A	Task Mode	Task Name	Duration	Start	Finish	Predecessors	Resource Names	March April May June 15/02 22/02 01/03 08/03 15/03 22/03 29/03 05/04 12/04 19/04 26/04 03/05 10/05 17/05 24/05 31/05 07/06
2	0	*	Component design	9 days	Sat 11/04/15	Wed 22/04/1	14		13/02 22/02 01/03 30/03 13/03 22/03 03/01 12/01 03/01 13/01 23/03 1
2	1 🗸	*	Class diagrams	7 days	Sat 11/04/15	Sat 18/04/15			100%
2	2	*	Package diagrams	4 days	Sat 18/04/15	Wed 22/04/1			0%
_		*	Database desires	24 -1	24 20/02/	15-+ 25 /04 /45			
4	3	*	Database design	21 days	IVION 30/03/	1Sat 25/04/15			
	4 🗸	*	Conceptual Design	12 days	Mon 30/03/1	Tue 14/04/15			100%
			conseptual Design	12 00,5		1.00 1.70 .710			
2	5 🗸	*	Logical Design	6 days	Wed 15/04/1	1Wed 22/04/1	24		100%
2	6	*	Physical Design	3 days	Thu 23/04/1	Sat 25/04/15	25		── 0 %
2	7	*	Documentation	19 days	Wed 01/04/1	1Sat 25/04/15			75%
2	8	*	Documentation	26 days	Mon 30/03/1	1Sun 03/05/15			100%
_	9	_	Design complete	0 days	Tue 21/02/1	Tue 31/03/15			→ 31/03
'	9	-5	Design complete	0 days	Tue 31/03/1	1ue 31/03/13			\$ 3,733
-	0	*	Implementation and	26 days	Mon	Mon			
			unit testing		04/05/15	08/06/15			9%
-	1	->	Review specifications	s 5 days	Fri 01/05/15	Thu 07/05/15			U%
-	2	_5	Identify modular des	i 2 davs	Thu 07/05/1	Fri 08/05/15			0%
		-	identity modular des	.2 00,0	1114 077 057 1	55, 55, 15			
3	3	-5	Develop code	25 days	Mon 04/05/1	1Fri 05/06/15			0%
-3	4		Developer testing	10 days	Mon 25/05/1	1Fri 05/06/15			0%
3	5		Development comple	e 0 days	Mon 04/05/1	1Mon 04/05/1			♦ 04/05
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