

Project: DM1092\_LiPo  
Current Revision: R0M0E0

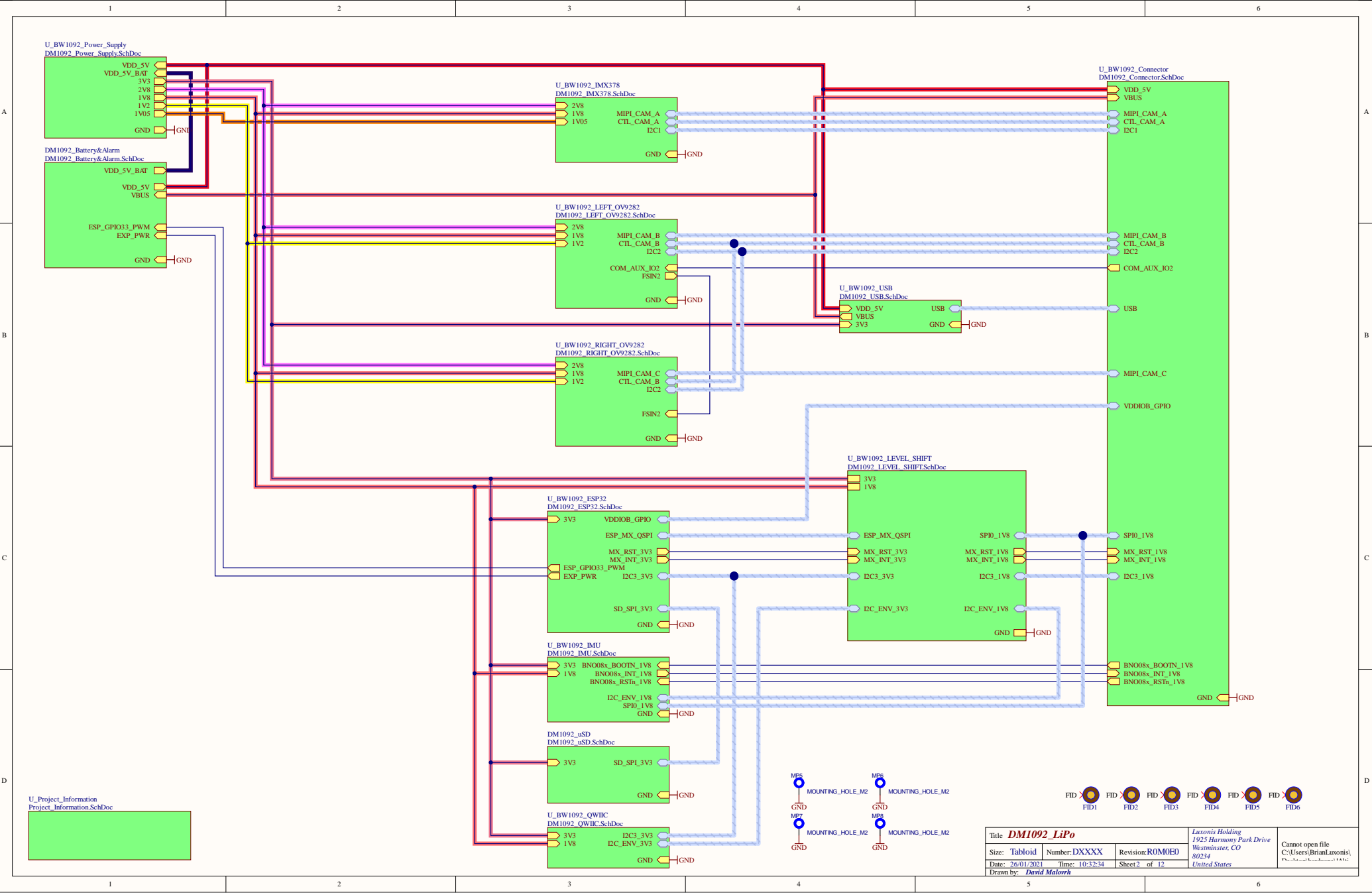
DM1092\_LiPo Revision History:

Date	Revision	Reason for Change	Changes Implemented
July 9th, 2020	Initial release		
December 7th, 2020	BW1092_R0M0E0 -> DM1092_R0M0E0	1. IMU data transfer maximum 1kHz rate over I2C 2. Issues with floating lines (to weak MX pull-up)	1. Changed power supply from 3V3 to 1V8 for IO, removed INT, RSTn and BOOTN from level shifter. Removed I2C3_3V3 interface added SPI0 connected directly to MX bus. Corrected chip setup for SPI. Updated routing and matched signals. 2. Added 10k pullups to BNO08s_INT_1V8 and BNO08s_RSTn_1V8
December 28th, 2020	DM1092_R0M0E0 -> DM1092_LiPo_R0M0E0	1. QWIC ENV connector I2C voltage level issue 2. Battery powered 3. Alarm buzzer	1. ENV I2C signals routed over level shifter to J12 2. Added BMS for LiPo battery and power switch which disconnects power to BW1099 and camera modules 3. Added buzzer and driving circuit

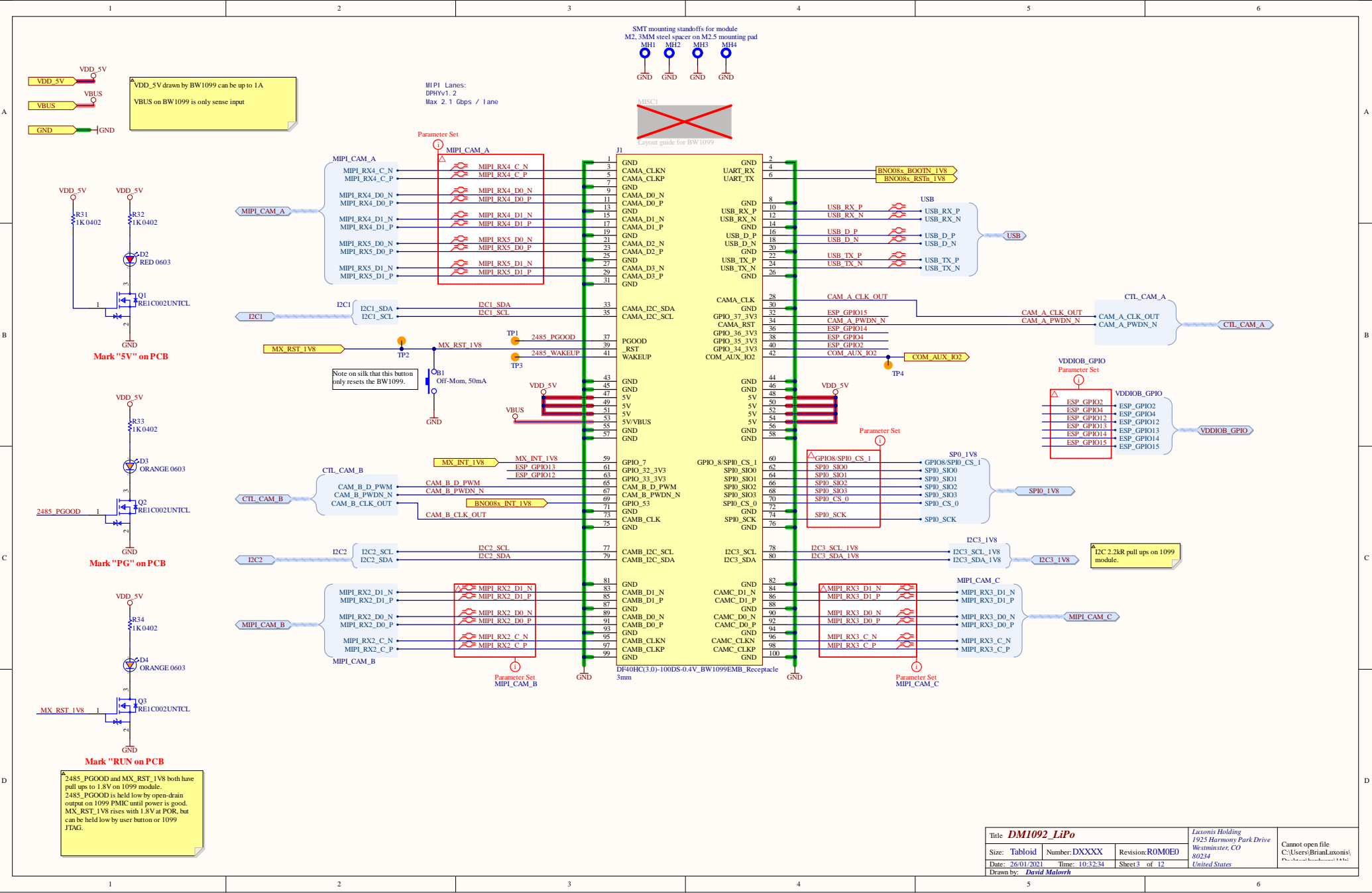
ESP32 WROOM_IO_MUX																DM1092				BW1099EMB			
ESP32 Pin	ESP32-WROOM-32D PIN	Analog Function1	Analog Function2	Analog Function3	RTC Function1	RTC Function2	Function 1	Function 2	Function 3	Function 4	Function 5	Function 6	GPIO Matrix	At RST	After RST	DM1092 NET NAME	Level Shift	Level Shifted NET NAME	QUIIC / AUX connector/ uSD	1099 Connector Pin	1099 NET NAME	1099 PU/PD	
9	4	ADC_H	ADC1_CH0		RTC_GPIO0		GPIO36		GPIO36					0e=0,le=0	0e=0,le=0	ESP_GPIO36	no	n/a	AUX: J5,6				
8	5	ADC_H	ADC1_CH3		RTC_GPIO3		GPIO39		GPIO39					0e=0,le=0	0e=0,le=0	ESP_GPIO39	no	n/a	AUX: J5,7				
10	6		ADC1_CH6		RTC_GPIO4		GPIO34		GPIO34					0e=0,le=0	0e=0,le=0	ESP_GPIO34	no	n/a	AUX: J5,4 / ESP_GPIO34				
11	7		ADC1_CH7		RTC_GPIO5		GPIO35		GPIO35					0e=0,le=0	0e=0,le=0	ESP_GPIO35	no	n/a	AUX: J5,5				
12	8	XTAL_32K_P	ADC1_CH4	TOUCH9	RTC_GPIO9		GPIO32		GPIO32					0e=0,le=0	0e=0,le=0	MX_INT_3V3	3.3V <-> 1.8V	MX_INT_1V8		59	GPIO_7	40.2kR/1.8V	
13	9	XTAL_32K_N	ADC1_CH5	TOUCH8	RTC_GPIO8		GPIO33		GPIO33					0e=0,le=0	0e=0,le=0	ESP_GPIO33_PWM	no	n/a					
14	10	DAC_1	ADC2_CH8		RTC_GPIO6		GPIO25		GPIO25			EMAC_RXD0		0e=0,le=0	0e=0,le=0	I2C3_SCL_3V3	3.3V <-> 1.8V	I2C3_SCL_1V8	QUIIC: J11,1	78	GPIO_24	2.2kR/1.8V	
15	11	DAC_2	ADC2_CH9		RTC_GPIO7		GPIO26		GPIO26			EMAC_RXD1		0e=0,le=0	0e=0,le=0	I2C3_SDA_3V3	3.3V <-> 1.8V	I2C3_SDA_1V8	QUIIC: J11,2	80	GPIO_25	2.2kR/1.8V	
16	12		ADC2_CH7	TOUCH7	RTC_GPIO17		GPIO27		GPIO27			EMAC_RX_DV		0e=0,le=0	0e=0,le=1	ESP_GPIO27	3.3V <-> 1.8V	GPIO8/SPI0_CS_1	C	60	GPIO_8	no	
17	13		ADC2_CH6	TOUCH6	RTC_GPIO16		MTMS	HSPI_CLK	GPIO14	HS2_CLK	SD_CLK	EMAC_TXD2		0e=0,le=0	0e=0,le=1	ESP_GPIO14	no	n/a	AUX: J6,6	36	GPIO_36_3V3	40.2kR/1.8V	
18	14		ADC2_CH5	TOUCH5	RTC_GPIO15		MTDI	HSPI_Q	GPIO12	HS2_DATA2	SD_DATA2	EMAC_TXD3		0e=0,le=1,wpd	0e=0,le=1,wpd	ESP_GPIO12	no	n/a	AUX: J6,4	63	GPIO_33_3V3	40.2kR/1.8V	
20	16		ADC2_CH4	TOUCH4	RTC_GPIO14		MTCK	HSPI_D	GPIO13	HS2_DATA3	SD_DATA3	EMAC_TX_ER		0e=0,le=0	0e=0,le=1	ESP_GPIO13	no	n/a	AUX: J6,5	61	GPIO_32_3V3	40.2kR/1.8V	
21	23		ADC2_CH3	TOUCH3	RTC_GPIO13	I2C_SDA	MTDO	HSPI_CS0	GPIO15	HS2_CMD	SD_CMD	EMAC_RXD3		0e=0,le=1,wpu	0e=0,le=1,wpu	ESP_GPIO15	no	n/a	AUX: J6,7	32	GPIO_37_3V3	300kR/GND	
22	24		ADC2_CH2	TOUCH2	RTC_GPIO12	I2C_SCL	GPIO2	HSPI_WP	GPIO2	HS2_DATA0	SD_DATA0			0e=0,le=1,wpd	0e=0,le=1,wpd	ESP_GPIO2	no	n/a	AUX: J6,2	40	GPIO_34_3V3	40.2kR/1.8V	
23	25		ADC2_CH1	TOUCH1	RTC_GPIO11	I2C_SDA	GPIO0	CLK_OUT1	GPIO0			EMAC_TX_CLK		0e=0,le=1,wpu	0e=0,le=1,wpu	ESP_GPIO0	no	n/a					
24	26		ADC2_CH0	TOUCH0	RTC_GPIO10	I2C_SCL	GPIO4	HSPIHD	GPIO4	HS2_DATA1	SD_DATA1	EMAC_TX_ER		0e=0,le=1,wpd	0e=0,le=1,wpd	ESP_GPIO4	no	n/a	AUX: J6,3	38	GPIO_35_3V3	40.2kR/1.8V	
25	27						GPIO16	GPIO16	HS1_DATA4	U2RXD		EMAC_CLK_OUT	SD_SPI_CS	0e=0,le=0	0e=0,le=1	SD_SPI_CS	no	n/a	SD_SPI_CS				
27	28						GPIO17	GPIO17	HS1_DATA5	U2TXD		EMAC_CLK_OUT_180	SD_SPI_SCK	0e=0,le=0	0e=0,le=1	SD_SPI_SCK	no	n/a	SD_SPI_SCK				
34	29						GPIO5	VSPI_CS0	GPIO5	HS1_DATA6		EMAC_RX_CLK		0e=0,le=1,wpu	0e=0,le=1,wpu	VSPI_CS0	3.3V <-> 1.8V	SPI0_CS_0		70	SPI0_55_0	1kR/1.8V	
35	30						GPIO18	VSPI_CLK	GPIO18	HS1_DATA7				0e=0,le=0	0e=0,le=1	VSPI_SCK	3.3V <-> 1.8V	SPI0_SCK		74	SPI0_SCK	no	
38	31						GPIO19	VSPI_Q	GPIO19	UOCTS		EMAC_TXD0		0e=0,le=0	0e=0,le=1	VSPI_SDI_SIO1	3.3V <-> 1.8V	SPI0_SIO1		64	SPI0_SIO1	no	
42	33						GPIO21	VSPIHD	GPIO21			EMAC_TX_EN	SD_SPI_MOSI	0e=0,le=0	0e=0,le=1	VSPI_HOLDn_SIO3	3.3V <-> 1.8V	SPI0_SIO3	SD_SPI_MOSI		68	SPI0_SIO3	1kR/1.8V
40	34						U0RXD	CLK_OUT2	GPIO3					0e=0,le=1,wpu	0e=0,le=1,wpu	ESP_RXD0	no	n/a					
41	35						U0TXD	CLK_OUT3	GPIO1			EMAC_RXD2		0e=0,le=1,wpu	0e=0,le=1,wpu	ESP_TXD0	no	n/a					
39	36						GPIO22	VSPI_WP	GPIO22	U0RTS		EMAC_TXD1	SD_SPI_MISO	0e=0,le=0	0e=0,le=1	VSPI_WPn_SIO2	3.3V <-> 1.8V	SPI0_SIO2	SD_SPI_MISO		66	SPI0_SIO2	1kR/1.8V
36	37						GPIO23	VSPI_D	GPIO23	HS1_STROBE				0e=0,le=0	0e=0,le=1	VSPI_SDO_SIO0	3.3V <-> 1.8V	SPI0_SIO0		62	SPI0_SIO0	no	

PCA9570		DM1092		BW1099EMB		
BNO08S PIN	DM1092 NET NAME	Level Shift	Level Shifted NET NAME	QUIIC / AUX connector	1099 Connector PIN	1099 NET NAME
1	MX_RST_3V3	3.3V <-> 1.8V	MX_RST_1V8		39	SYS_RST
2	EXP_PWR	no	n/a		nc	
3	EXP_P2	no	n/a	AUX: J5,2	nc	
5	EXP_P3	no	n/a	AUX: J5,3	nc	
6	I2C3_SCL_3V3	3.3V <-> 1.8V	I2C3_SCL_1V8	QUIIC: J11,1	78	GPIO_24
7	I2C3_SDA_3V3	3.3V <-> 1.8V	I2C3_SDA_1V8	QUIIC: J11,2	80	GPIO_25
NOTE: Green boxes are intended primary usage. Orange boxes are nets enabled with jumper resistor populated.						

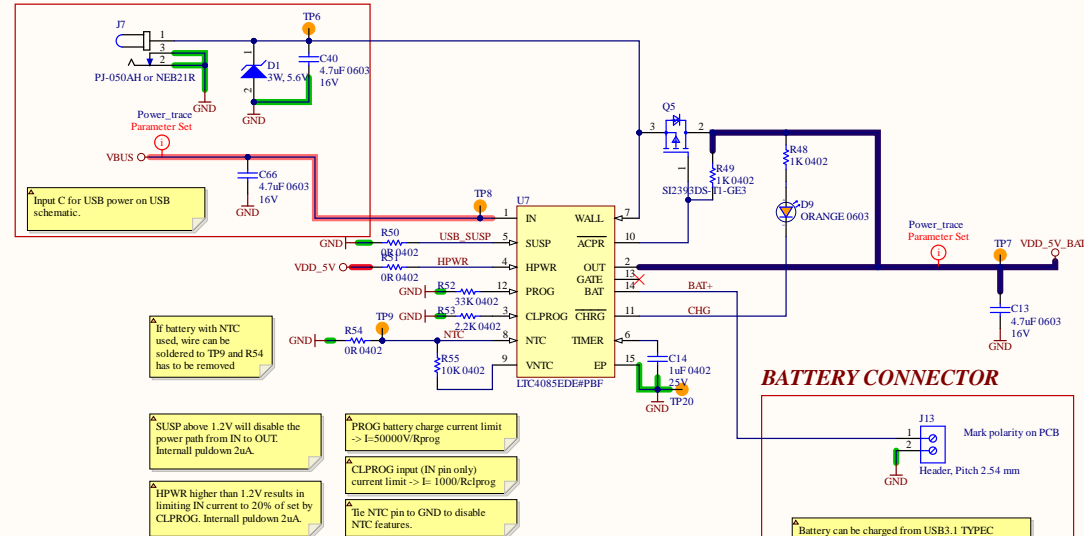
BNO08S		DM1092		BW1099EMB		
BNO08S PIN	DM1092 NET NAME	Level Shift	Level Shifted NET NAME	QUIIC / IO connector	1099 Connector PIN	1099 NET NAME
11	BNO08s_RSTn_1V8	no	n/a			
14	BNO08s_INT_1V8	no	n/a			
18	GPIO8/SPI0_CS_1	3.3V <-> 1.8V	ESP_GPIO27 (ESP_MX_QSPI)		60	GPIO_8
19	SPI0_SCK	3.3V <-> 1.8V	VSPI_SCK (ESP_MX_QSPI)		74	SPI0_SCK
17	SPI0_SIO0	3.3V <-> 1.8V	VSPI_SDO_SIO0 (ESP_MX_QSPI)		64	SPI0_SIO1
20	SPI0_SIO1	3.3V <-> 1.8V	VSPI_SDI_SIO1 (ESP_MX_QSPI)		62	SPI0_SIO0
15	ENV_SCL_3V3	3.3V <-> 1.8V	ENV_SCL_1V8	QUIIC: J12,1	nc	
16	ENV_SDA_3V3	3.3V <-> 1.8V	ENV_SDA_1V8	QUIIC: J12,2	nc	
4	BNO08s_BOOTN_1V8	no	n/a		4	UART_RX
NOTE: Green boxes are intended primary usage.						



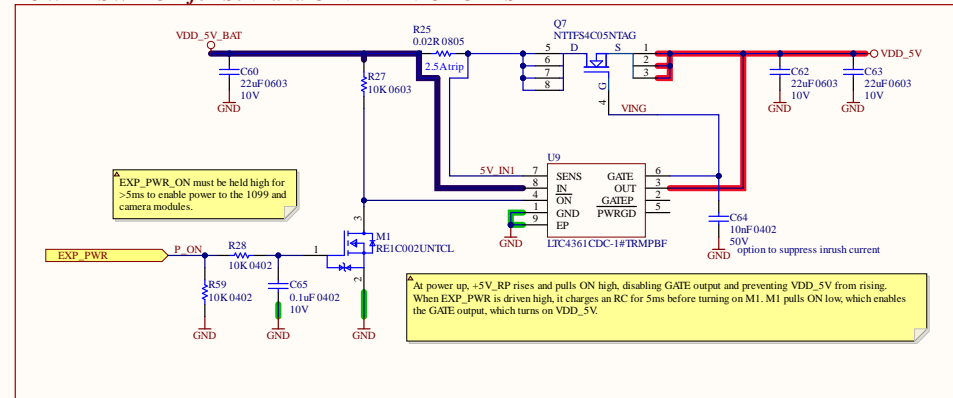
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Size: Tabloid	Number: DXXXX	Revision: ROM0E0			
Date: 26/01/2021	Time: 10:32:34	Sheet 2 of 12			
Drawn by: <i>David Malowh</i>					



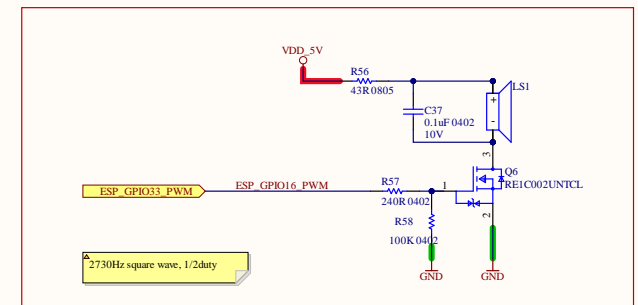
## POWER INPUT



## POWER SWITCH for SoM and CAMERA MODULES

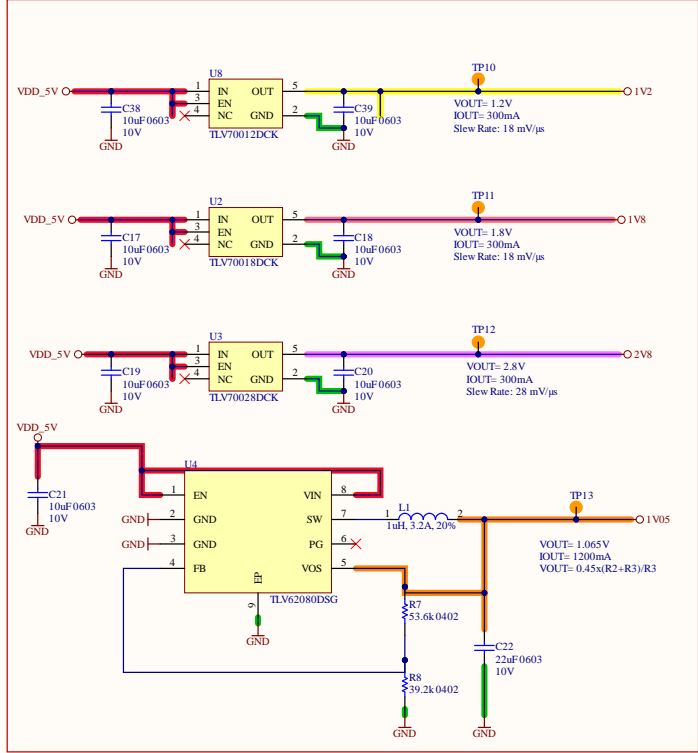


## BUZZER

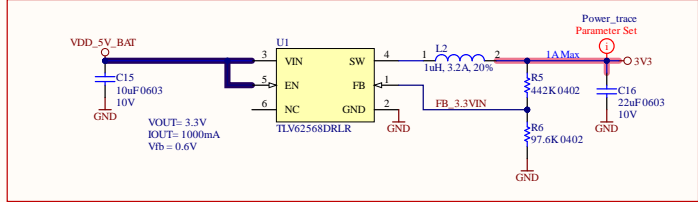


Title <b>DM1092_LiPo</b>			<i>Luxonis Holding</i> <i>1925 Harmony Park Drive</i> <i>Westminster, CO</i> <i>80234</i> <i>United States</i>	Cannot open file C:\Users\Brian.Luxonis\Documents\... P:\external\luxonon\DATA\
Size: <b>Tabloid</b>	Number: <b>DXXXX</b>	Revision: <b>R0M0E0</b>		
Date: <b>26/01/2021</b>	Time: <b>10:32:34</b>	Sheet <b>3</b> of <b>13</b>		
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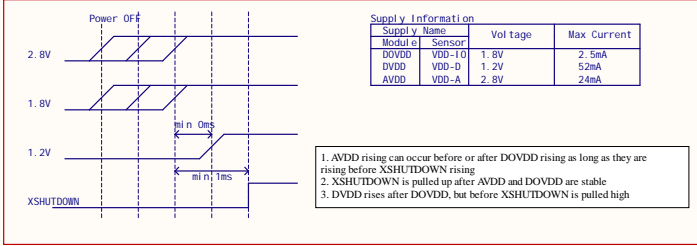
### POWER SUPPLIES FOR CAMERA MODULES



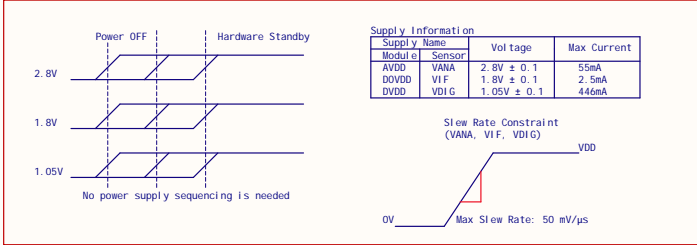
### 3.3V USB SW POWER



### OV9282 POWER REQUIREMENTS



### IMX378 POWER REQUIREMENTS



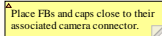
#### POWER SEQUENCING REQUIREMENTS:

The BW1099 module handles it's own power sequencing on-board.

The camera modules have their own power sequencing requirements. The OV9282 have requirements for sequencing, and the IMX378 has a max slew rate requirement. See above.

### FAN CONTROLLER

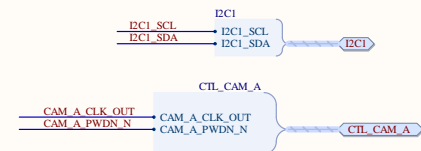
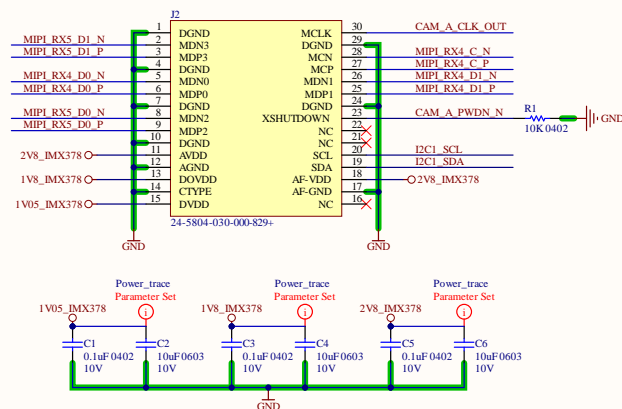




MIPI Lanes:  
DPHYv1.2  
Max 2.1 Gbps / lane



MODULE & SENSOR INFORMATION			
MODULE	A12N02A-201	12C Clock Rate	1000 kHz Max
SENSOR	1MX378-AAQH5-C	12C Address (8 bits)	0x34 (Sensor)
	12.3 Mega pixel CMOS 1/2.3 inch		0x18 (VCM driver) 0xA0 (EEPROM driver)
MAX RESOLUTION	4056x3040	Sensor Clock Input	6 - 27 MHz



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Size: <u>Tabloid</u>	Number: <u>DXXXX</u>	Revision: <u>R0M0E0</u>		
Date: <u>26/01/2021</u>	Time: <u>10:32:35</u>	Sheet <u>4</u> of <u>12</u>		
Drawn by: <u>David Malanah</u>				



Supply Name		Voltage	Max Current
Module	Sensor		
DOVDD	VDD-I/O	1.8V	2.5mA
DVDD	VDD-D	1.2V	52mA
AVDD	VDD-A	2.8V	24mA



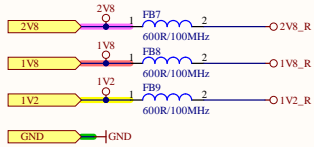
## D



D

D

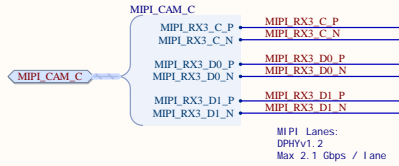
DD



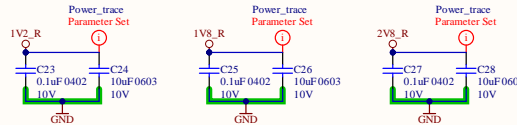
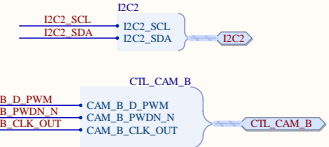
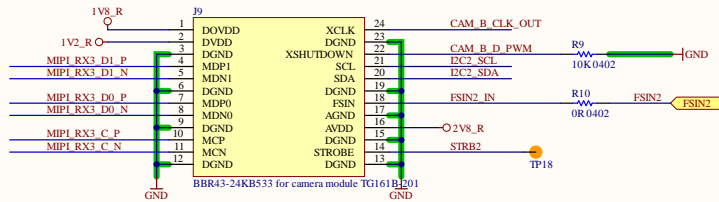
Place FBs and caps close to their associated camera connector.

MODULE & SENSOR INFORMATION			
MODULE	TG161B-201 OR AN01V32-0JG	I2C Clock Rate	400 kHz Max
SENSOR	OV9282-GA4A 8M 1 Mega pixel CMOS 1/4 inch	I2C Address (8 bits)	0xC0(W) 0xC1(R)
MAX RESOLUTION	1280X800	Sensor Clock Input	6 - 64 MHz (24 MHz typ.)

Supply Information			
Module	Sensor	Vol tage	Max Current
DOVDD	VDD-10	1.8V	2.5mA
DVDD	VDD-D	1.2V	52mA
AVDD	VDD-A	2.8V	24mA



Mark "RIGHT" on PCB  
Place so that this is the module's right camera.



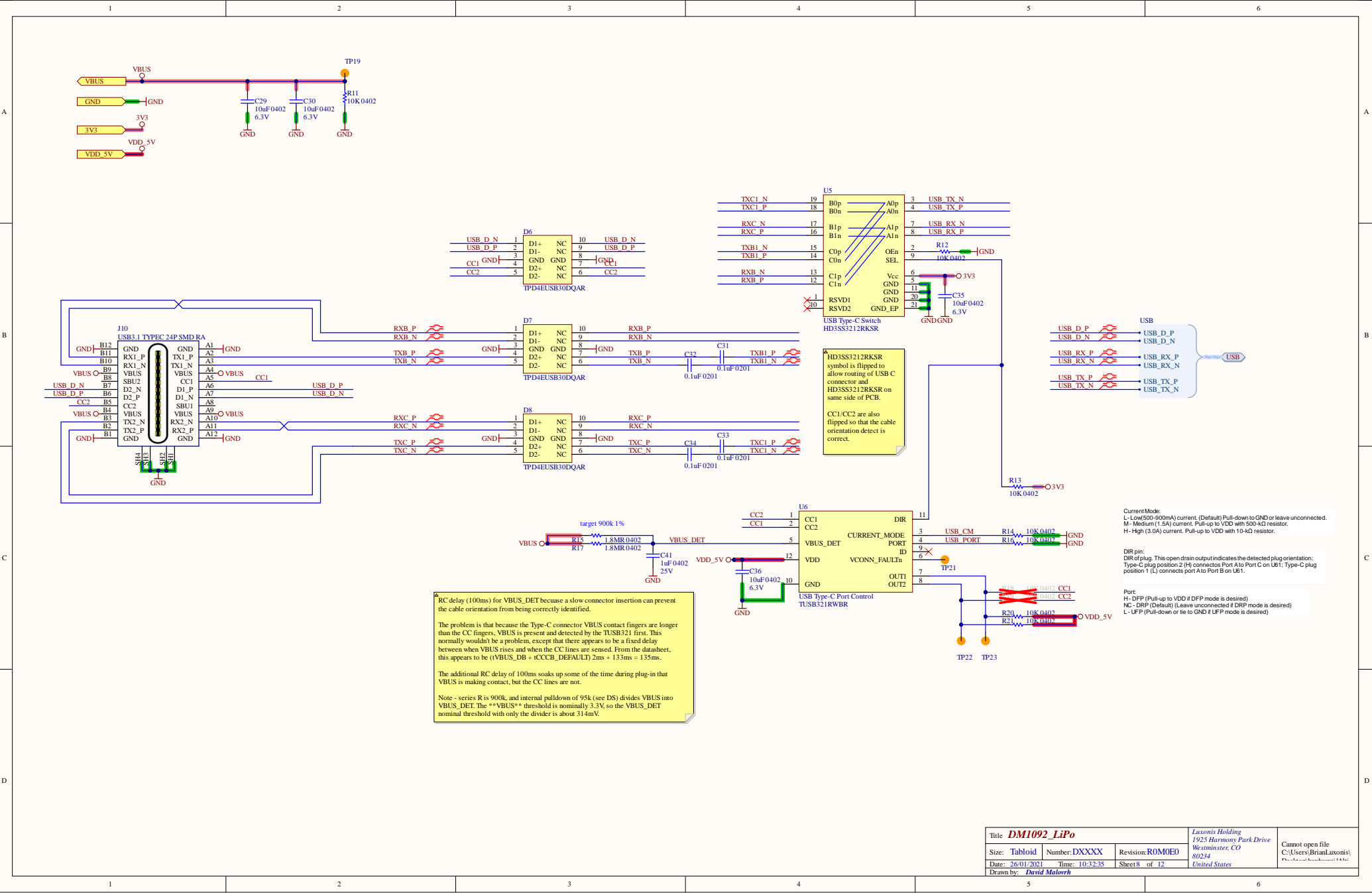
Because the stereo pair of OV9282 modules hard wired to CAM\_B (below) no additional reset circuitry is required to account for different conditions. This means that "CAM1" (Left) is reset via CAM\_PWDN, and "CAM2" (Right), is reset via CAM\_PWM. This also means that the signal CAM\_AUX\_101 is no longer required here, as that was only possible if the stereo pair were connected to CAM\_C or CAM\_D

OV9282 sensor I2C address may be changed via I2C protocol. Therefore, in order to assign different I2C address to the sensors on the same I2C bus, one needs to hold the reset the all sensors except one and assign a unique I2C address to the active sensor. This routine should be applied for all sensors in the initialization routine.

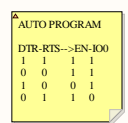
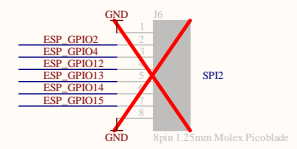
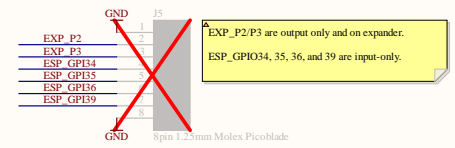
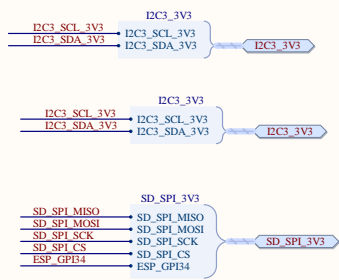
CAM NO	CAMERA CONNECTOR RESET CONNECTION TABLE			
	CAM_A	CAM_B	CAM_C	CAM_D
CAM 1	CAM_PWDN	CAM_PWDN	CAM_PWDN	CAM_PWDN
CAM 2	CAM_PWM	CAM_PWM	CAM_AUX_101	CAM_AUX_101

Title <b>DM1092_LiPo</b>			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States		Cannot open file C:\Users\Brian.Luxonis\Documents\... [Drawing: dm1092.dwg] Fatal
Size: <b>Tabloid</b>	Number: <b>DXXXXX</b>	Revision: <b>ROM0E0</b>			
Date: 26/01/2021	Time: 10:32:35	Sheet 7 of 12			
Drawn by: <b>David Malorch</b>					

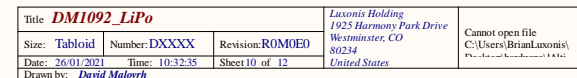


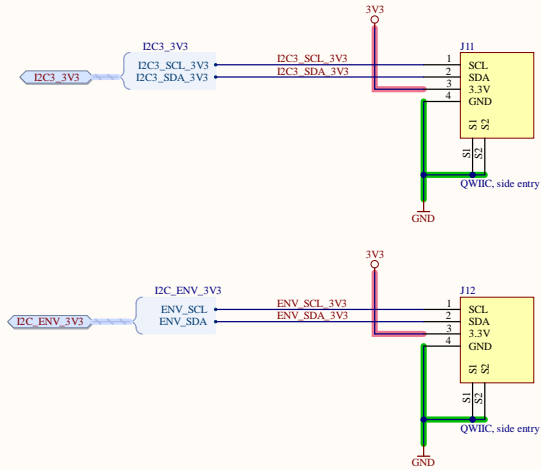
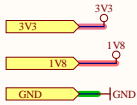


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Size: <b>Tabloid</b>	Number: <b>DXXXXX</b>	Revision: <b>ROM0E0</b>			
Date: 26/01/2021	Time: 10:32:35	Sheet 8 of 12			
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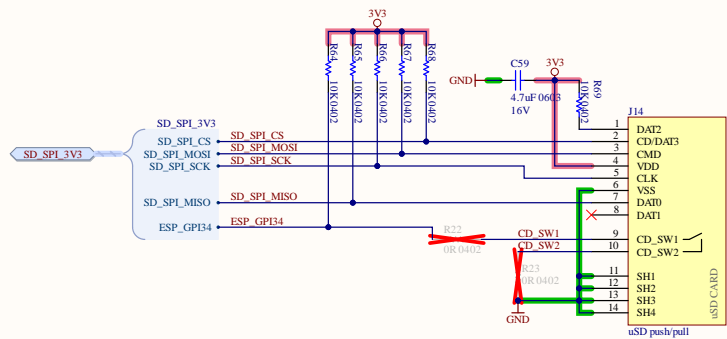
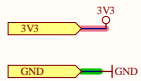
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Date: <b>26/01/2021</b>	Time: <b>10:32:35</b>	Sheet <b>9</b> of <b>12</b>			
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Title <b>DM1092_LiPo</b>			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	Cannot open file C:\Users\Brian.Luxonis\ Documents\...
Size: <b>Tabloid</b>	Number: <b>DXXXX</b>	Revision: <b>ROM0E0</b>		
Date: 26/01/2021	Time: 10:32:35	Sheet 11 of 12		
Drawn by: <b>David Malowrh</b>				

Title <i>DM1092 LiPo</i>			<i>Luxonis Holding</i>	Cannot open file C:\Users\Brian\Luxonis\ Project\dm1092\dm1092
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Date: <u>26/01/2021</u>	Time: <u>10:32:35</u>	Sheet <u>12</u> of <u>12</u>	<i>Westminster, CO</i>	
Drawn by: <i>David Malarch</i>			<i>80234</i>	
			<i>United States</i>	



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Size: <b>Tabloid</b>	Number: <b>DXXXX</b>	Revision: <b>ROM0E0</b>			
Date: 26/01/2021	Time: 10:32:35	Sheet of	<b>United States</b>		
Drawn by: <b>David Malorh</b>					