

Experiment - 2

Applications of Boolean Algebra.

Objectives:

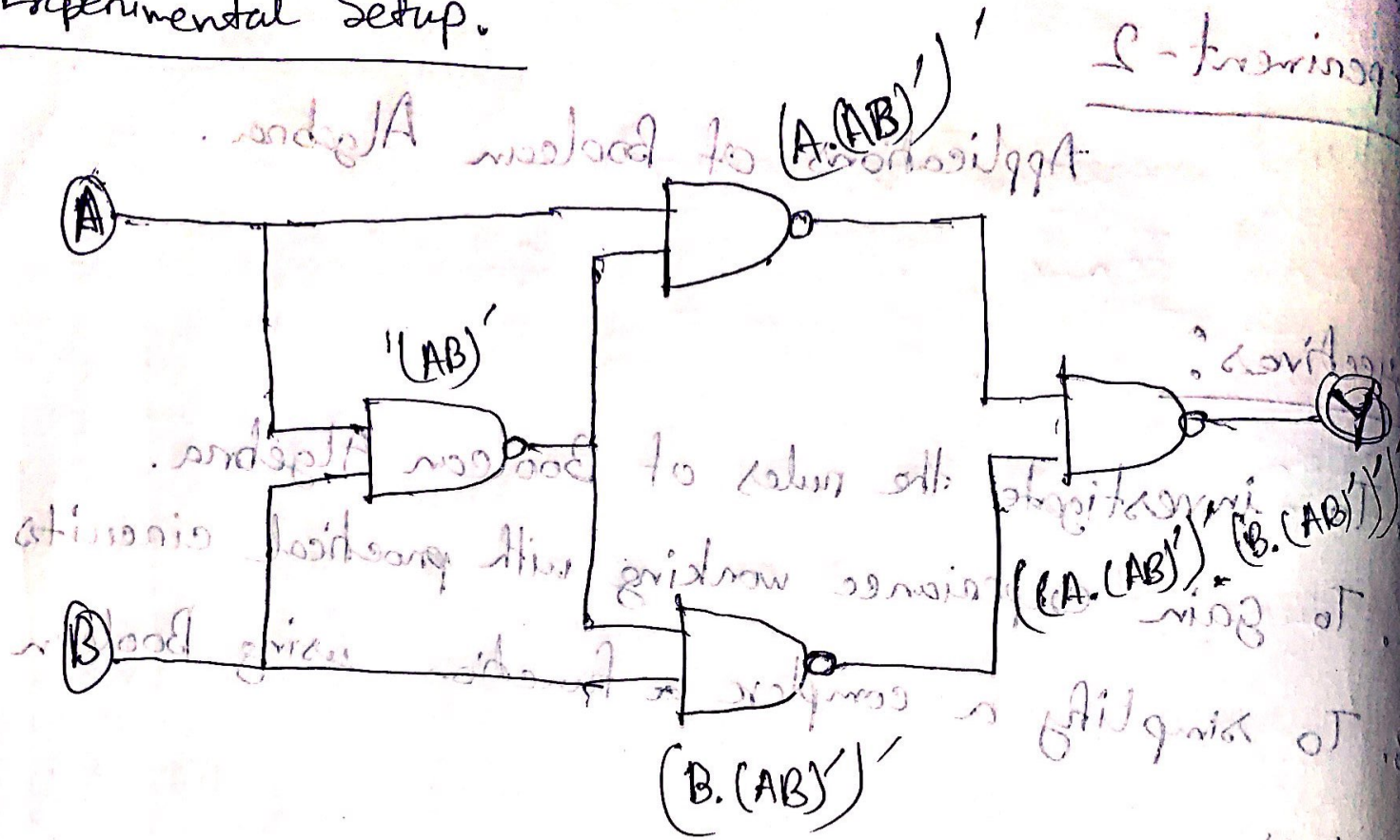
1. To investigate the rules of Boolean Algebra.
2. To gain experience working with practical circuits
3. To simplify a complex function using Boolean algebra

Required components:

- NAND gate
- LOGIC STATE
- LOGIC PROBE
- LED - BLUE

X	Y	A
0	0	0
1	1	0
1	0	1
0	1	1

Experimental Setup:



Truth Table:

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0

~~AND~~

Result:

The output Boolean equation is:

$$\begin{aligned} Y &= ((A \cdot (AB)')')' \cdot (B \cdot (AB)')')' \\ &= ((A \cdot (AB)')')' + ((B \cdot (AB)')')' \\ &= A \cdot (AB)' + B \cdot (AB)' \\ &= A \cdot (\bar{A} + \bar{B}) + B \cdot (\bar{A} + \bar{B}) \\ &= \cancel{A\bar{A}} + \cancel{A\bar{B}} + B\bar{A} + B\bar{B} \\ &= A\bar{B} + B\bar{A} \\ &= A \oplus B \end{aligned}$$

Discussion:

The output of this gate works similarly as XOR gate.