Condensed FR Doc 2

FR1 System shall displays welcome message in the UVSim Prototype 1

FR2 System shall hold in memory up to 100 instructions

FR3 System shall read first two digits of the four digits of user’s current memory address and         compares it to instruction

FR4   System shall allow a full program to be entered before running said program.

FR5   System shall distinguish between word instruction and operand (memory location)

FR6   System shall write any word from memory to the screen

FR7   System shall load a word from memory into accumulator

FR8   System shall store a word from accumulator into memory

FR9   System shall add a word from accumulator and a word in memory saving result in accumulator

FR10 System Shall subtract a word in memory from a word in accumulator

FR11 System Shall divide a word in accumulator by a word in memory

FR12 System Shall multiply a word in memory by a word in accumulator

FR13 System shall branch from operating on an instruction to an instruction in another memory location.

FR14 System shall branch to new memory location only if accumulator is equal to zero

FR15 System Shall Output Accumulator calculation and display sections of: Accumulator, Instruction Counter, Instruction Register, Operation Code, and Operand