

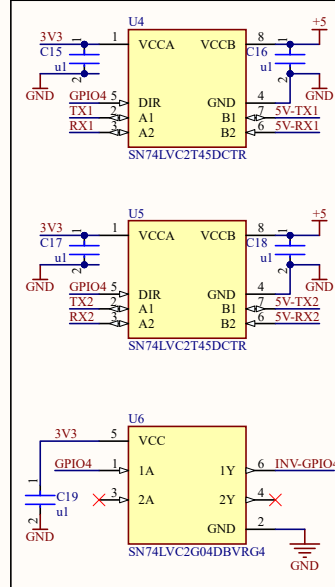
## D



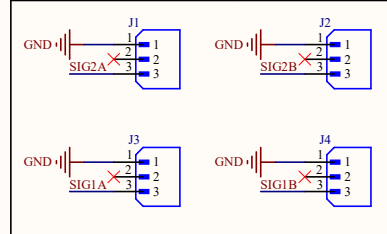
Drawn By:
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## D

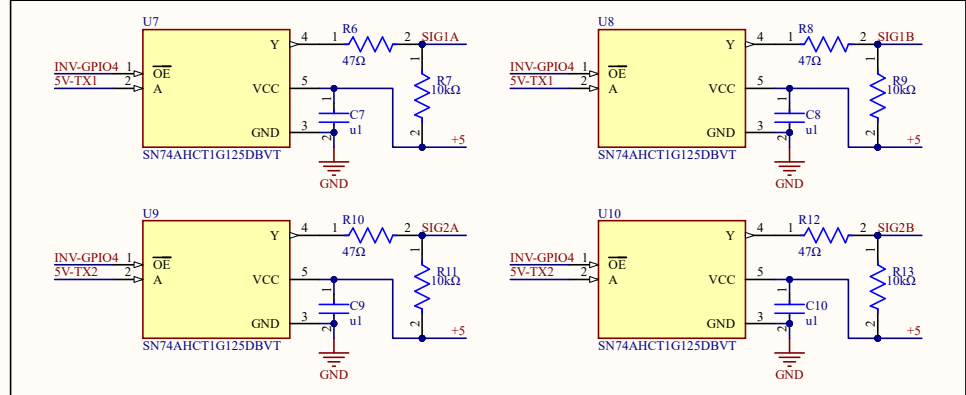
## Logic Shifter + Inverter



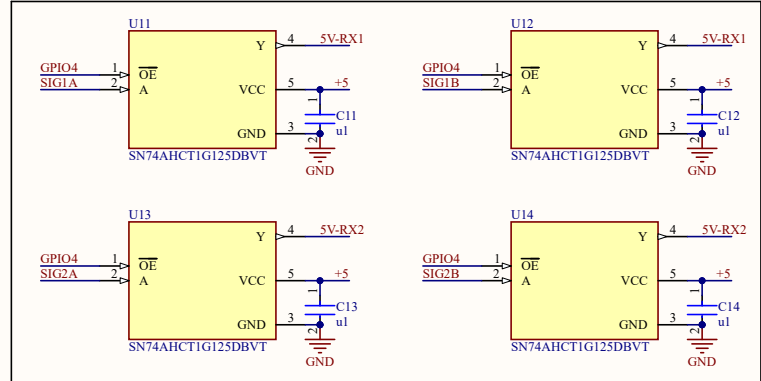
## Servo Connectors (SIG)



## TX Gate Buffers



## RX Gate Buffers



Title

Size

Number

Revision

Date:

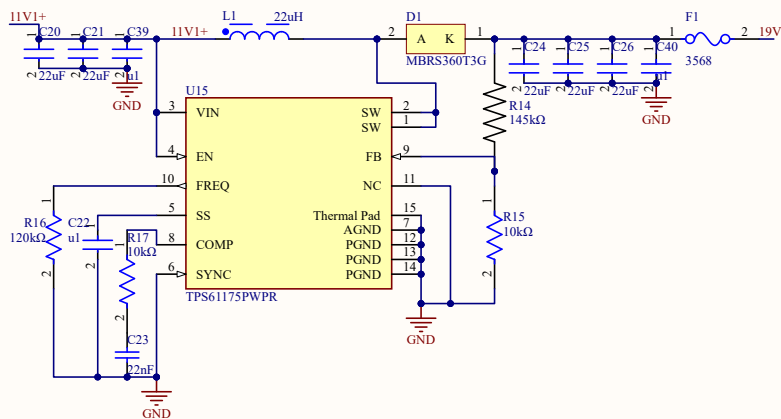
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Sheet of

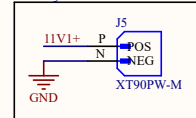
File: C:\Users\U\UART\_LOGIC.SchDoc

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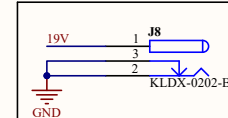
# Jetson Regulator



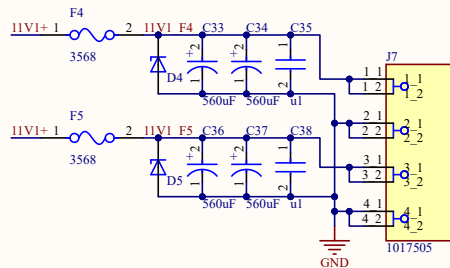
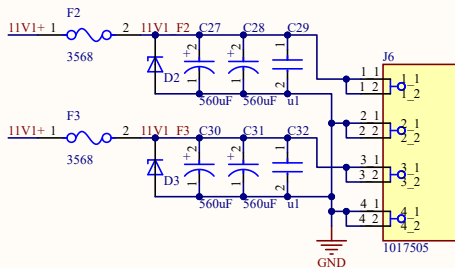
## Battery Connector



## Jetson Connector



# Servo Connectors (PWR)



Title

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Revision

Date:

10/06/2025

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File:

C:\Users\...Jetson\_power.SchDoc

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