FERCSA – 24 channel ADC and 8 channel PWM w/I2C
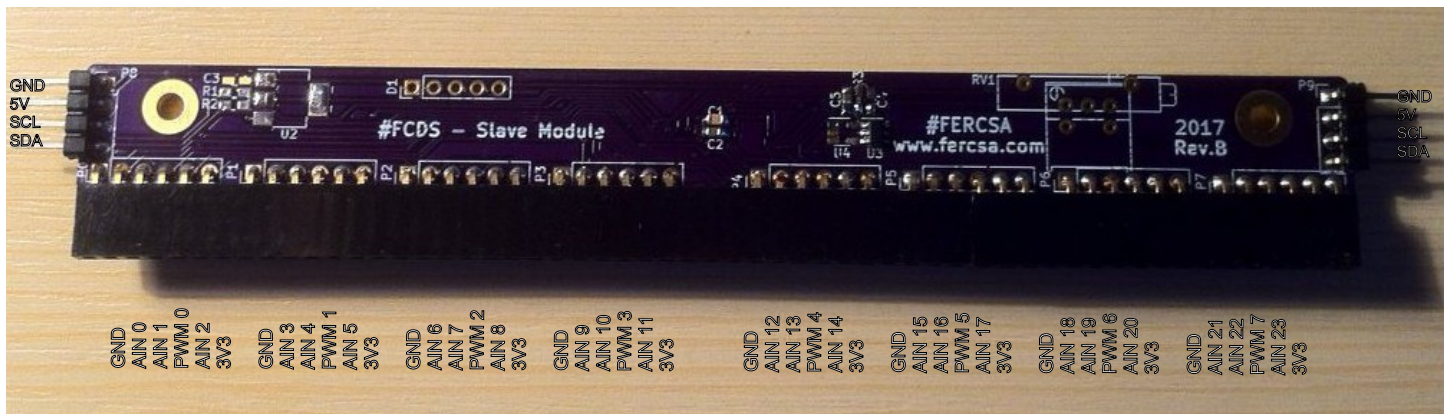
ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Typical	Units
5V	Voltage input	5	V
AIN(n)	Analog input	0-3.3	V
3V3	Voltage output	3.3	V
SCL	I2C clock line	3.3	V
SDA	I2C data line	3.3	V
-	I2C bus speed	100	Kbits/s
PWM(n)	PWM frequency	24	Mhz

ANALOG INPUT LINEARITY (based on two different channel)
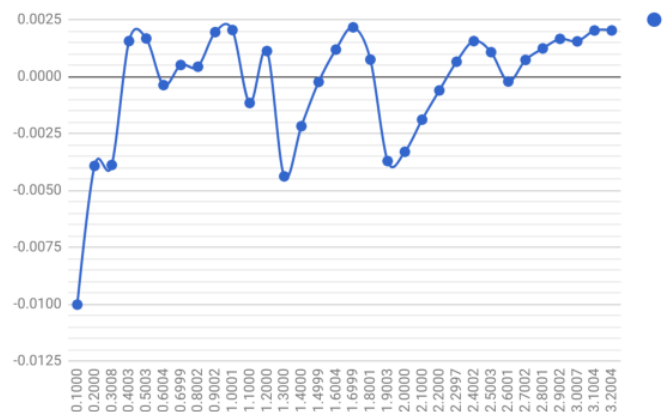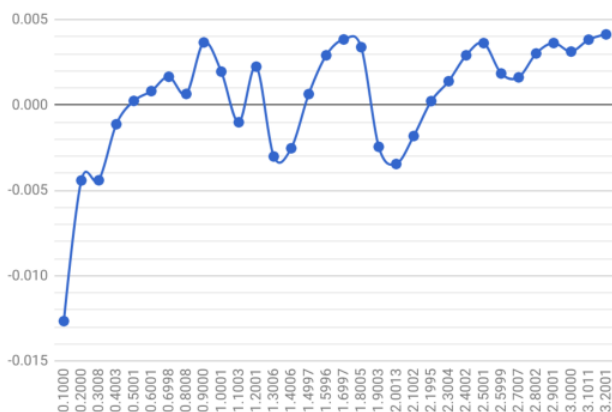
CODE EXAMPLES

- Available for Arduino and Linux C.
- Please visit https://github.com/FERCSA/FCDS/tree/master/SM_demo