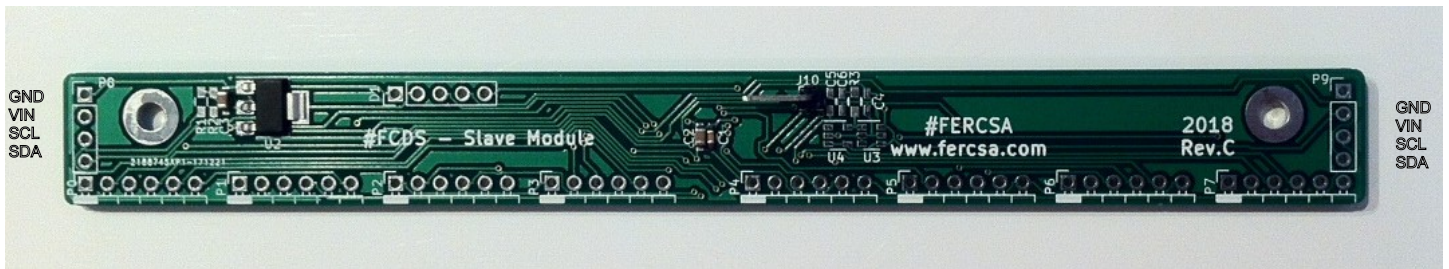


# FERCSA – 32 channel MUX w/I2C



GND I/O 0 I/O 1 I/O 2 I/O 3 VOUT GND I/O 4 I/O 5 I/O 6 I/O 7 VOUT GND I/O 8 I/O 9 I/O 10 I/O 11 VOUT GND I/O 12 I/O 13 I/O 14 I/O 15 VOUT GND I/O 16 I/O 17 I/O 18 I/O 19 VOUT GND I/O 20 I/O 21 I/O 22 I/O 23 VOUT GND I/O 24 I/O 25 I/O 26 I/O 27 VOUT GND I/O 28 I/O 29 I/O 30 I/O 31 VOUT

## ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Typical (for 3.3V variant)	Typical (for 5.0V variant)	Units
VIN	Voltage supply input	5 (max. 12)	7 (max.12)	V
I/O(n)	Analog input / output voltage	0 – 3.3	0 – 5.0	V
VOUT	Voltage supply output	3.3	5.0	V
SCL	I2C clock line	3.3	3.3 – 5.0 (based on pull-up)	V
SDA	I2C data line	3.3	3.3 – 5.0 (based on pull-up)	V
-	I2C bus speed	100	100	Kbits/s
R1, R2	I2C pull-up resistors (optional)	1.7	1.7	kOhm
J10	Common input / output	3.3	5.0	V

## CODE EXAMPLES

- Available for Arduino and Linux C.
- Please visit [https://github.com/FERCSA/FCDS/tree/master/SM\\_demo](https://github.com/FERCSA/FCDS/tree/master/SM_demo)