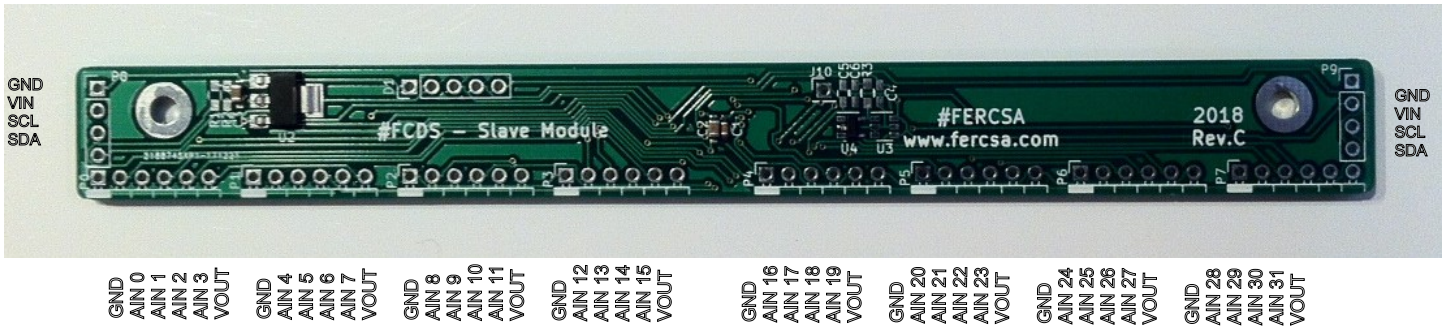
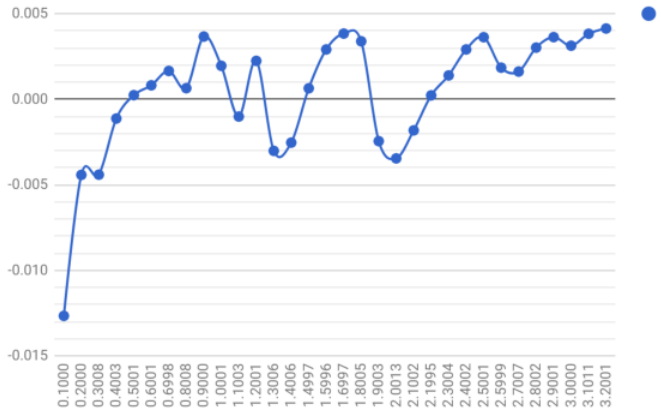
FERCSA – 32 channel ADC w/I2C

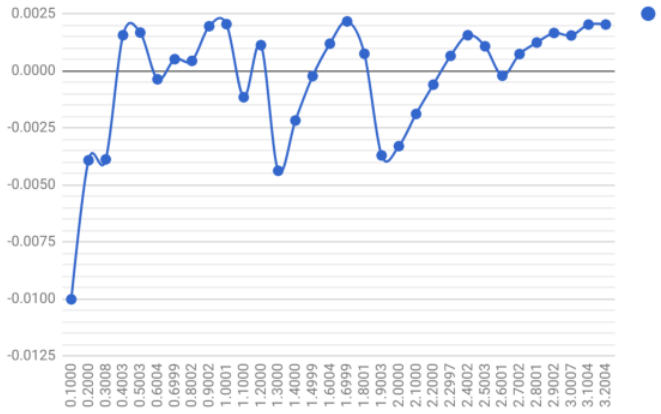


ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Typical (for 3.3V variant)	Typical (for 5.0V variant)	Units
VIN	Voltage supply input	5	7 (max.12)	V
AIN(n)	Analog input voltage	0 – 3.3	0 – 5.0	V
VOUT	Voltage supply output	3.3	5.0	V
SCL	I2C clock line	3.3	3.3 – 5.0 (based on pull-up)	V
SDA	I2C data line	3.3	3.3 – 5.0 (based on pull-up)	V
-	I2C bus speed	100	100	Kbits/s
R1, R2	I2C pull-up resistors (optional)	1.7	1.7	kOhm
J10	Vref output voltage	3.3	5.0	V
-	ADC bitrate	12 (11 effective)	12 (11 effective)	bit
-	ADC sampling rate	~8000	~8000	SPS
-	ADC input impedance	~2.2	~2.2	kOhm

ANALOG INPUT LINEARITY (based on two different channel)





CODE EXAMPLES

- Available for Arduino and Linux C.
- Please visit https://github.com/FERCSA/FCDS/tree/master/SM_demo