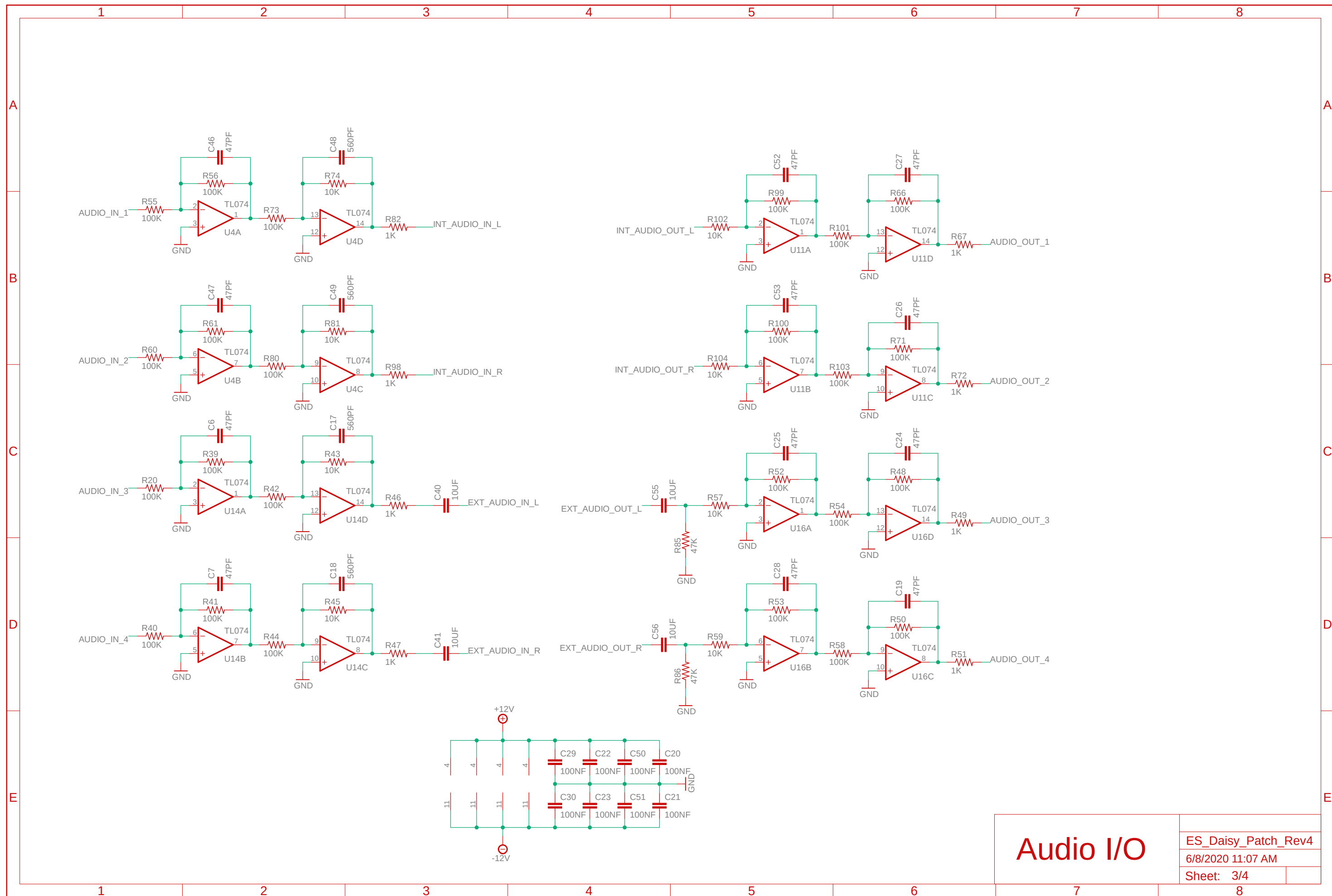


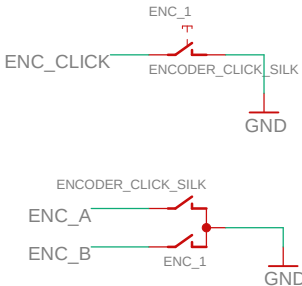
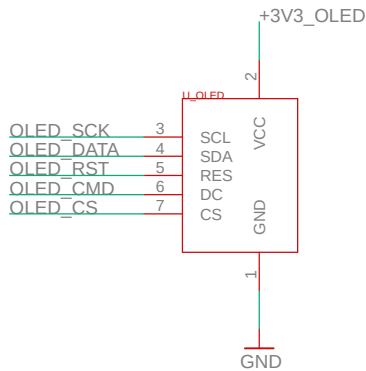
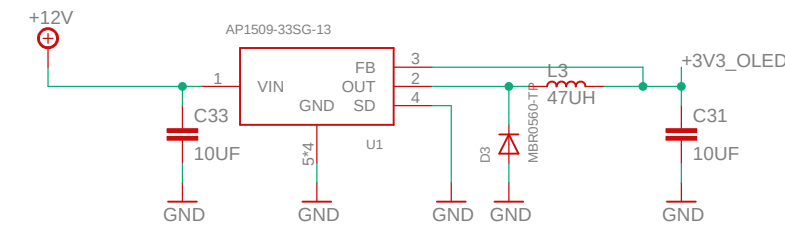
The image contains three circuit diagrams. The top diagram shows the DAC1_OUT1 circuit, which uses two TL074 op-amp buffers (U5B and U5C) to drive a 1K load (R12) connected to a TIP NORM GND terminal. The input is DAC1_OUT1 through a 33K resistor (R8). The feedback network for U5B consists of a 49K resistor (R9) and a 100K resistor (R14). The feedback network for U5C consists of a 100K resistor (R15) and a 100K resistor (R14). The output is labeled DAC1_OUT1_ J CV_OUT_1 with specifications: IN: 0 - 3.3V, OUT: 0 - 5V.

The bottom diagram shows the DAC1_OUT2 circuit, which uses two TL074 op-amp buffers (U5A and U5D) to drive a 1K load (R13) connected to a TIP NORM GND terminal. The input is DAC1_OUT2 through a 33K resistor (R10). The feedback network for U5A consists of a 49K resistor (R11) and a 100K resistor (R16). The feedback network for U5D consists of a 100K resistor (R17) and a 100K resistor (R16). The output is labeled DAC1_OUT2_ J CV_OUT_2 with specifications: IN: 0 - 3.3V, OUT: 0 - 5V.

The right diagram shows a voltage divider circuit. It consists of a +12V supply connected to a 4 ohm resistor, followed by a 100NF capacitor (C9) to ground. The output is taken from the node between the 4 ohm resistor and the 100NF capacitor, which is connected to a 11 ohm resistor, followed by another 100NF capacitor (C10) to ground. The output is connected to a -12V supply.



Switching supply for OLED



Power

