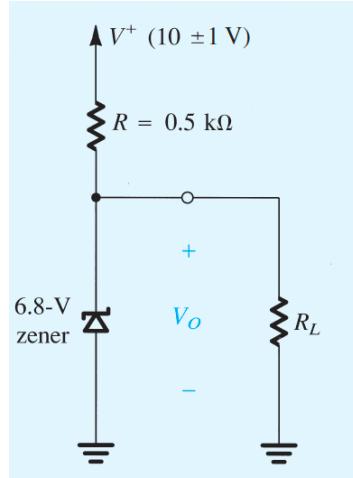
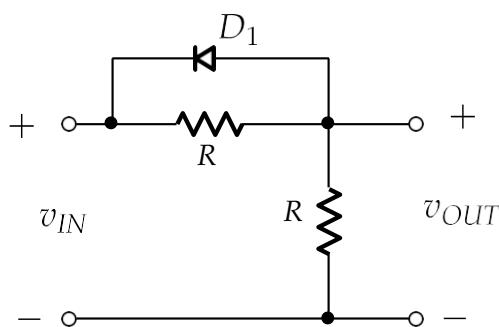


## Homework 8 Topic: Diodes II + BJTs

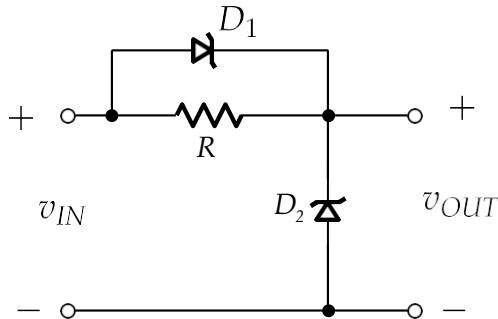
- Consider a half-wave peak rectifier fed with a voltage  $v_s$  having a triangular waveform with 24-V peak-to-peak amplitude, zero average, and 1-kHz frequency. Assume that the diode has a 0.7-V drop when conducting. Let the load resistance  $R = 100\Omega$  and the filter capacitor  $C = 100 \mu F$ . Find the average dc output voltage, the time interval during which the diode conducts, the average diode current during conduction, and the maximum diode current.
- Zener Diode Regulator: For this problem you will need the data sheet for a Zener Diode, which you can find your own or use the one Fairchild Semiconductor that is provided on Canvas. Note that the data sheet provides values for  $V_Z$ , which is measured at the current  $I_Z$ , as well as for the series resistance. We want to design a 12 V regulator using a reverse-bias Zener diode similar to the circuit below the load of  $2.2 \text{ k}\Omega$ . For this design problem, the supply voltage  $V^+$  is  $25 \pm 2.5$  V (not  $10 \pm 1$  V as shown in the figure below).
  - Choose one Zener diode to regulate the load voltage to approximately 12 V. Based on the information in the data sheet, find  $V_{Z0}$  for this diode.
  - If the Zener current is 25 mA, find  $V_Z$ . Then find the current through  $R$ , and from that determine a value for  $R$ . Use the nominal voltage for the power supply (i.e., 25 V).
  - Find the line regulation.



- For the circuit below using the constant voltage drop model with  $V_{D0} = 0.7 \text{ V}$  and  $r_D = 0$ :
  - Write an expression for  $v_{OUT}$  in terms of  $v_{IN}$ .
  - Sketch a graph of  $v_{OUT}$  vs.  $v_{IN}$ .



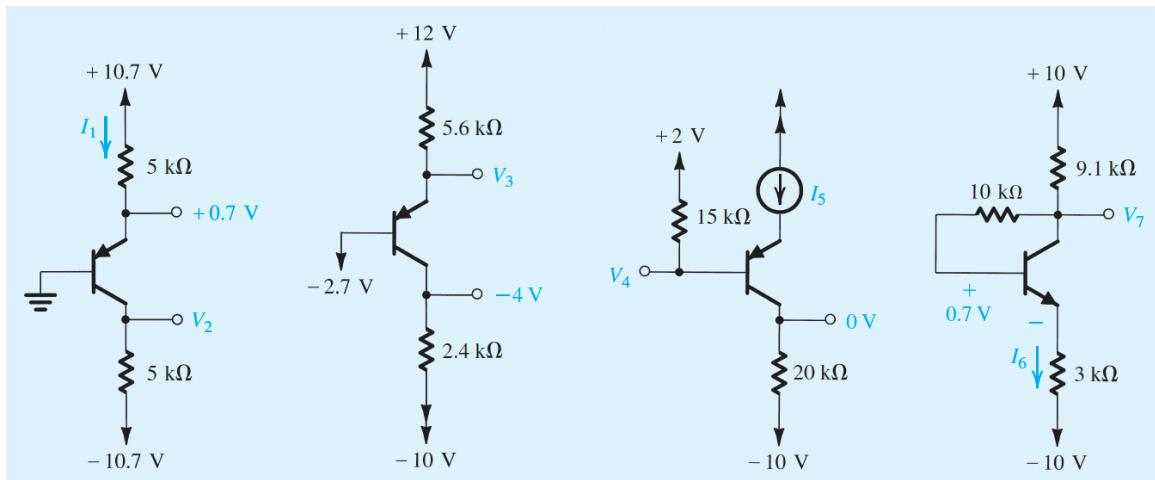
4. Repeat problem 3 with  $r_D = R$ .
5. For the circuit below using the constant voltage drop model with  $V_{D0} = 0.7\text{ V}$ ,  $V_{Z0} = 1.4\text{ V}$ ,  $r_D = r_z = 0$ :
- Write an expression for  $v_{OUT}$  in terms of  $v_{IN}$ .
  - Sketch a graph of  $v_{OUT}$  vs.  $v_{IN}$ .



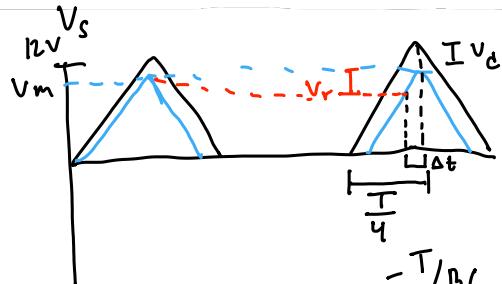
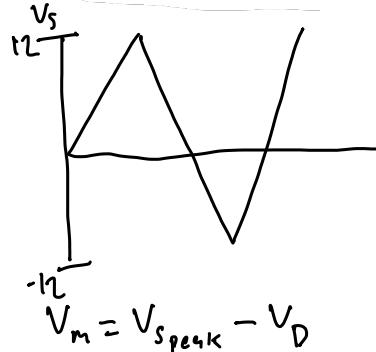
6. Measurements of  $V_{BE}$  and two terminal currents taken on a number of npn transistors operating in the active mode are tabulated below. For each, calculate the missing current value as well as  $\alpha$ ,  $\beta$ , and  $I_S$  as indicated by the table.

Transistor	a	b	c	d	e
$V_{BE}$ (mV)	700	690	580	780	820
$I_C$ (mA)	1.000	1.000		10.10	
$I_B$ ( $\mu\text{A}$ )	10		5	120	1050
$I_E$ (mA)		1.020	0.235		75.00
$\alpha$					
$\beta$					
$I_S$					

7. For the circuits below, assume that the transistors have very large  $\beta$ . Some measurements have been made on these circuits, with the results indicated in the figure. Find the values of the other labeled voltages and currents.



1. Consider a half-wave peak rectifier fed with a voltage  $v_s$  having a triangular waveform with 24-V peak-to-peak amplitude, zero average, and 1-kHz frequency. Assume that the diode has a 0.7-V drop when conducting. Let the load resistance  $R = 100\Omega$  and the filter capacitor  $C = 100 \mu F$ . Find the average dc output voltage, the time interval during which the diode conducts, the average diode current during conduction, and the maximum diode current.



$$V_m = 12 - (0.7)$$

$$V_m = 11.3 \text{ [V]}$$

$$V_{dc,avg} \approx V_m - \frac{V_m}{2fRL}$$

$$V_{dc,avg} \approx 11.3 - \frac{11.3}{2(1K)(100)(100\mu)}$$

$$V_{dc,avg} = 10.735 \text{ [V]}$$

$$V_m - V_r = V_m e^{-T/RC}$$

$$V_m - V_m e^{-T/RC} = V_r$$

$$V_m \frac{T}{RC} \approx V_r$$

$$\frac{V_m}{fRC} \approx V_r$$

$$\frac{11.3}{1K(100)(100\mu)} \approx V_r$$

$$V_r = 1.13$$

$$RL = .01 \Rightarrow T = .001$$

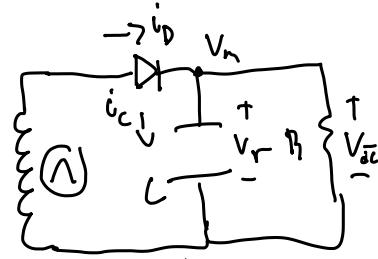
$$e^{-T/RC} \approx 1 - \frac{T}{RC}$$

$$\frac{V_{S_{peak}}}{T} = \frac{V_r}{\Delta t}$$

$$4fV_{S_p} = \frac{V_r}{\Delta t}$$

$$\Delta t = \frac{1.13}{4(1K)(12)}$$

$\Delta t = 23.5 \mu s$   
interval diode conducts



$$i_L = \frac{V_{dc}}{R}$$

$$i_C = C \frac{dv_o}{dt} = i_L \Delta t = CV_r$$

$$i_{D_{avg}} = i_{C_{avg}} + i_L$$

$$i_{D_{avg}} = C \frac{dv_o}{dt} + \frac{V_{dc}}{R}$$

$$i_{D_{avg}} = \frac{CV_r}{\Delta t} + \frac{V_{dc}}{R}$$

$$i_{D_{avg}} = \frac{100\mu(1.13)}{23.5\mu} + \frac{10.735}{100}$$

$$i_{D_{avg}} = 11.92 \text{ [A]}$$

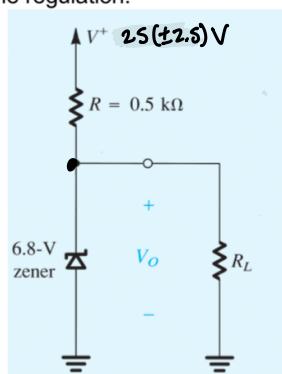
average diode current in FB

$$i_{D_{max}} = 11.92 \text{ [A]}$$

since  $i_{C_{max}} \approx i_{C_{avg}}$  in  $\Delta t$

2. Zener Diode Regulator: For this problem you will need the data sheet for a Zener Diode, which you can find your own or use the one Fairchild Semiconductor that is provided on Canvas. Note that the data sheet provides values for  $V_z$ , which is measured at the current  $I_z$ , as well as for the series resistance. We want to design a 12 V regulator using a reverse-bias Zener diode similar to the circuit below the load of 2.2 kΩ. For this design problem, the supply voltage  $V^+$  is  $25 \pm 2.5$  V (not  $10 \pm 1$  V as shown in the figure below).

- Choose one Zener diode to regulate the load voltage to approximately 12 V. Based on the information in the data sheet, find  $V_{z0}$  for this diode.
- If the Zener current is 25 mA, find  $V_z$ . Then find the current through  $R$ , and from that determine a value for  $R$ . Use the nominal voltage for the power supply (i.e., 25 V).
- Find the line regulation.



$$V_z(25mA) = V_{z0} + Z_2 I_z$$

$$= 11.9 + (20)(.025)$$

$$V_z(25mA) = 12.41 \text{ [V]}$$

$$V_{z0} = V_z - I_z \cdot Z_2$$

$$V_{z0} = 12 - (.005)(20)$$

$$V_{z0} = 11.9 \text{ [V]}$$

$$i_L = \frac{V_z}{R_L} = \frac{12.4}{2.2K}$$

$$i_L = 5.64 \text{ [mA]}$$

$$i_R = i_L + i_L$$

$$i_R = 25 + 5.64$$

$$i_R = 30.64 \text{ [mA]}$$

Fairchild BZX55C2V4

$$V_z(\text{nominal}) = 12 \text{ [V]}$$

$$I_z = 5 \text{ [mA]}$$

$$Z_2 = 20 \text{ [\Omega]} = r_z$$

line regulation:

$$\Delta V_o = \Delta V^+ \left( \frac{r_z || R_L}{R_L + (r_z || R_L)} \right)$$

$$\frac{\Delta V_o}{\Delta V^+} = \frac{20 || 2.2K}{4 || + (20 || 2.2K)}$$

$$\frac{\Delta V_o}{\Delta V^+} = 0.046$$

$$\Delta V_o = \text{line reg.} (\Delta V^+)$$

$$V^+ - V_z = i_R \cdot R \quad \Delta V_o = (.046)(2.5)$$

$$R = \frac{V^+ - V_z}{i_R}$$

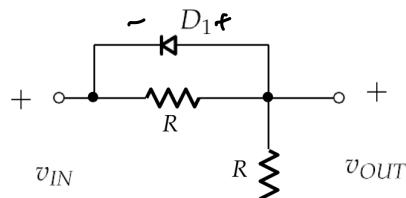
$$R = \frac{25 - 12.4}{30.64}$$

$$R = 411.3 \approx 411 \text{ [\Omega]}$$

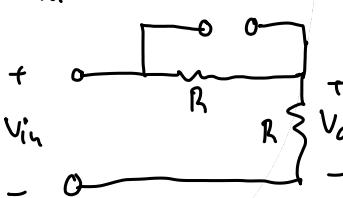
$$\Delta V_o = 0.115 \text{ [V]}$$

3. For the circuit below using the constant voltage drop model with  $V_{D0} = 0.7$  V and  $r_D = 0$ :

- Write an expression for  $v_{OUT}$  in terms of  $v_{IN}$ .
- Sketch a graph of  $v_{OUT}$  vs.  $v_{IN}$ .



$$v_{IN} - 0.7 \text{ mV}$$



$$V_o = v_{IN} \left( \frac{R}{R+R} \right)$$

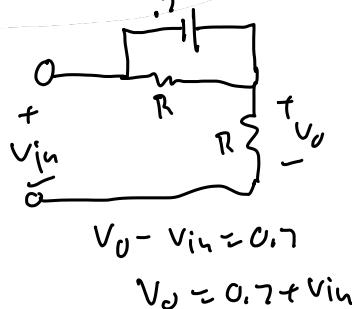
$$V_o = \frac{v_{IN}}{2}$$

$D_1$  conducts only for  $V_{out} \geq v_{in} + 0.7$  [V]

$$-0.7 \geq v_{IN} - \frac{V_o}{2}$$

$$-0.7 \geq \frac{1}{2} v_{IN}$$

$$-1.4 \geq v_{IN}$$

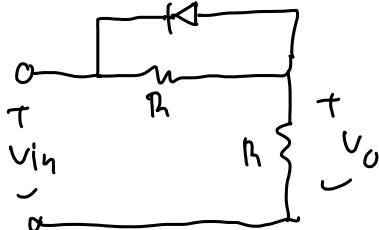


$$V_o - v_{IN} = 0.7$$

$$V_o = 0.7 + v_{IN}$$

$$V_o = \begin{cases} \frac{1}{2} v_{IN}, & v_{IN} \geq -1.4 \\ 0.7 + v_{IN}, & v_{IN} \leq -1.4 \end{cases}$$

4. Repeat problem 3 with  $r_D = R$ .



$$v_{IN} \text{ small} \Rightarrow V_o = \frac{1}{2} v_{IN}$$

$$V_o = \begin{cases} \frac{1}{2} v_{IN}, & v_{IN} \geq -1.4 \\ \frac{7}{30} + \frac{2}{3} v_{IN}, & v_{IN} \leq -1.4 \end{cases}$$

conducts for  $V_{out} \geq v_{in} + 0.7$

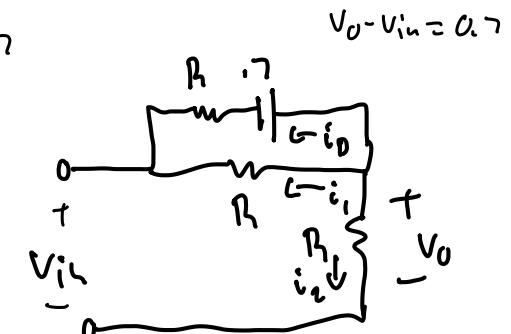
$$i_D + i_1 + i_2 = 0$$

$$\frac{V_o + 0.7 - v_{IN}}{R} + \frac{V_o - v_{IN}}{R} + \frac{V_o}{R} = 0$$

$$3V_o - 2v_{IN} - 0.7 = 0$$

$$3V_o = 0.7 + 2v_{IN}$$

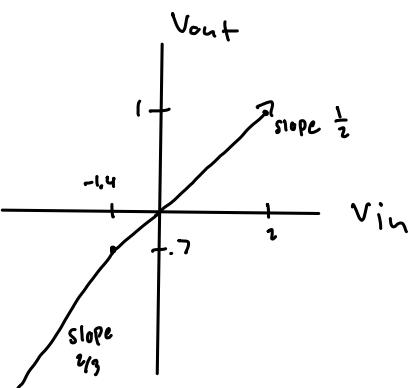
$$V_o = \frac{7}{30} + \frac{2}{3} v_{IN}$$



$$\frac{7}{30} + \frac{2}{3} v_{IN} \geq v_{IN} + 0.7$$

$$-\frac{7}{15} \geq \frac{1}{3} v_{IN}$$

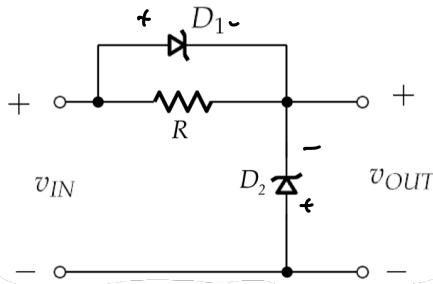
$$-1.4 \geq v_{IN}$$



5. For the circuit below using the constant voltage drop model with  $V_{D0} = 0.7 \text{ V}$ ,  $V_{Z0} = 1.4 \text{ V}$ ,

$$r_D = r_Z = 0:$$

- a. Write an expression for  $v_{OUT}$  in terms of  $v_{IN}$ .
- b. Sketch a graph of  $v_{OUT}$  vs.  $v_{IN}$ .



$$V_{out} \leq -0.7, D_2 \rightarrow \text{FB} \therefore V_{out} = -0.7 \text{ [V]}$$

$$V_{out} \geq 1.4, D_2 \rightarrow \text{rev. breakdown} \therefore V_{out} = 1.4 \text{ [V]}$$

need  $V_{out}$  for  $-0.7 \leq v_{in} \leq 1.4$

$v_{in}$  small: cant overcome  $V_{D0} = 0.7 \therefore D_1 \rightarrow \text{open}$

$v_{in} = 0.7$ , cant overcome

$V_{Z0} = 1.4$  for  $D_2 \rightarrow \text{open}$

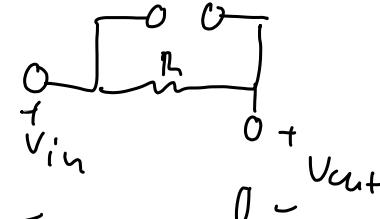
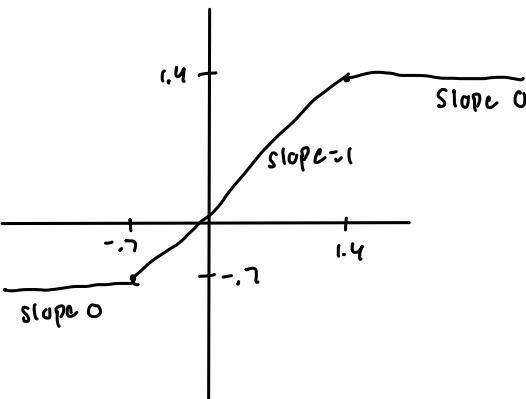
$$\therefore V_{out} = v_{in}$$

$$1.4 \text{ [V]} \quad v_{in} \geq 1.4$$

$$v_{in}$$

$$-0.7 \leq v_{in} \leq 1.4$$

$$v_{in} \leq -0.7$$



6. Measurements of  $V_{BE}$  and two terminal currents taken on a number of npn transistors operating in the active mode are tabulated below. For each, calculate the missing current value as well as  $\alpha$ ,  $\beta$ , and  $I_S$  as indicated by the table.

Transistor	a	b	c	d	e
$V_{BE}$ (mV)	700	690	580	780	820
$I_C$ (mA)	1.000	1.000	0.23	10.10	73.45
$I_B$ ( $\mu$ A)	10	20	5	120	1050
$I_E$ (mA)	1.01	1.020	0.235	10.22	75.00
$\alpha$	0.99	0.48	0.979	0.988	0.986
$\beta$	100	50	46	84	70.43
$I_S$	$6.91 \cdot 10^{-16}$	$1.03 \cdot 10^{-15}$	$1.93 \cdot 10^{-14}$	$2.85 \cdot 10^{-16}$	$4.21 \cdot 10^{-16}$

$$I_E = I_B + I_C \quad \alpha = \frac{I_C}{I_E}$$

$$\beta = \frac{\alpha}{1-\alpha}$$

$$I_C = I_S e^{\frac{V_{BE}}{V_T}}$$

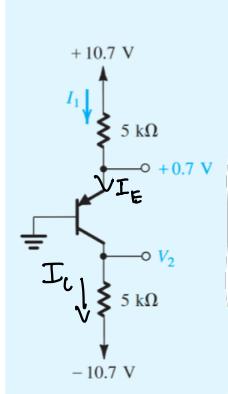
$$V_T \approx 25 \text{ mV}$$

$$I_m = I_S e^{\frac{700 \text{ mV}}{25 \text{ mV}}}$$

$$I_m e^{-28} = I_S$$

$$I_S = 6.91 \cdot 10^{-16}$$

7. For the circuits below, assume that the transistors have very large  $\beta$ . Some measurements have been made on these circuits, with the results indicated in the figure. Find the values of the other labeled voltages and currents.



$$I_1 = \frac{10.7 - 0.7}{5k}$$

$$I_1 = 2 \text{ mA} = I_E$$

$$V_2 - (-10.7) = I_c \cdot 5k$$

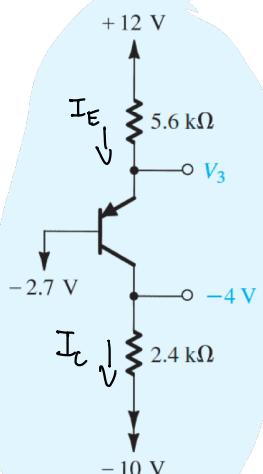
$$V_2 = I_c(5k) - 10.7$$

$$V_2 = -0.7 \text{ V}$$

$$I_c = \beta I_B \Rightarrow I_B = \frac{I_c}{\beta}$$

$$I_E = I_c + I_B \Rightarrow I_{C^+} = \frac{I_c}{\beta}$$

$$I_E = I_c(1 + \frac{1}{\beta}) \quad \beta \rightarrow \infty \Rightarrow I_E \approx I_c \therefore I_c \approx 2 \text{ mA}$$



$$I_c \approx I_E \text{ since } \beta \rightarrow \infty$$

$$I_E \approx I_c = \frac{-4 - (-10)}{2.4k} = 2.5 \text{ mA}$$

$$\frac{12 - V_3}{5.6k} = I_E$$

$$12 - I_E(5.6k) = V_3$$

$$V_3 = -2 \text{ V}$$

$$I_c \approx I_E, I_B \approx 0$$

$$I_c + I_B = I_E$$

$$I_c = I_E = I_Q$$

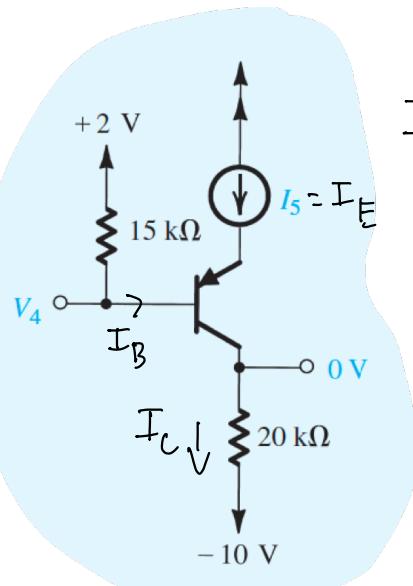
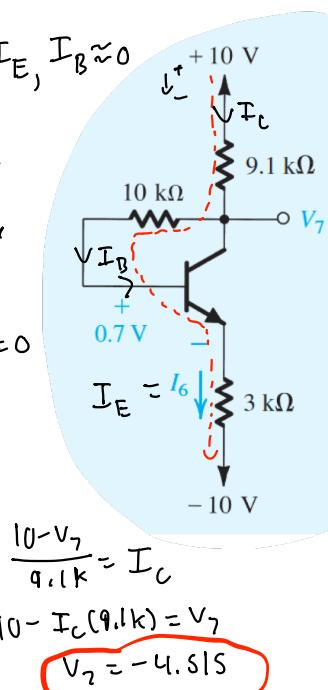
KVL:

$$10 + I_Q 9.1k + I_B 10k + .7 + I_Q 3k - 10 = 0$$

$$I_Q (9.1k + 3k) = 20 - .7$$

$$I_Q = \frac{19.3}{12.1k}$$

$$I_Q = 1.598 \text{ mA}$$



$$I_E \approx I_c = \frac{0 - (-10)}{20k} = 0.5 \text{ mA} = I_S$$

$$I_B = \frac{I_C}{\beta}, \quad \beta \rightarrow \infty \Rightarrow I_B = 0$$

$$\frac{2 - V_4}{15k} = 0$$

$$V_4 = 2 \text{ V}$$