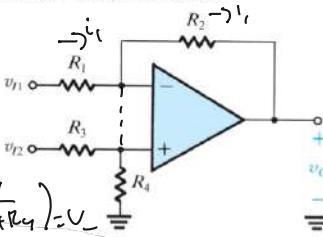


1. Find the voltage gain  $v_o/v_{id}$  for the difference amplifier below for the case  $R_1 = R_3 = 5 \text{ k}\Omega$  and  $R_2 = R_4 = 100 \text{ k}\Omega$ . What is the differential input resistance  $R_{id}$ ? If the two key resistance ratios ( $R_2/R_1$ ) and ( $R_4/R_3$ ) are different from each other by 1%, what do you expect the common-mode gain  $A_{cm}$  to be? Also, find the CMRR in this case. Neglect the effect of the ratio mismatch on the value of  $A_d$ .



$$V_+ = V_{I2} \left( \frac{R_4}{R_3 + R_4} \right) - V_-$$

$$\frac{V_+ - V_-}{R_1} = \frac{V_- - V_o}{R_2}$$

$$\frac{V_o}{R_2} = V_- \left( \frac{1}{R_1} + \frac{1}{R_2} \right) - V_{I1} \left( \frac{1}{R_1} \right)$$

$$\frac{V_o}{R_2} = V_{I2} \left( \frac{1}{R_1} + \frac{1}{R_2} \right) - V_{I1} \left( \frac{1}{R_1} \right)$$

$$V_o = V_{I2} \left( \frac{R_2}{R_1} + 1 \right) \left( \frac{R_4}{R_3 + R_4} \right) - V_{I1} \left( \frac{R_2}{R_1} \right)$$

$$V_o = V_{I2} \left( \frac{R_4}{R_3 + R_4} \right) \left( \frac{R_2 R_4}{R_1 (R_3 + R_4)} \right) + V_{I1} \left( -\frac{R_2}{R_1} \right)$$

$$V_o = V_{I2} \left( \frac{R_4}{R_3 + R_4} \right) \left( 1 - \frac{R_2 R_3}{R_1 R_4} \right) + V_{I1} \left( -\frac{R_2}{R_1} \right), \quad \frac{R_2 R_3}{R_1 R_4} = 1$$

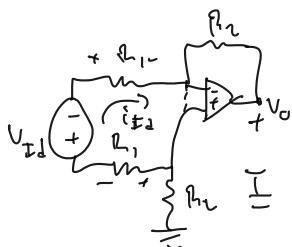
$$V_o = -\frac{R_2}{R_1} V_{I1}$$

$$\frac{V_o}{V_{I1}} = -\frac{R_2}{R_1}$$

$$V_o = V_I \frac{R_2}{R_1}, \quad V_{Id} = V_{I2} - V_{I1}$$

$$V_{Id} = -V_{I1}$$

$$\boxed{\frac{V_o}{-V_{I1}} = -\frac{R_2}{R_1} = \frac{100k}{5k} = 20 = A_d}$$



$$V_{Id} - i_{Id} R_1 - i_{Id} R_3 = 0$$

$$V_{Id} = 2 i_{Id} R_1$$

$$\frac{V_{Id}}{i_{Id}} = 2 R_1 = R_{Id} \therefore 2(SK) = R_{Id}$$

$$\boxed{R_{Id} = 10k\Omega}$$

$$\frac{V_o}{V_{Id}} = A_{I1} = -\frac{R_2}{R_1} \quad \frac{V_o}{V_{cm}} = A_{I1} + A_{I2}$$

$$\frac{V_o}{V_{Id}} = A_{I2} = \left( 1 + \frac{R_2}{R_1} \right) \frac{R_4}{R_4 + R_3}$$

$$A_{cm} = A_{I1} + A_{I2} \quad (\text{superposition})$$

$$= -\frac{R_2}{R_1} + \frac{R_4}{R_4 + R_3} + \frac{R_2 R_4}{R_1 (R_4 + R_3)}$$

$$= \frac{R_2}{R_1} \left( -1 + \frac{R_4}{R_4 + R_3} \right) + \frac{R_4}{R_4 + R_3}$$

$$A_{cm} = \frac{R_2}{R_1} \left( \frac{-R_4 - R_3 + R_4}{R_4 + R_3} \right) + \frac{R_4}{R_4 + R_3}$$

$$= -\frac{R_2}{R_1} \frac{R_3}{R_4 + R_3} + \frac{R_4}{R_4 + R_3}$$

$$= \frac{R_4 - \frac{R_2 R_3}{R_1}}{R_4 + R_3}$$

$$= \frac{R_4 \left( 1 - \frac{1}{1.01} \right)}{R_4 + R_3}$$

$$A_{cm} = \frac{R_4}{R_4 + R_3} \left( 1 - \frac{R_2 R_3}{R_1 R_4} \right)$$

$$= \frac{1.01 \frac{R_2 R_3}{R_1}}{1.01 \frac{R_2 R_3}{R_1} + R_3} \left( 1 - \frac{R_2 R_3}{R_1} \cdot \frac{1}{1.01 \frac{R_2 R_3}{R_1}} \right)$$

$$R_3 \left( 1.01 \frac{R_2}{R_1} + 1 \right)$$

$$A_{cm} = \frac{1.01 \frac{R_2}{R_1}}{1 + 1.01 \frac{R_2}{R_1}} \left( 1 - \frac{1}{1.01} \right), \quad \frac{R_2}{R_1} = A_d = 20$$

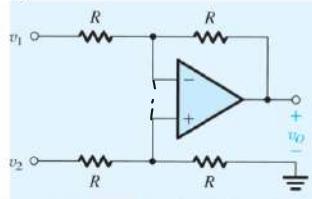
$$= \frac{1.01 (20)}{1 + 1.01 (20)} \left( 1 - \frac{1}{1.01} \right)$$

$$\boxed{A_{cm} = 0.0094}$$

$$\text{CMRR} = 20 \log \left( \frac{A_d}{A_{cm}} \right)$$

$$\boxed{\text{CMRR} = 66.53 \text{ dB}}$$

2. For the circuit shown below, express  $v_o$  as a function of  $v_1$  and  $v_2$ . What is the input resistance seen by  $v_1$  alone? By  $v_2$  alone? By a source connected between the two input terminals? By a source connected to both input terminals simultaneously?



$$V_f = V_2 \left( \frac{R}{R+R} \right) = \frac{V_2}{2} = V_- \quad \text{KCL at } V_-: \frac{V_1 - V_-}{R} = \frac{V_- - V_o}{R}$$

$$\frac{V_1}{R} - \frac{2V_-}{R} = -\frac{V_o}{R}$$

$$2V_- + V_1 = V_o$$

$$2\left(\frac{V_2}{2}\right) + V_1 = V_o$$

$$\boxed{V_o = V_1 + V_2}$$

$$\begin{aligned} \text{B}_{\text{in1}}: \frac{V_1}{i_{\text{in1}}} &= \frac{V_1}{R} \\ i_{\text{in1}} &= \frac{V_1 - V_-}{R} = \frac{V_1}{R} \\ \text{B}_{\text{in2}}: \frac{V_2}{i_{\text{in2}}} &= \frac{V_2}{R} \\ i_{\text{in2}} &= \frac{V_2 - V_-}{R} = \frac{V_2}{R} \end{aligned}$$

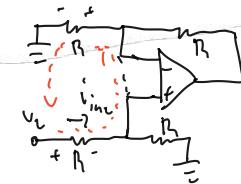
$$\boxed{\text{B}_{\text{in}} = \frac{V_1}{V_1 + V_2} = R}$$

$$\begin{aligned} \text{Circuit diagram: } &\text{A voltage-controlled voltage source } V_T \text{ is connected between } V_+ \text{ and } V_- \text{ through resistor } R. \\ &\text{The current } i \text{ flows through } R \text{ and the op-amp's feedback loop.} \\ &\text{At steady state: } V_+ = V_-, i = 0, V_o = 0. \\ &\text{KVL: } -V_T + iR + R_i i = 0 \quad \Rightarrow \quad V_T = 2iR \\ &\text{Input resistance: } R_T = \frac{V_T}{i} = 2R \end{aligned}$$

$$\text{KVL: } -V_T + iR + R_i i = 0$$

$$V_T = 2iR$$

$$\boxed{\frac{V_T}{i} = 2R = R_T}$$



$$\text{B}_{\text{in2}} = \frac{V_2}{i_{\text{in2}}} \quad \text{KVL at } V_-: -V_2 + 2i_{\text{in2}}R = 0$$

$$V_2 = 2i_{\text{in2}}R$$

$$\boxed{\frac{V_2}{i_{\text{in2}}} = 2R = \text{B}_{\text{in2}}}$$

$$\begin{aligned} \text{Circuit diagram: } &\text{A capacitor } C_C \text{ is connected between } V_+ \text{ and } V_- \text{ through resistor } R. \\ &\text{The current } i_1 \text{ flows through } R \text{ and the op-amp's feedback loop.} \\ &\text{The current } i_2 \text{ flows through } R \text{ and the inverting terminal.} \\ &\text{At steady state: } V_+ = V_-, i_1 = 0, V_o = 0. \\ &\text{KVL: } -V_T + iR + R_i i = 0 \quad \Rightarrow \quad V_T = 2iR \\ &\text{Input resistance: } R_T = \frac{V_T}{i} = 2R \end{aligned}$$

$$i_1 = \frac{V_C - V_-}{R} = \frac{V_C - \frac{V_C}{2}}{R} = \frac{V_C}{2R} \quad i_2 = \frac{V_C - V_f}{R} = \frac{V_C - \frac{V_C}{2}}{R} = \frac{V_C}{2R}$$

$$\begin{aligned} R_C &= \frac{V_C}{i_1 + i_2} = \frac{V_C}{\frac{V_C}{2R} + \frac{V_C}{2R}} = \frac{V_C}{2R} = R \\ &= \frac{2V_C - \frac{V_C}{2}}{R} = \frac{V_C}{2R} \\ &= \frac{V_C}{2R} \end{aligned}$$

3. Consider the difference amplifier in problem 1 with the two input terminals connected together to an input common-mode signal source. For  $R_2/R_1 = R_4/R_3$ , show that the input common-mode resistance is  $(R_3+R_4)\parallel(R_1+R_2)$ .

$$i_1 = \frac{V_{cm} - V_-}{R_1} = \frac{V_{cm} - V_{cm} \left( \frac{R_4}{R_3+R_4} \right)}{R_1}$$

$$= \frac{R_3 V_{cm} + V_{cm} R_4 - R_4 V_{cm}}{R_1 (R_3 + R_4)}$$

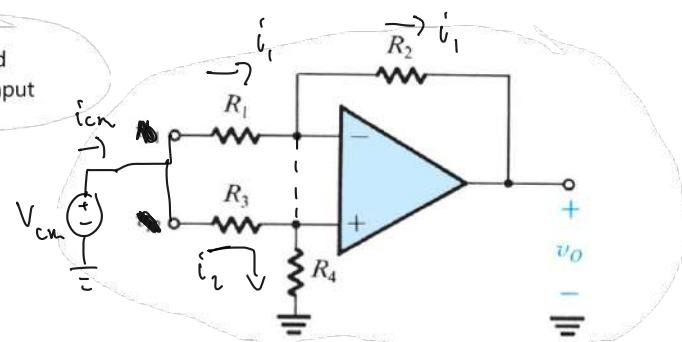
$$= V_{cm} \frac{R_3}{R_1 (R_3 + R_4)}$$

$$i_2 = \frac{V_{cm} - V_{cm} \left( \frac{R_4}{R_3+R_4} \right)}{R_3}$$

$$i_2 = \frac{V_{cm} R_3}{R_3 (R_3 + R_4)}$$

$$i_2 = \frac{V_{cm}}{R_3 + R_4}$$

$$R_{cm} = \frac{V_{cm}}{i_{cm}} = \frac{V_{cm}}{i_1 + i_2} = \frac{V_{cm}}{ }$$



$$V_f = V_{cm} \left( \frac{R_4}{R_3+R_4} \right) = V_-$$

$$i_{cm} = i_1 + i_2$$

$$= V_{cm} \left( \frac{R_3}{R_1 (R_3 + R_4)} + \frac{1}{R_3 + R_4} \right)$$

$$= V_{cm} \left( \frac{1}{R_1} \left( \frac{R_3}{R_3 + R_4} \right) + \frac{1}{R_3 + R_4} \right), \quad R_1 = R_3 \\ R_2 = R_4$$

$$i_{cm} = V_{cm} \left( \frac{1}{R_1} \left( \frac{R_3}{R_3 + R_4} \right) + \frac{1}{R_3 + R_4} \right)$$

$$= V_{cm} \left( \frac{1}{R_1 + R_2} + \frac{1}{R_3 + R_4} \right)$$

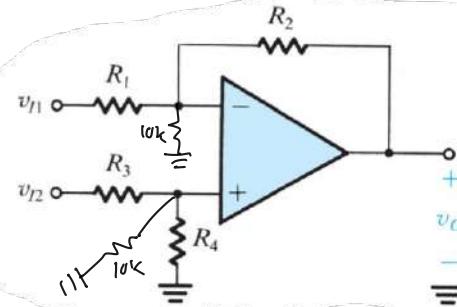
$$\left( \frac{1}{R_1 + R_2} + \frac{1}{R_3 + R_4} \right) = (R_1 + R_2) \parallel (R_3 + R_4) = \frac{V_{cm}}{i_{cm}} = R_{cm}$$

def of  
parallel

4. (a) Find  $A_d$  and  $A_{cm}$  for the difference amplifier circuit in Problem 1 with  $R_1=R_2=R_3=R_4=100k\Omega$ .

(b) If the op amp is specified to operate properly as long as the common-mode voltage at its positive and negative inputs falls in the range  $\pm 2.5$  V, what is the corresponding limitation on the range of the input common-mode signal  $v_{cm}$ ? (This is known as the *common-mode range* of the differential amplifier.)

(c) The circuit is modified by connecting a  $10-k\Omega$  resistor between inverting input and ground, and another  $10-k\Omega$  resistor between non-inverting input and ground. What will now be the values of  $A_d$ ,  $A_{cm}$ , and the input common-mode range?



$$a) \text{ From 1. } A_{cm} = \left( \frac{R_4}{R_3 + R_4} \right) \left( 1 - \frac{R_2 R_3}{R_1 R_4} \right)$$

$$R_1 = R_2 = R_3 = R_4 = 100k \Rightarrow A_{cm} = \left( \frac{1}{2} \right) \left( 1 - 1 \right) = 0$$

$$\boxed{A_{cm} = 0}$$

$$b) V_{Icm} = V_{I1} = V_{I2}$$

$$V_+ = V_{Icm} \quad \frac{R_4}{R_3 + R_4} = V_-$$

$$V_+ = \frac{1}{2} V_{Icm} = V_-$$

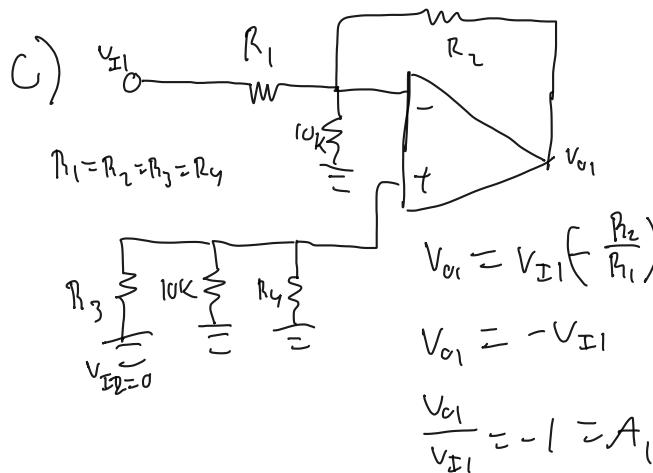
$$V_- = -2.5 = \frac{1}{2} V_{Icm}$$

$$V_t = 2.5 = \frac{1}{2} V_{Icm}$$

$$\boxed{-5[V] \leq V_{Icm} \leq 5[V]}$$

$$\text{From 1. } A_d = \frac{R_2}{R_1}$$

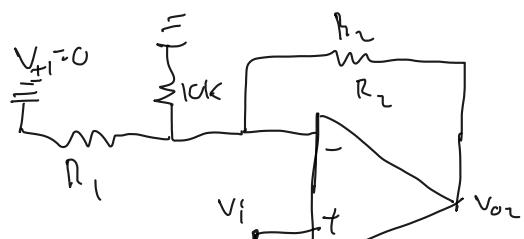
$$\boxed{A_d = 1}$$



$$V_{O1} = V_{I1} \left( \frac{R_2}{R_1} \right)$$

$$V_{O1} = -V_{I1}$$

$$\frac{V_{O1}}{V_{I1}} = -1 = A_1$$



$$\frac{V_O}{V_I} = 1 + \frac{R_f}{R_g}$$

$$R_g = 10k \parallel R_1 = 10k \parallel 100k$$

$$V_{O2} = V_{I2} \left( \frac{100k \parallel 10k}{10k + (100k \parallel 10k)} \right) \left( 1 + \frac{100k}{10k \parallel 10k} \right)$$

$$\frac{V_{O2}}{V_{I2}} = \frac{1}{12}(12) = 1 = A_2$$

$$A_d = \frac{A_2 - A_1}{2} = \frac{1 - (-1)}{2} = 1$$

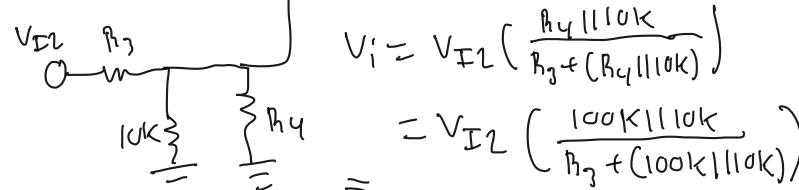
$$\boxed{A_d = 1}$$

$$A_{cm} = A_1 + A_2 \Rightarrow A_{cm} = -1 + 1$$

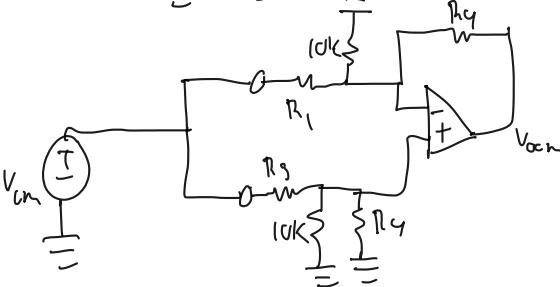
$$\boxed{A_{cm} = 0}$$

$$-2.5[V] \leq \frac{1}{12} V_{cm} \leq 2.5[V]$$

$$\boxed{-30[V] \leq V_{cm} \leq 30[V]}$$



$$V_i = V_{I2} \left( \frac{R_4 \parallel 10k}{R_3 + (R_4 \parallel 10k)} \right) = V_{I2} \left( \frac{100k \parallel 10k}{R_3 + (100k \parallel 10k)} \right)$$



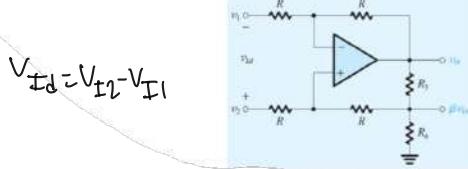
$$V_t = V_- = V_{cm} \left( \frac{10k \parallel 100k}{10k + (10k \parallel 100k)} \right) = \frac{1}{12} V_{cm}$$

5. To obtain a high-gain, high-input-resistance difference amplifier, the circuit below employs positive feedback, in addition to the negative feedback provided by the resistor  $R$  connected from the output to the negative input of the op amp. Specifically, a voltage divider ( $R_5, R_6$ ) connected across the output feeds a fraction  $\beta$  of the output, that is, a voltage  $\beta V_{O1}$ , back to the positive-input terminal of the op amp through a resistor  $R$ . Assume that  $R_5$  and  $R_6$  are much smaller than  $R$  so that the current through  $R$  is much lower than the current in the voltage divider, with the result that  $\beta \sim R_6/(R_5+R_6)$ . Show that the differential gain,  $A_d$ , is given by

$$\frac{v_o}{v_{Id}} = \frac{1}{1-\beta}$$

(Hint: Use superposition.)

Design the circuit to obtain a differential gain of 10 V/V and differential input resistance of  $2M\Omega$ . Select values for  $R$ ,  $R_5$ , and  $R_6$ , such that  $(R_5+R_6) \leq R/100$ .



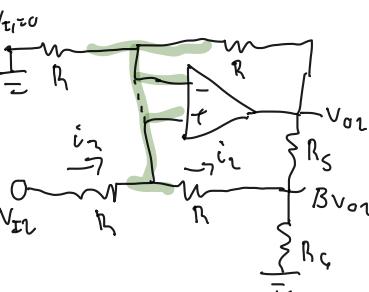
$$i_1 = \left( \frac{V_{I1} - V_-}{R} - \frac{V_- - V_{O1}}{R} \right) R$$

$$V_{O1} - 2V_- = -V_{I1}$$

$$V_{O1} - 2 \frac{\beta V_{O1}}{2} = -V_{I1}$$

$$V_{O1}(1-\beta) = -V_{I1}$$

$$V_{O1} = \frac{-V_{I1}}{1-\beta}$$



$$V_- = V_{O2} \left( \frac{R}{2R} \right) = V_t$$

$$V_- = V_t = \frac{V_{O2}}{2}$$

$$i_2 = \frac{V_{I2} - V_t}{R} = \frac{V_t - \beta V_{O2}}{R}$$

$$= -\frac{V_{I2}}{1-\beta} + \frac{V_{I2}}{1-\beta}$$

$$\beta V_{O2} - 2V_t = -V_{I2}$$

$$\beta V_{O2} - 2 \frac{V_{O2}}{2} = -V_{I2}$$

$$V_{O2}(\beta - 1) = -V_{I2}$$

$$V_{O2} = \frac{V_{I2}}{1-\beta}$$

$$V_o = \underbrace{(V_{I2} - V_{I1})}_{V_{Id}} \frac{1}{1-\beta}$$

$$\boxed{\frac{V_o}{V_{Id}} = \frac{1}{1-\beta} = A_d}$$

$$A_d = 10 = \frac{1}{1-\beta}$$

$$1-\beta = .1$$

$$\beta = .9, \quad \beta \approx \frac{R_G}{R_s + R_G}$$

$$R_s + R_G \leq \frac{1M}{100}$$

$$R_s + R_G \leq 10K$$

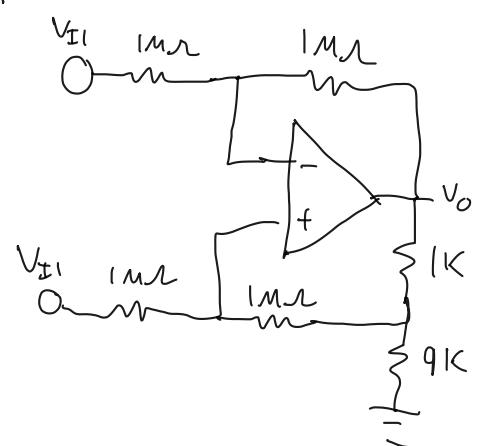
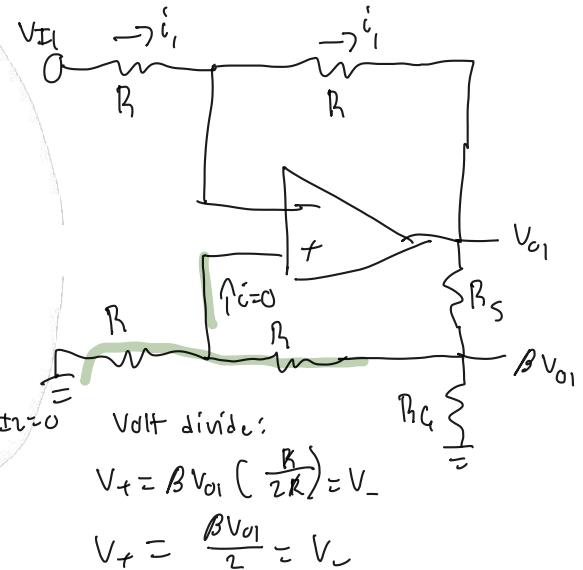
$$R_{id} = R + R = 2R$$

$$2M\Omega = 2R$$

$$\boxed{1M\Omega = R}$$

$$.9 \approx \frac{R_G}{10K}$$

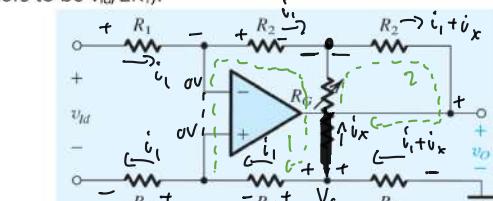
$$\boxed{9K \approx R_G \therefore R_s \approx 1K}$$



6. Figure below shows a modified version of the difference amplifier. The modified circuit includes a resistor  $R_G$ , which can be used to vary the gain. Show that the differential voltage gain is given by

$$\frac{v_o}{v_{id}} = -2 \frac{R_2}{R_1} \left[ 1 + \frac{R_2}{R_G} \right]$$

(Hint: The virtual short circuit at the op-amp input causes the current through the  $R_1$  resistors to be  $v_{id}/2R_1$ .)



$$KCL \text{ at } i_1 R_2 + 0 + i_1 R_2 - V_G = 0 \\ V_G = 2i_1 R_2$$

$$i_1 = \frac{V_{id}}{2R_1} \\ V_{id} = 2R_1 i_1 \\ V_G = i_1 R_G \\ i_x = \frac{V_G}{R_g} = \frac{2i_1 R_2}{R_g}$$

$$\frac{V_o}{V_{id}} = \frac{-4R_2 i_1 \left(1 + \frac{R_2}{R_g}\right)}{2R_1 i_1}$$

$$= -2 \frac{R_2}{R_1} \left(1 + \frac{R_2}{R_g}\right)$$

$KVL \text{ (2)}$ :

$$V_G - (-R_2(i_1 + i_x)) - (R_2(i_1 + i_x)) + V_o = 0$$

$$V_G = -V_o - 2R_2(i_1 + 2i_1 \frac{R_2}{R_g})$$

$$V_o = -2R_2 i_1 - 2i_1 R_2 \left(1 + 2 \frac{R_2}{R_g}\right)$$

$$V_o = -2R_2 i_1 \left(1 + \left(1 + 2 \frac{R_2}{R_g}\right)\right)$$

$$= -2R_2 i_1 \left(2 + 2 \frac{R_2}{R_g}\right)$$

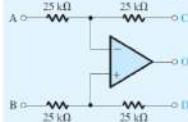
$$V_o = -4R_2 i_1 \left(1 + \frac{R_2}{R_g}\right)$$

7. The circuit shown below is a representation of a versatile, commercially available IC, the INA105, manufactured by Burr-Brown and known as a differential amplifier module. It consists of an op amp and precision, laser-trimmed, metal-film resistors. The circuit can be configured for a variety of applications by the appropriate connection of terminals A, B, C, D, and O.

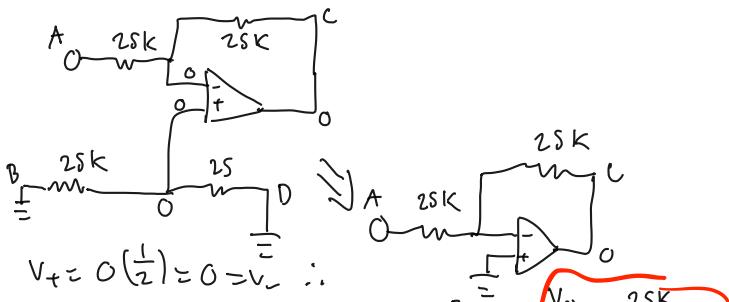
- (a) Show how the circuit can be used to implement a difference amplifier of unity gain.  
 (b) Show how the circuit can be used to implement single-ended amplifiers with gains:

- (i)  $-1 \text{ V/V}$
- (ii)  $+1 \text{ V/V}$
- (iii)  $+2 \text{ V/V}$
- (iv)  $+1/2 \text{ V/V}$

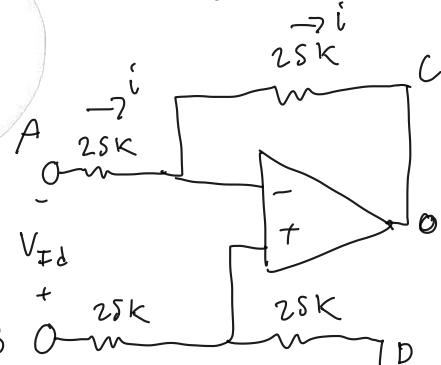
Avoid leaving a terminal open-circuited, for such a terminal may act as an "antenna," picking up interference and noise through capacitive coupling. Rather, find a convenient node to connect such a terminal in a redundant way. When more than one circuit implementation is possible, comment on the relative merits of each, taking into account such considerations as dependence on component matching and input resistance.



$$i) \frac{V_o}{V_i} = -\frac{R_f}{R_i} = -1 \Rightarrow \frac{R_f}{R_i} = 1$$



$$a) \text{Unity gain} = 1 \text{ V/V}$$



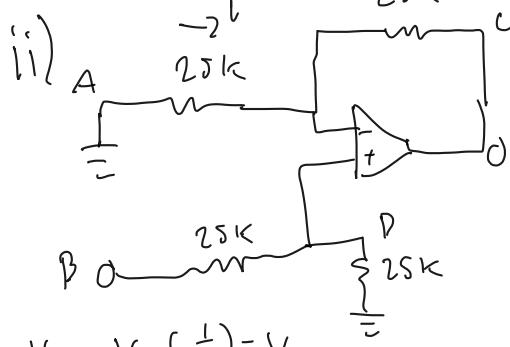
$$i = \frac{V_A - V_-}{25k} = \frac{V_- - V_o}{25k}$$

$$V_o = 2V_- - V_A$$

$$V_o = 2\left(\frac{V_B}{2}\right) - V_A$$

$$V_o = V_B - V_A$$

$$V_o = V_{Id} \Rightarrow \boxed{\frac{V_o}{V_{Id}} = 1}$$



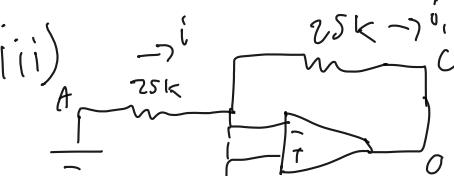
$$V_o = V_B (1/2) = V_-$$

$$i = -\frac{V_-}{25} = \frac{V_- - V_o}{25k}$$

$$V_o = 2V_-$$

$$V_o = 2\left(\frac{V_B}{2}\right)$$

$$\boxed{\frac{V_o}{V_B} = 1}$$



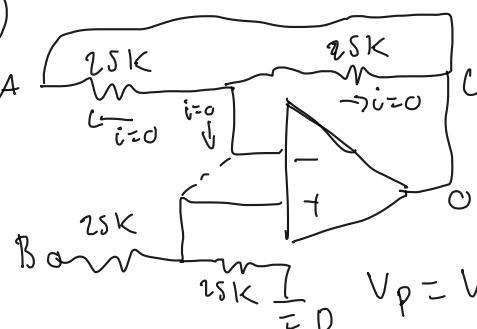
$$i = -\frac{V_-}{25k} = \frac{V_- - V_o}{25k}$$

$$V_o = 2V_-$$

$$V_o = 2V_i$$

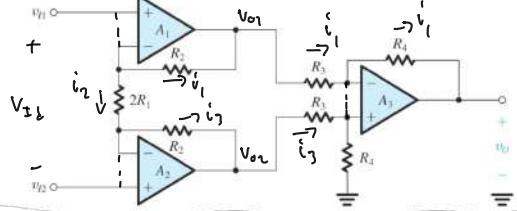
$$\boxed{\frac{V_o}{V_i} = 2}$$

iv)



$$\boxed{\frac{1}{2} = \frac{V_o}{V_B}}$$

8. Design the instrumentation-amplifier circuit below to realize a differential gain, variable in the range 2 to 100, utilizing a 100-kΩ pot as variable resistor.



$$2R_1 = R_{\text{pot}} + R_o \quad \text{Assume } R_4 = 100\text{k} \quad R_3 = 100\text{k}$$

$$R_1 = \frac{R_{\text{pot}} + R_o}{2} \quad A_J = \frac{R_4}{R_3} = 1$$

$$A_d = 2 = \left(1 + \frac{R_2}{R_1}\right) \frac{R_o}{R_2}$$

$$2 = 1 + \frac{R_2}{\frac{100\text{k} + R_o}{2}}, \text{ low: pot max}$$

$$1 = \frac{2R_2}{100\text{k} + R_o}$$

$$100\text{k} + R_o = 2R_2$$

$$A_d = 50 = 1 + \frac{R_2}{\frac{R_o}{2}}, \text{ high: pot=0}$$

$$49 = \frac{2R_2}{R_o}$$

$$49R_o = 2R_2$$

$$49R_o = 100\text{k} + R_o$$

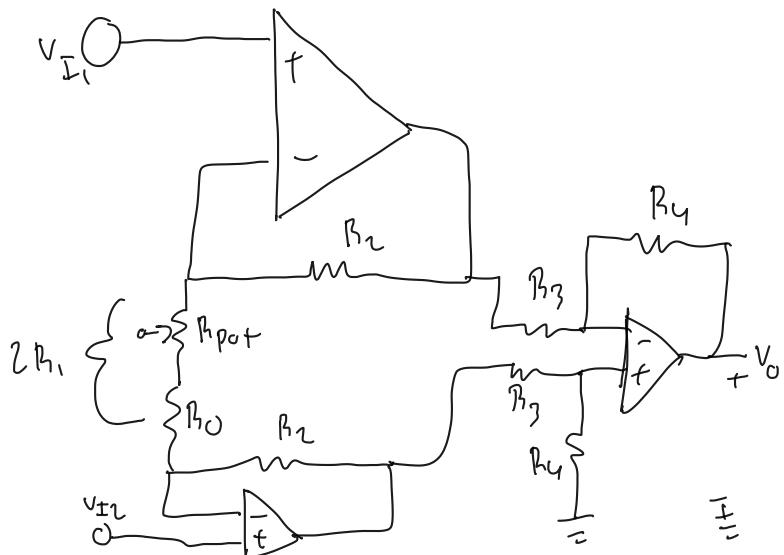
$$R_o = 2,083\text{k}\Omega$$

$$49R_o = 2R_2$$

$$51,042\text{k}\Omega \approx R_2$$

$$\frac{R_o}{2} \leq R_1 \leq \frac{100\text{k} + R_o}{2}$$

$$1,042\text{k}\Omega \leq R_1 \leq 51,042\text{k}\Omega$$



$$\text{For } A_3: V_t = V_{o2} \left( \frac{R_4}{R_3 + R_4} \right) = V_-$$

$$i_1 = \frac{V_{o1} - V_-}{R_3} = \frac{V_- - V_o}{R_4}$$

$$= \frac{V_o}{R_4} = V_- \left( \frac{1}{R_3} + \frac{1}{R_4} \right) - V_{o1} \left( \frac{1}{R_3} \right)$$

$$V_o = V_{o2} \left( \frac{R_o}{R_3 + R_o} \right) \left( \frac{R_4}{R_3} + 1 \right) - V_{o1} \left( \frac{R_4}{R_3} \right)$$

$$= V_{o2} \left( \frac{R_o}{R_3 + R_o} \right) \left( \frac{R_4 + R_o}{R_3} \right) - V_{o1} \left( \frac{R_4}{R_3} \right)$$

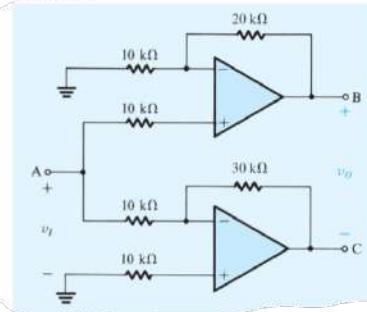
$$V_o = \frac{R_4}{R_3} (V_{o2} - V_{o1})$$

$$V_o = \frac{R_4}{R_3} (V_{I1} - V_{I2}) \left( \frac{R_2}{R_1} + 1 \right)$$

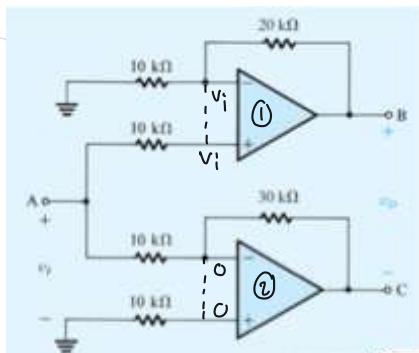
$$\frac{V_o}{V_{Id}} = \left(1 + \frac{R_2}{R_1}\right) \frac{R_4}{R_3} = A_d$$

9. The circuit shown below is intended to supply a voltage to floating loads (those for which both terminals are ungrounded) while making greatest possible use of the available power supply.

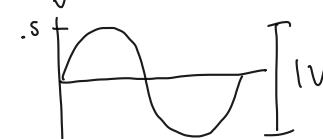
- (a) Assuming ideal op amps, sketch the voltage waveforms at nodes B and C for a 1-V peak-to-peak sine wave applied at A. Also sketch  $v_o$ .  
 (b) What is the voltage gain  $v_o/v_i$ ?  
 (c) Assuming that the op amps operate from  $\pm 15$ -V powersupplies and that their output saturates at  $\pm 14$  V (in the manner shown in Fig. 1.14), what is the largest sine-wave output that can be accommodated? Specify both its peak-to-peak and rms values.



a)



$$v_i = 1 \text{ V}_{\text{pp}}$$



$$v_o = v_B - v_C = 3v_i - (-3v_i) = 6v_i$$

$$\textcircled{1} \quad \frac{v_i}{10k} + \frac{v_i - v_B}{20k} = 0$$

$$\frac{v_B}{20k} = v_i \left( \frac{1}{10k} + \frac{1}{20k} \right)$$

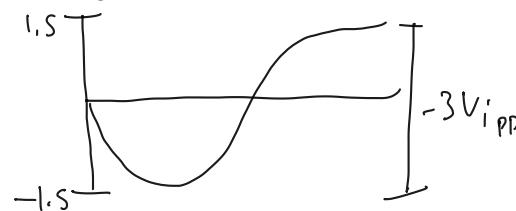
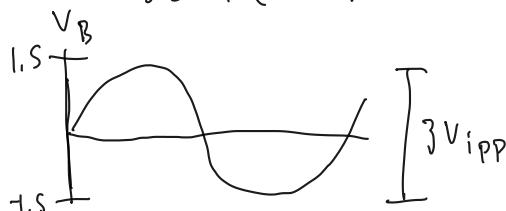
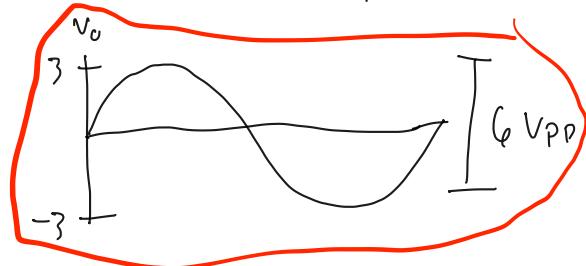
$$v_B = v_i (2+1) = 3v_i$$

$$\textcircled{2} \quad -\frac{v_i}{10k} - \frac{v_C}{30k} = 0$$

$$\frac{v_C}{30k} = -\frac{v_i}{10k}$$

$$v_C = -3v_i$$

$$\frac{v_o}{v_i} = 6$$



c) output saturation  $\pm 14$  [V]

$$3v_{i,\text{peak}} \leq 14$$

$$V_{o,\text{pk}} = 6v_{i,\text{pk}} = 6 \left( \frac{14}{3} \right)$$

$$v_{i,\text{peak}} \leq \frac{14}{3}$$

$$V_{o,\text{pk}} = 28 \quad \therefore$$

$$V_{o,\text{pp}} = 56 \text{ [V]}$$

$$V_{o,\text{rms}} = \frac{V_{\text{pk}}}{\sqrt{2}} = 19.8 \text{ [V]}$$

10. A particular op amp using  $\pm 15$ -V supplies operates linearly for outputs in the range  $-14$  V to  $+14$  V. If used in an inverting amplifier configuration of gain  $-100$ , what is the rms value of the largest possible sine wave that can be applied at the input without output clipping?

$$V_o = A_{op} \sin(\omega t)$$

$$A_v = \frac{V_o}{V_i} = 100$$

$$V_o = 14 \sin(\omega t)$$

$$V_i = \frac{14}{100} \sin(\omega t)$$

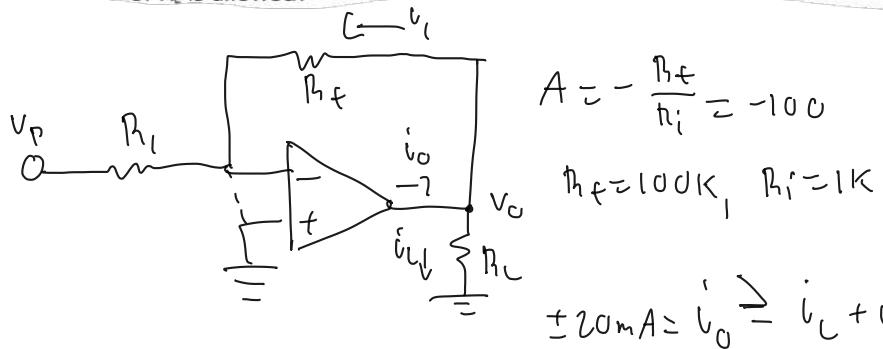
$$V_i = .14 \sin(\omega t) [V]$$

$$V_{i,rms} = \frac{14}{\sqrt{2}}$$

$$= .09899 [V]$$

11. Consider an op amp connected in the inverting configuration to realize a closed-loop gain of  $-100$  V/V utilizing resistors of  $1\text{ k}\Omega$  and  $100\text{ k}\Omega$ . A load resistance  $R_L$  is connected from the output to ground, and a low-frequency sine-wave signal of peak amplitude  $V_p$  is applied to the input. Let the op amp be ideal except that its output voltage saturates at  $\pm 10$  V and its output current is limited to the range  $\pm 20$  mA.

- (a) For  $R_L = 1\text{ k}\Omega$ , what is the maximum possible value of  $V_p$  while an undistorted output sinusoid is obtained?
- (b) Repeat (a) for  $R_L = 200\text{ }\Omega$ .
- (c) If it is desired to obtain an output sinusoid of  $10$ -V peak amplitude, what minimum value of  $R_L$  is allowed?



$$A = -\frac{R_f}{R_i} = -100$$

$$R_f = 100\text{ k}\Omega, R_i = 1\text{ k}\Omega$$

$$\pm 20\text{ mA} = i_o \geq i_L + i_U$$

a)  $\frac{V_o}{V_p} = -100, R_L = 1\text{ k}\Omega$

$$V_o = -100 V_p \sin(\omega t)$$

$$10 = 100 V_p$$

$$.1 [V] = V_{p,max}$$

for undistorted

$$V_o$$

$$\left( \frac{V_o}{R_L} + \frac{V_o}{100\text{ k}} \leq 20 \text{ mA} \right) 100\text{ k}$$

$$100V_o + V_o \leq 2\text{ k}$$

$$101V_o \leq 2\text{ k}$$

$$V_o \leq \pm 19.8 \text{ V} \quad \text{but saturates at}$$

$$V_o = \pm 10 \text{ V}$$

c)  $V_o = 10 = 100 V_p$

$$\frac{V_o}{R_L} + \frac{V_o}{100\text{ k}} = 20 \text{ mA}$$

$$\frac{10}{R_L} + .1\text{ m} = 20 \text{ mA}$$

$$\frac{10}{19.9\text{ m}} = R_L$$

$$502.5 \text{ } \Omega = R_{L,min}$$

for  $V_o = 10$

b)  $\frac{V_o}{200} + \frac{V_o}{100\text{ k}} \leq 20 \text{ mA}$

$$500V_o + V_o \leq 2\text{ k}$$

$$501V_o \leq 2\text{ k}$$

$$V_o \leq \pm 3.99 \text{ V}$$

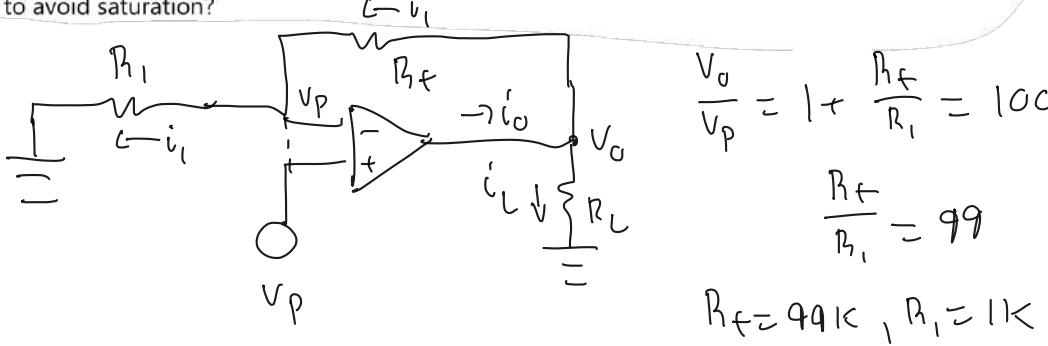
$$V_o = 100 V_p$$

$$3.99 = 100 V_p$$

$$39.9 \text{ mV} = V_{p,max}$$

for  $R_L = 200$

12. Repeat problem 11 from above assuming this time that it is a non-inverting configuration amplifier and that it utilizes 1k and 99k resistors. What is the minimum output resistor value to avoid saturation?



$$R_L = 1k$$

$$V_o = 100 v_p \sin(\omega t)$$

$$i_o = i_i + i_L$$

$$i_o = \frac{V_o}{R_f + R_1} + \frac{V_o}{R_L}$$

$$\frac{10}{100k} + \frac{10}{R_L} = 20mA$$

$$\frac{10}{R_L} = 19.9mA$$

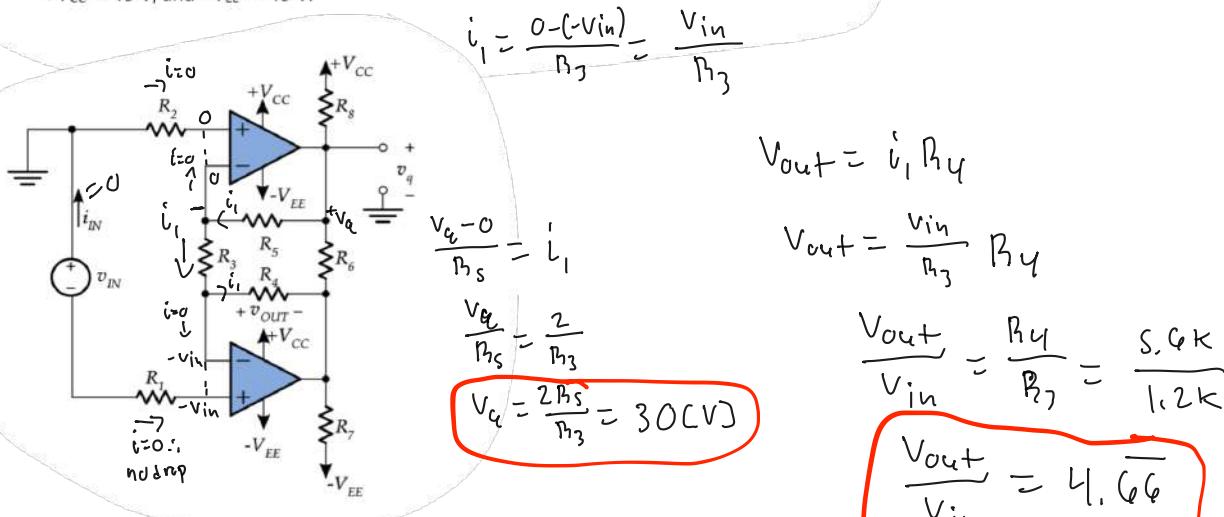
$$R_{L,\min} = 502.5\Omega$$

to avoid saturation

13. Assuming ideal op-amps,

- Find the voltage gain,  $V_{OUT}/V_{IN}$
- Find input resistance,  $V_{IN}/I_{IN}$
- If  $V_{IN} = 2V$ , find  $V_Q$

Use  $R_1 = 38k\Omega$ ,  $R_2 = 15k\Omega$ ,  $R_3 = 1.2k\Omega$ ,  $R_4 = 5.6k\Omega$ ,  $R_5 = 18k\Omega$ ,  $R_6 = 2.2k\Omega$ ,  $R_7 = 4.7k\Omega$ ,  $R_8 = 3.3k\Omega$ ,  $+V_{CC} = +15V$ , and  $-V_{EE} = -15V$ .



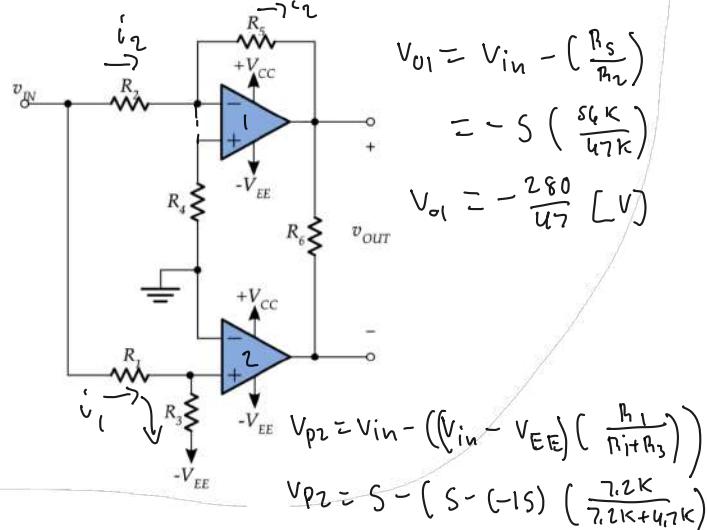
$$V_{in} = 0 \therefore R_{in} = \frac{V_{in}}{i_{in}}$$

$$= \frac{V_{in}}{0}$$

$$R_{in} \rightarrow \infty$$

14. Assuming ideal op-amps, find  $v_{out}$

Use  $R_1=7.2\text{ k}\Omega$ ,  $R_2=47\text{ k}\Omega$ ,  $R_3=4.7\text{ k}\Omega$ ,  $R_4=3.3\text{ k}\Omega$ ,  $R_5=56\text{ k}\Omega$ ,  $R_6=8.2\text{ k}\Omega$ ,  $V_{IN}=+5\text{ V}$ ,  $+V_{CC}=+15\text{ V}$ , and  $-V_{EE}=-15\text{ V}$ .



$$V_{out} = V_{o1} - V_{o2}$$

$$\approx -\frac{280}{47} - (-15)$$

$$= 9.043 [\text{V}]$$

$$V_{p2} = -7.1 [\text{V}]$$

$$V_{n2} = 0$$

$$V_{o2} = A_2 (V_+ - V_-)$$

$$V_+ < V_- \therefore V_o \rightarrow -V_{EE}$$