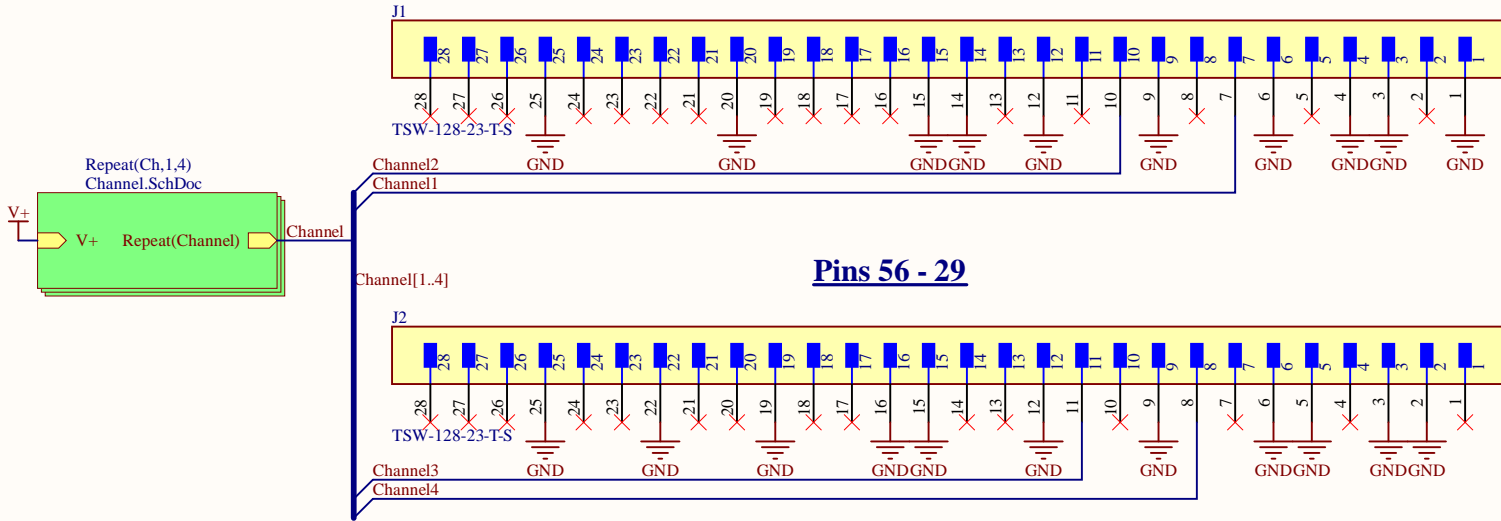


MCC USB-1808X

Pins 28 - 01

Pins 56 - 29

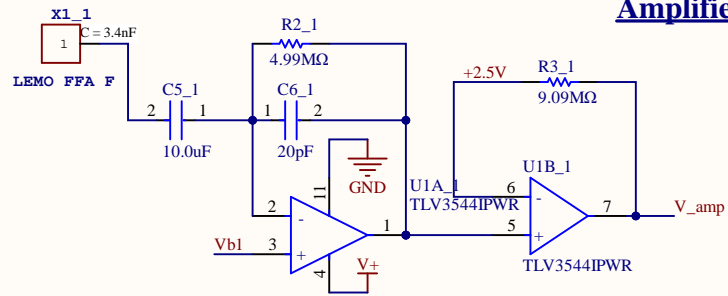


Single-ended mode pinout

Terminal			Terminal		
#	Label	Use	#	Label	Use
1	CH0H	AI channel 0 HI	29	NC	No connection
2	NC	No connection	30	CH7H	AI channel 7 HI
3	AGND	Analog ground	31	AGND	Analog ground
4	CH1H	AI channel 1 HI	32	NC	No connection
5	NC	No connection	33	CH6H	AI channel 6 HI
6	AGND	Analog ground	34	AGND	Analog ground
7	CH2H	AI channel 2 HI	35	NC	No connection
8	NC	No connection	36	CH5H	AI channel 5 HI
9	AGND	Analog ground	37	AGND	Analog ground
10	CH3H	AI channel 3 HI	38	NC	No connection
11	NC	No connection	39	CH4H	AI channel 4 HI
12	AGND	Analog ground	40	AGND	Analog ground
13	+VO	+5V power output	41	AOUT0	AO channel 0
14	AGND	Analog ground	42	AOUT1	AO channel 1
15	GND	Digital Ground	43	AGND	Analog ground
16	TMR0	Timer 0 output	44	GND	Digital Ground
17	TMR1	Timer 1 output	45	DIO0	DIO channel 0
18	ICLK0	Input scan clock output	46	DIO1	DIO channel 1
19	OCLK0	Output scan clock output	47	GND	Digital Ground
20	GND	Digital ground	48	DIO2	DIO channel 2
21	ICLK1	Input scan clock input	49	DIO3	DIO channel 3
22	OCLK1	Output scan clock input	50	GND	Digital Ground
23	ITRIG	Input Trigger	51	CTR0	Counter 0 Input
24	OTRIG	Output Trigger	52	CTR1	Counter 1 Input
25	GND	Digital ground	53	GND	Digital ground
26	ENC1A	Encoder 1 Input A	54	ENC0A	Encoder 0 Input A
27	ENC1B	Encoder 1 Input B	55	ENC0B	Encoder 0 Input B
28	ENC1Z	Encoder 1 Input Z	56	ENC0Z	Encoder 0 Input Z

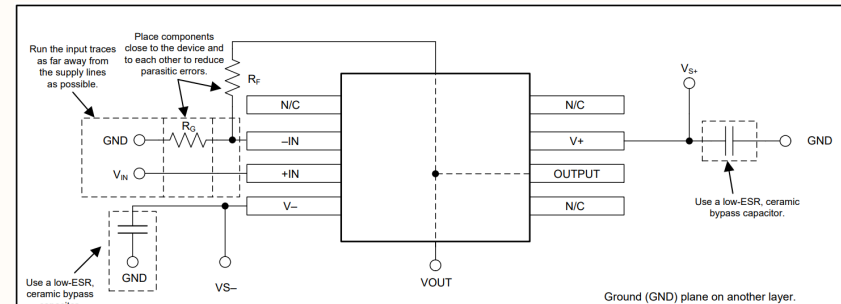
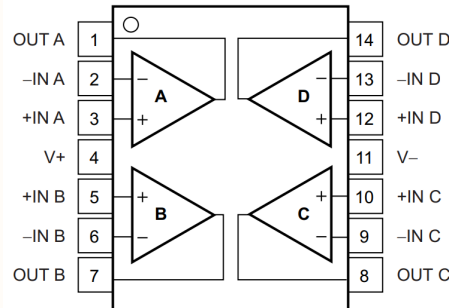
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Size	Number	Revision
A4		
Date:	12/10/2024	Sheet of
File:	ASV4-2 Acoustics Board.SchDoc	Drawn By:

Amplifier

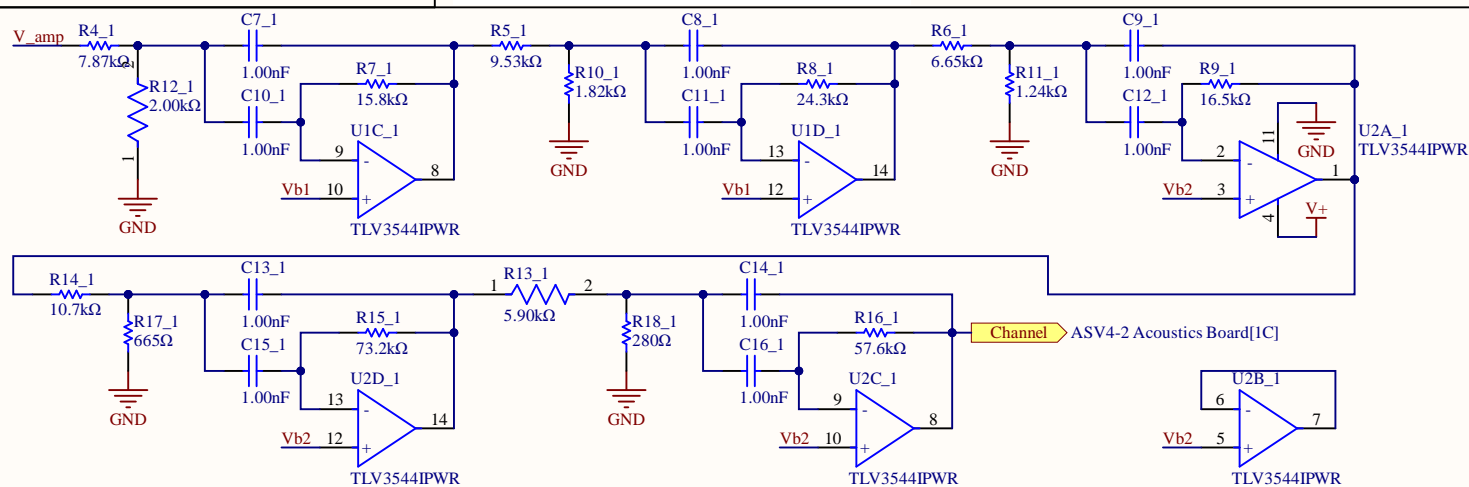


1% tolerance

TLV3544: D and PW Packages 14-Pin SOIC, TSSOP Top View



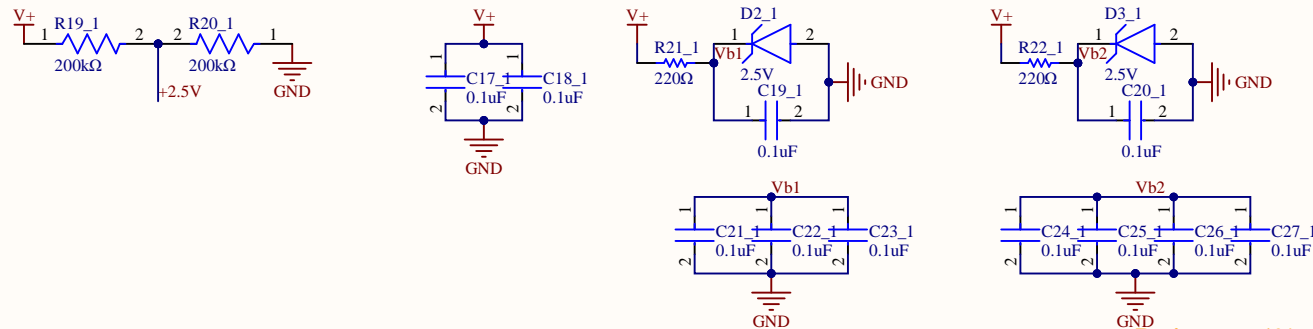
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1% tolerance

ASV4-2 Acoustics Board[1C] V+

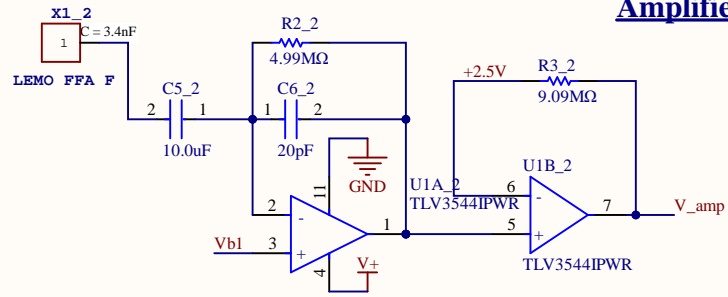
Power



Resistors at 1% tolerance
Capacitors at 5% tolerance

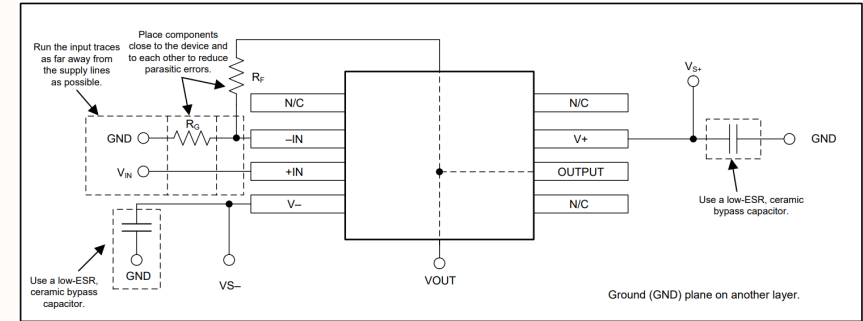
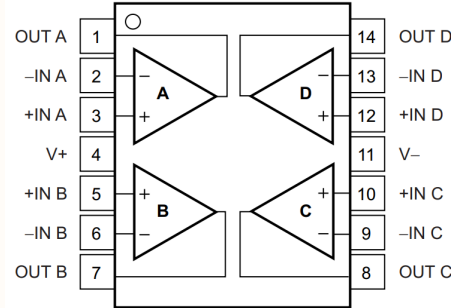
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Amplifier

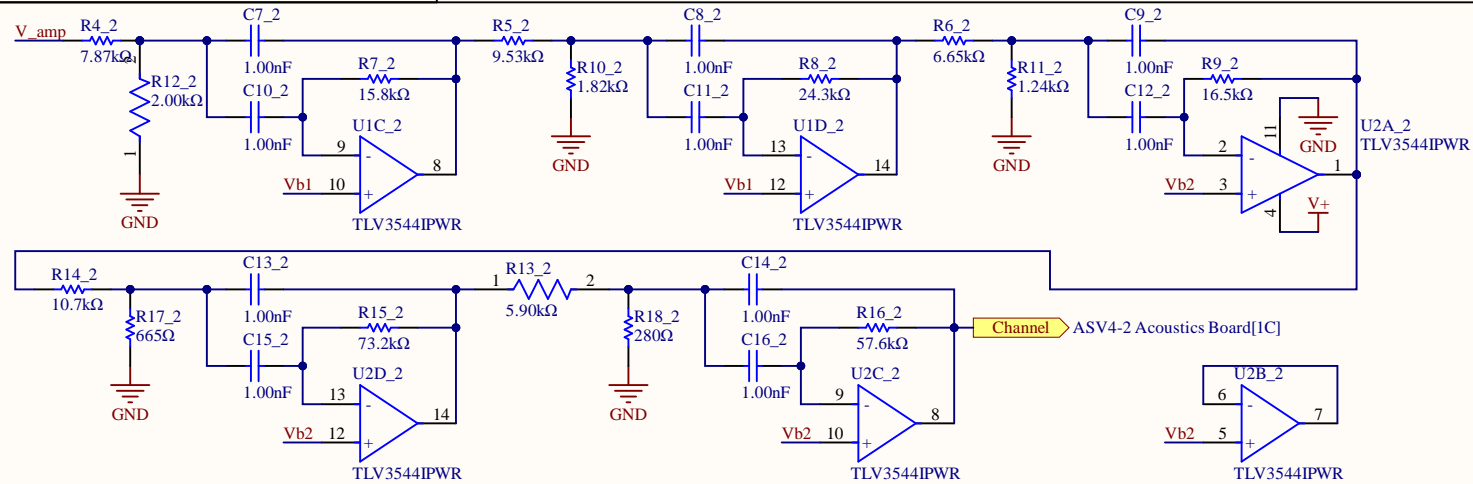


1% tolerance

TLV3544: D and PW Packages
14-Pin SOIC, TSSOP
Top View

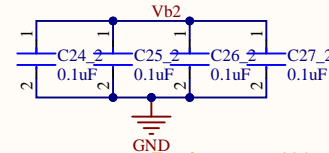
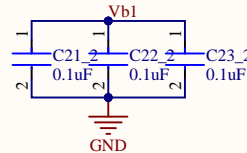
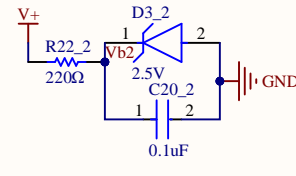
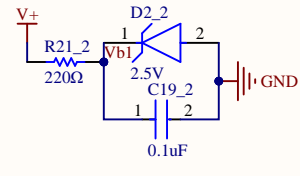
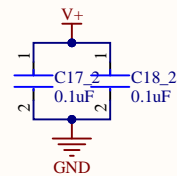
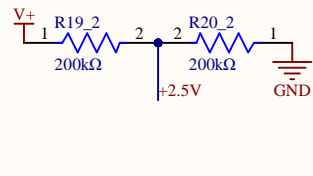


Filter



1% tolerance

Power



Resistors at 1% tolerance
Capacitors at 5% tolerance

Title

Size
A4

Number

Revision

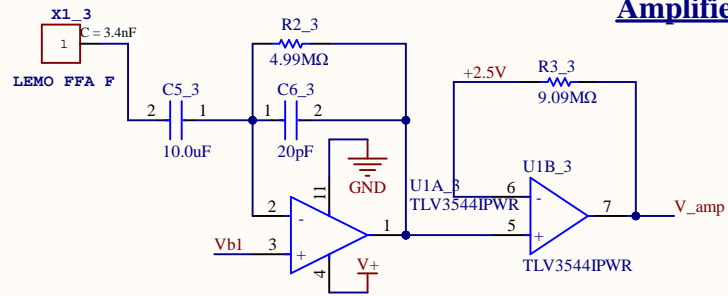
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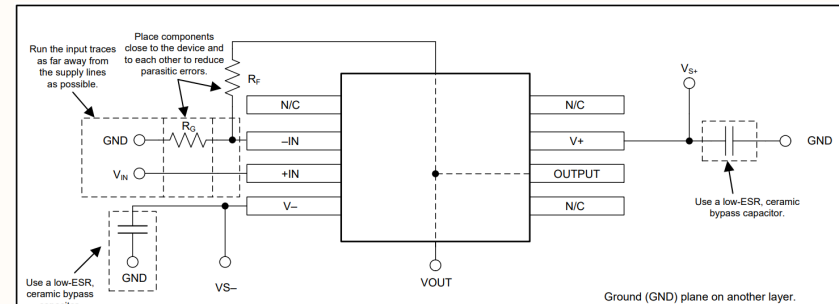
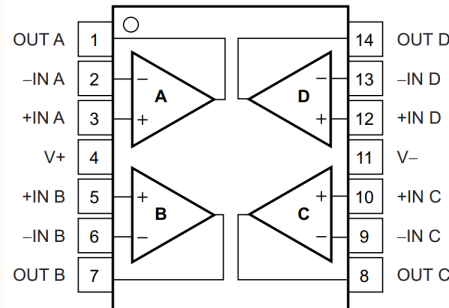
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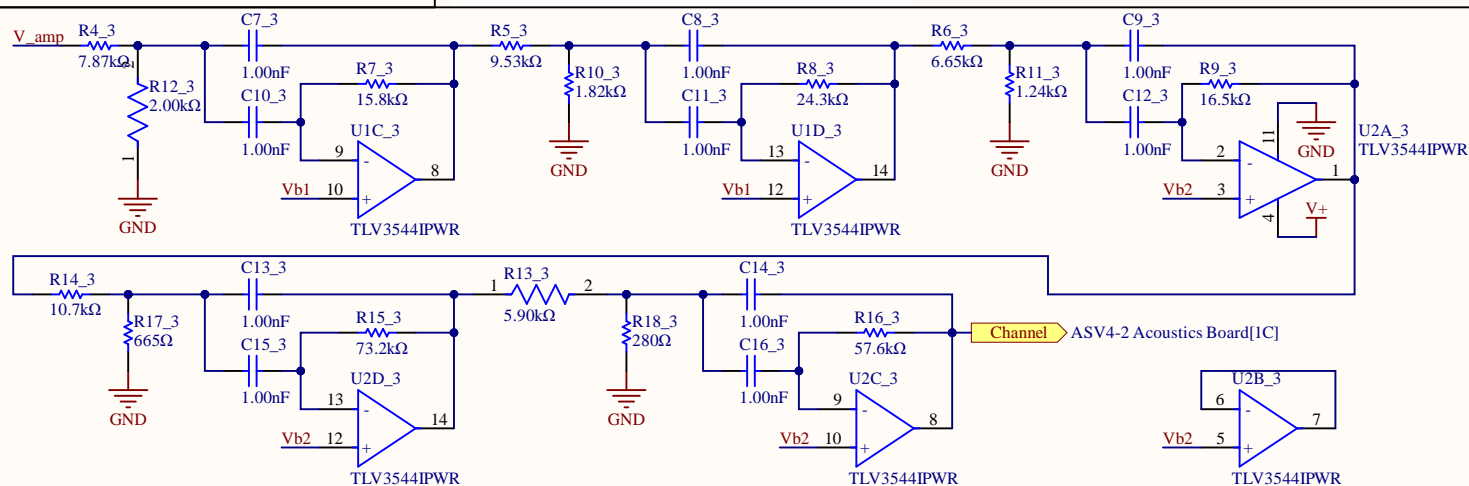


1% tolerance

TLV3544: D and PW Packages 14-Pin SOIC, TSSOP Top View

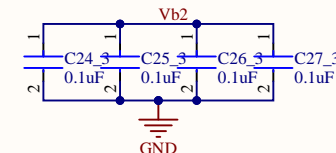
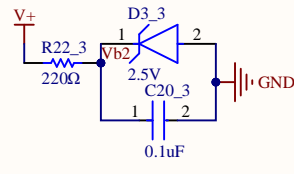
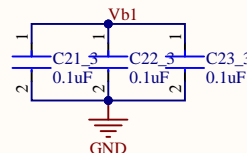
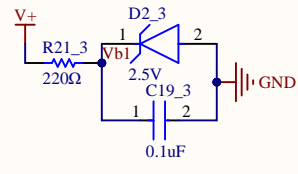
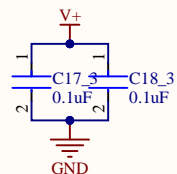
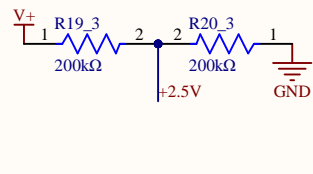


Filter



1% tolerance

ASV4-2 Acoustics Board[1C] V+



Resistors at 1% tolerance
Capacitors at 5% tolerance

Power

Title

Size
A4

Number

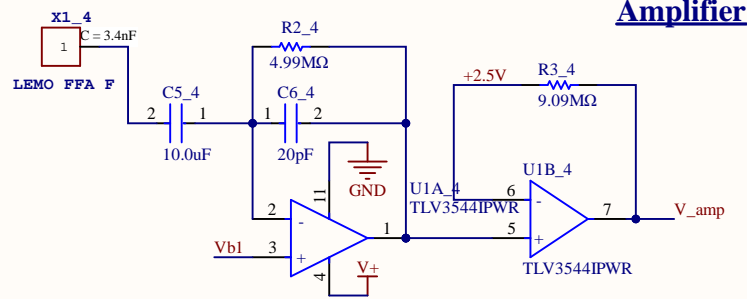
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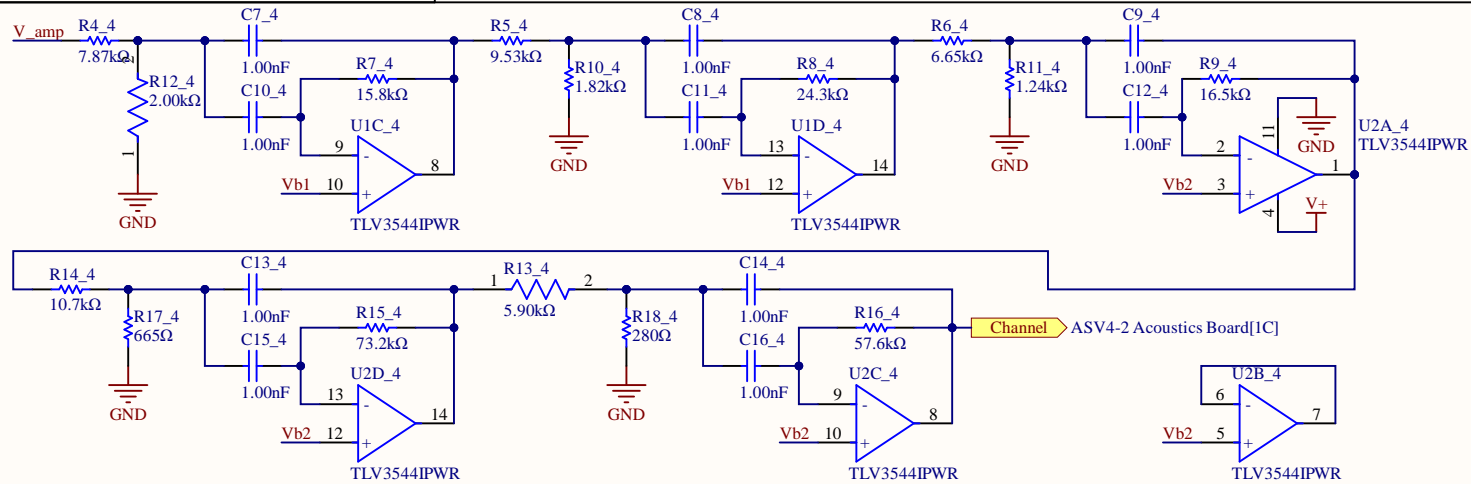
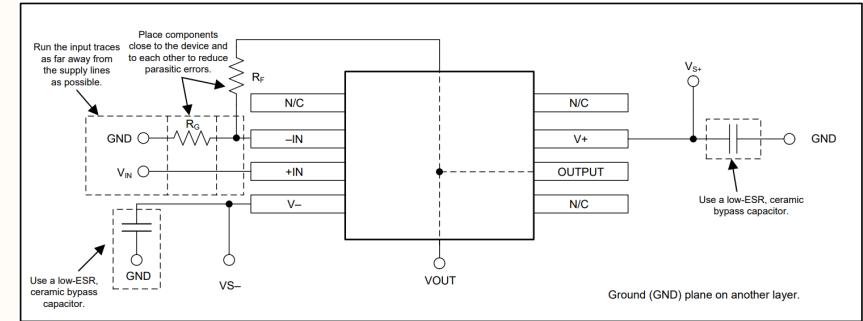
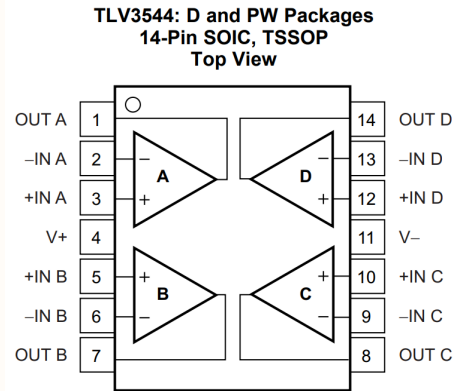
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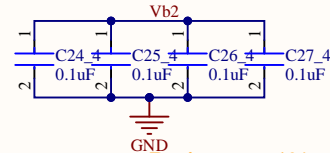
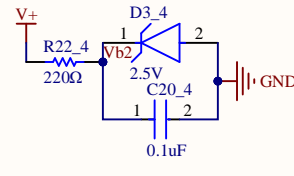
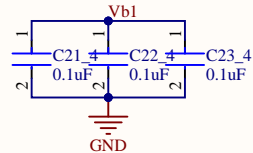
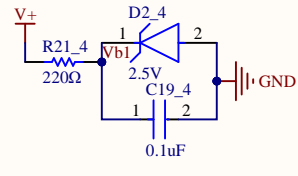
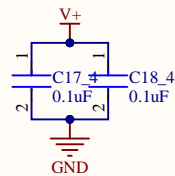
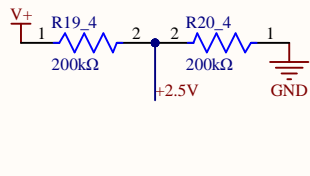
Amplifier

1% tolerance



Filter

1% tolerance



Resistors at 1% tolerance
Capacitors at 5% tolerance

Power

Title		
Size A4	Number	Revision
Date:	12/10/2024	Sheet of
File:	Channel.SchDoc	Drawn By: