EVAN LAU

ELECTRICAL ENGINEERING STUDENT

CONTACT

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Toronto, Ontario

SKILLS

- KiCad
- Programming
- Soldering
- Circuit Analysis
- PCB Design
- Communication
- Time Management
- Project Management
- Teamwork & Collaboration

ACHIEVEMENTS

- Ontario Scholar Award
- WHMIS Student Certification
- CCC Senior 2024 - Top 25%
- Taekwondo Provincial Bronze Medalist
- Taekwondo National Competitor
- Kukkiwon Certified 3rd Dan Black Belt

PROFILE

Motivated Engineering student with strong problem solving, technical, and communication skills. I am extremely interested in hardware design, particularly in PCB architecture and digital logic systems. I enjoy translating theoretical concepts into practical implementations. Additionally I am very eager to expand my technical knowledge through hands on experiences.

EDUCATION

Bachelors of Engineering 2025 - Present
Studying at Toronto Metropolitan University
GPA: 4.22

Ontario Secondary School Diploma 2021 - 2025
Graduated with Ontario Scholar Award

EXPERIENCES

Avionics Technician 2025 - Present
TMU MACH

- Assist in the design, and testing of avionics systems for student-led aerospace projects
- Support PCB layout, wiring, and sensor integration for flight hardware
- Hand-soldered and assembled ignition cabling for propulsion system integration

Taekwondo Instructor 2021 - 2025
Alliance Taekwondo

- Instructed classes of 4 - 13 year old children
- Mentored and motivated students, fostering confidence, teamwork, and perseverance.
- Developed lesson plans and adapted teaching methods to suit different learning styles.
- Balanced teaching responsibilities with national-level athletics and academics

RP2040 Sensor Board 2025
Student Organization Project

- Designed a custom RP2040 based PCB integrating 9-axis sensors
- Developed the schematic capture and PCB layout from scratch
- Applied foundational PCB design principles

555 Timer Roulette Simulation 2024
Academic Project

- Designed a PCB using the 555 timer IC to generate timed pulse sequences for a roulette simulation
- Assembled and soldered components, debugged timing behavior, and validated consistent operation

Arithmetic Logic Unit (ALU) 2024
Academic Project

- Designed and implemented an ALU capable of basic arithmetic and logic operations
- Extended the design to support signed numbers and base-conversion functionality
- Tested and verified correctness across multiple input cases