DESIGNED FOR:
2 layer stackup (1.6mm) – JLCPCB
Core Er = 4.6 @ 1 GHz
Core Loss Tan = 0.025 @ 1 GHz

REV1 BUGS / THINGS TO IMPROVE:

Project Name: PCB Log Periodic Antenna (0.8–6 GHz)		**BYTECHLAB RESEARCH**
Sheet Name: Root		
Sheet Path: /		CC BY–SA 4.0
File Name: LOG_PERIODIC_PCB.kicad_sch		
Size: A4	Date: 2026–01–03	**Rev: 1**
Designed By: Andrzej Laczewski	Website: www.bytechlab.com	Id: #/1