भारतीय प्रौद्योगिकी संस्थान हैदराबाद
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ESP Assignment

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I. ABSTRACT

This paper explains about determining the logic values X_2 and Y_2 for a given digital circuit by analyzing its truth table and circuit diagram. The circuit consists of multiple logic gates (AND, OR, and NOT gates) connected in a specific configuration, with inputs labeled G, A, B, P, and C. By evaluating the output of each gate based on the inputs, the values of X and Y are derived and matched against the truth table. This approach allows us to accurately identify the correct logic values for X_2 and Y_2.

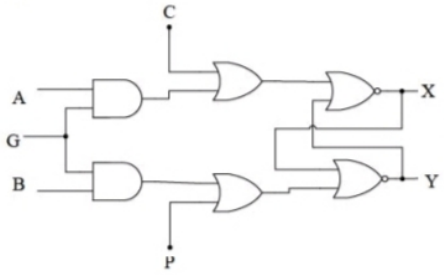

Fig. 1.

G	A	B	P	C	X	Y
1	0	1	0	0	0	1
0	0	0	1	0	X_2	Y_2
1	0	0	0	1	0	1

Fig. 2.

II. COMPONENTS

The required components list is given in Table: I., pin diagram of vaman is shown in Fig.1.

Components	Quantity
vaman	1
led	2
Jumper Wires	10
Breadboard	1

TABLE I

III. PROCEDURE

1) Pin Configuration of vaman board.

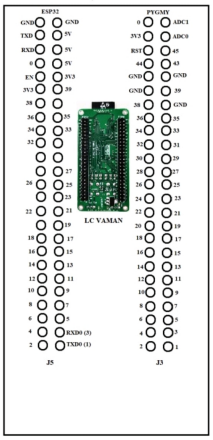

Fig. 3.

2) Make connections of vaman to two leds as per below table.

Led-1	Led-2	Vaman
Anode	-	GPIO-2
-	Anode	GPIO-4
Cathode	Cathode	Gnd

TABLE II

3) Execute the esp code with wifi in nvim editor using the command called pio run.
4) After upload the esp-code into hardware setup using the command called pio run -t nobuild -t upload –upload-port 192.168.217.128.

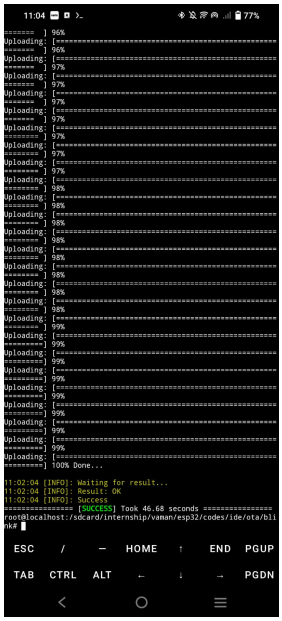

Fig. 4.

IV. RESULTS

1) Download the codes given in the link below and execute them to see the output as shown in figure 3.
2) https://github.com/BynaboyinaAiswarya/Fwc-/blob/main/Esp/main.cpp

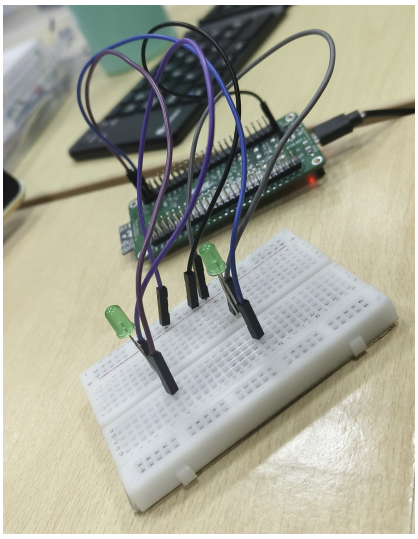

Fig. 5.

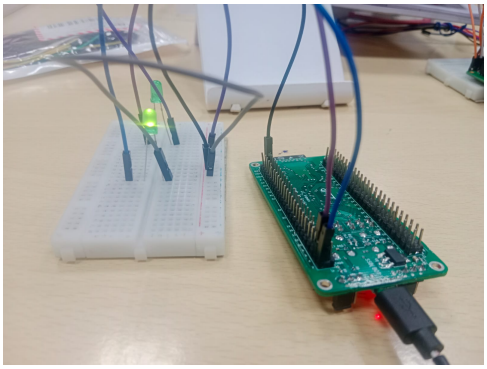

Fig. 6.

V. CONCLUSION

Hence implementation of above astract using esp code with vaman board and verification through leds is done .