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LEDOUX Louis

Curriculum Vitae

"Post Hoc Ergo Propter Hoc"

Education

2015
2018

Engineering School, *ESIR*, Université de Rennes

Three years to obtain the engineer diploma certified by the CTI (Comité des Titres d'Ingénieurs) and a Master (Magister) in computer sciences

- ddd
- DDD

2013
2015

Classe Préparatoire, *CUPGE ESIR*, Université de Rennes

- 6/23 first year
- 7/22 second year

Experience

2018

Researcher, *Barcelona Supercomputing Center (BSC)*, Barcelona

researching..

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- **Keywords:** FPGA, Arith, ASIC

2017
2018

FPGA as a Service Engineer, *b⟨⟩com*, Rennes

- Coop student known as professionalization contract in French for my last study's year. One year of R&D about Acceleration on FPGA on the cloud.
- **Keywords:** SDAccel, OpenCL, Xilinx FPGA (ultrascale vu9p), VHDL, SystemVerilog, AWS F1 instances, linux driver, linux kernel, PCIe, C++1x, C, DMA, AaaS (Acceleration as a Service).

July 2017

Back End Developer, *WaryMe*, Rennes

Summer Internship...

- Development of all the back end of a people security application.
- **Keywords:** NodeJS, sqlite3, git, c++, C, Angular, TypeScript, https, let's encrypt, pm2, nginx, AWS, jenkins.

July 2016 **Back End Developer**, *ASKIA*, Paris 10ème
Summer Internship...
- Development of a CLI tool which allows automated publication of surveys on well known platforms like Github or Zendesk.
- **Keywords:** NodeJS, http, api rest, git, test driven development, asynchronous programming, jasmine framework, mocks.

July 2014 **Electronics Technician**, *Radio Electronique Rennaise (R.E.R)*, Rennes
Summer Internship...
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- **Keywords:**

Peer-reviewed Conference Papers

- [Doe, 2021]
- [Doe, 2022]

Poster Presentations

- [Doe, 2021]

Invited Talks

- [Doe, 2022]

Publications

[Doe, 2021]   Doe, J. (2021). Sample article. *Journal of Examples*, 1(1):1–2.

[Doe, 2022]   Doe, J. (2022). *Sample Book*. Example Publisher.

Skills

Programming   Java, Python, C++
Languages   English, Spanish