Quick and easy development framework for OpenCAPI Accelerators

Software on Host Server

- Process C
- Process B
- Process A
- **Software Program**
  - osnap library
  - libocxl
  - ocxl

OpenCAPI

Acceleration on FPGA

OC/AXI Bridge mode

- cfg
- TLx DLx
- *mmio*
- *data bridge*
- snap core

AXI4 lite

AXI4-MM

Hardware Action

Master

Slave

Master

Verilog or HLS C/C++

AXI

DRAM on-card

Others ...
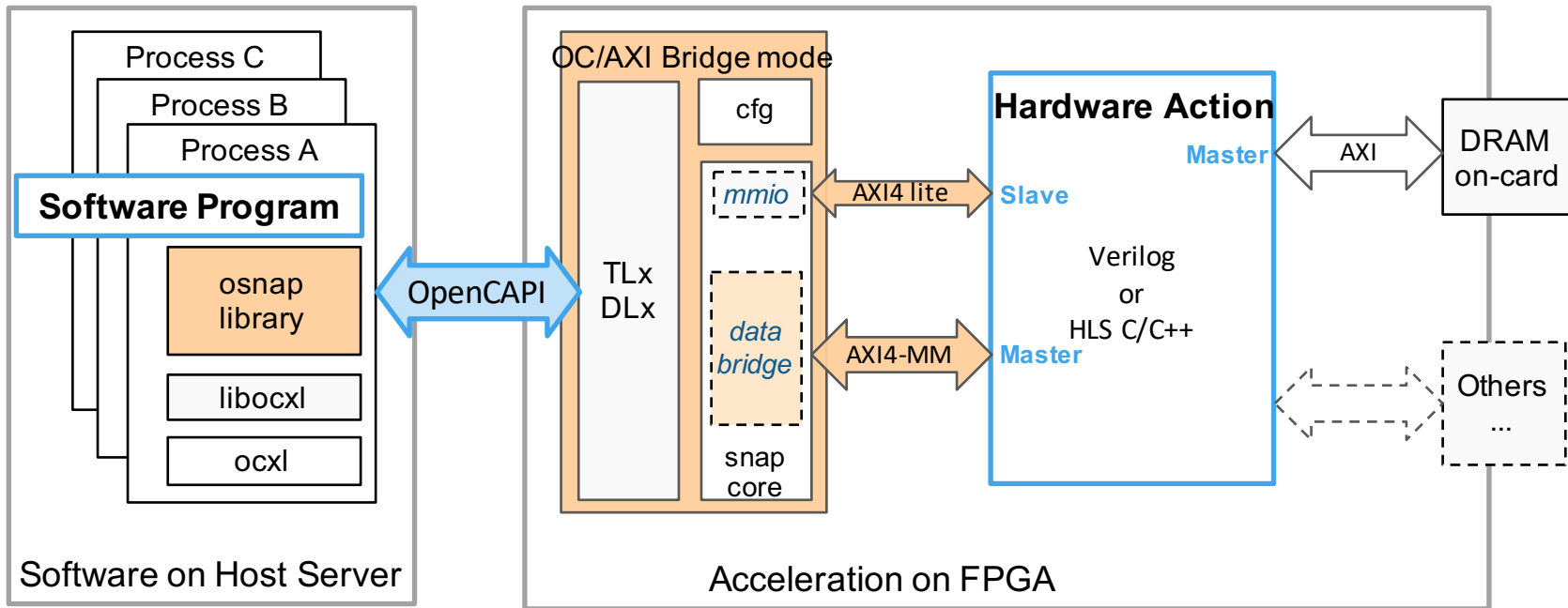
Integrated Development Environment (IDE)