

# oc-accel

*scripts* ✓

*defconfig*

*web-doc*

*software*

*hardware* ✓

*hdl*

*setup*

*sim*

*oc-bip*

*actions/<NAME>*

*sw*

*hw* ✓

*test*

## oc-bip

(OpenCAPI3.0\_Device\_RefDesign)

*Board\_support\_packages/<CARD>* ✓

*scripts* ✓

*config\_subsystem* ✓

*sim*

*tlx* ✓

*dlx* ✓

**Development on x86 Linux machine: Implementation**  
(Need Xilinx Vivado to generate bitstream )