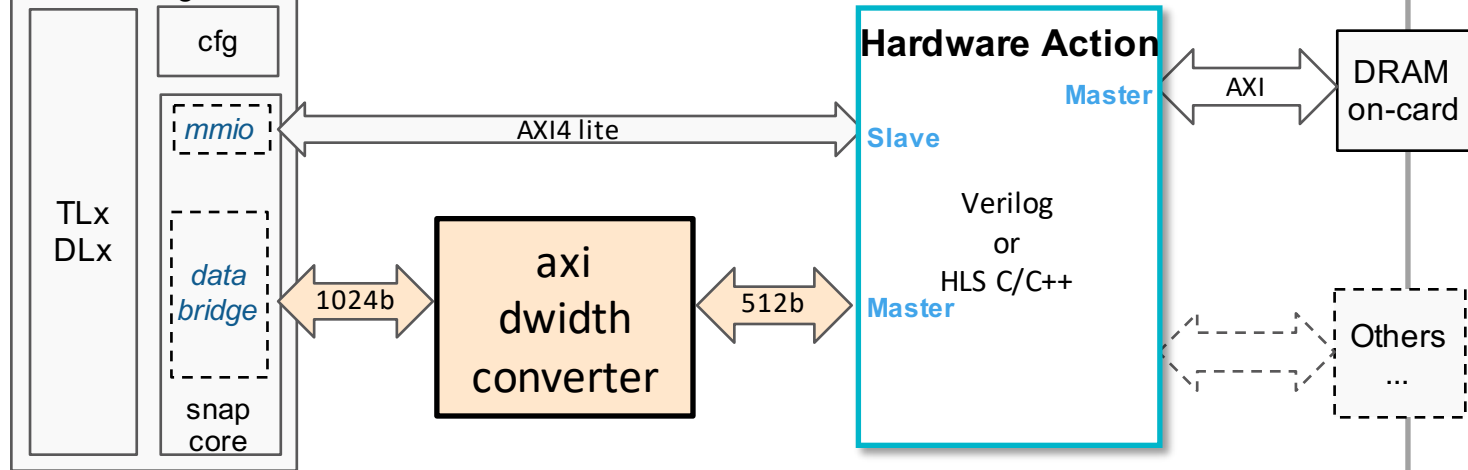


OC/AXI Bridge mode



Acceleration on FPGA