

This document is the exclusive property of Byte Lab d.o.o. Company and it should not be distributed or reproduced into any other format without prior permission from Byte Lab d.o.o.

### Module name

# BL-DK Core Module ESP32

## Change log

RELEASED

**DRAFT - Very early stage of schematic, ignore details.**

## PRELIMINARY - Close to the final schematic.

**CHECKED** - There should be no mistakes. Tell the engineer if you find one

**RELEASED - A board with this schematic has been sent to production.**

Index	Description
01	Cover
02	MCU

## DESIGN CONSIDERATIONS

**SOFTWARE CONFIG NOTE:**

- Contains important notes for software development.
- Things like: pin configuration, timing requirements, IC configuration etc.

**DESIGN NOTE:**  
- Hardware notes

-----  
Board support



FDI



FD2



FD3

PCB1

PCB

2200-827

LOGO

LG1

DOC1

HW

100212

DOC2

FW

1888

Title:  
**01\_COVER.SchDoc**

Project:	100212 BL-DK-core-module-esp32.PriPCB
----------	---------------------------------------

Company:	Medarska 69/1, 10000 Zagreb, Croatia
Byte Lab Grupa d.o.o.	<a href="http://www.byte-lab.com">www.byte-lab.com</a>

Author: M. Hamin

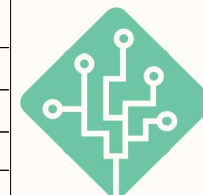
Acceptor:

C. Luetic

Sheet 1 of 2

	Rev: 12
--	---------

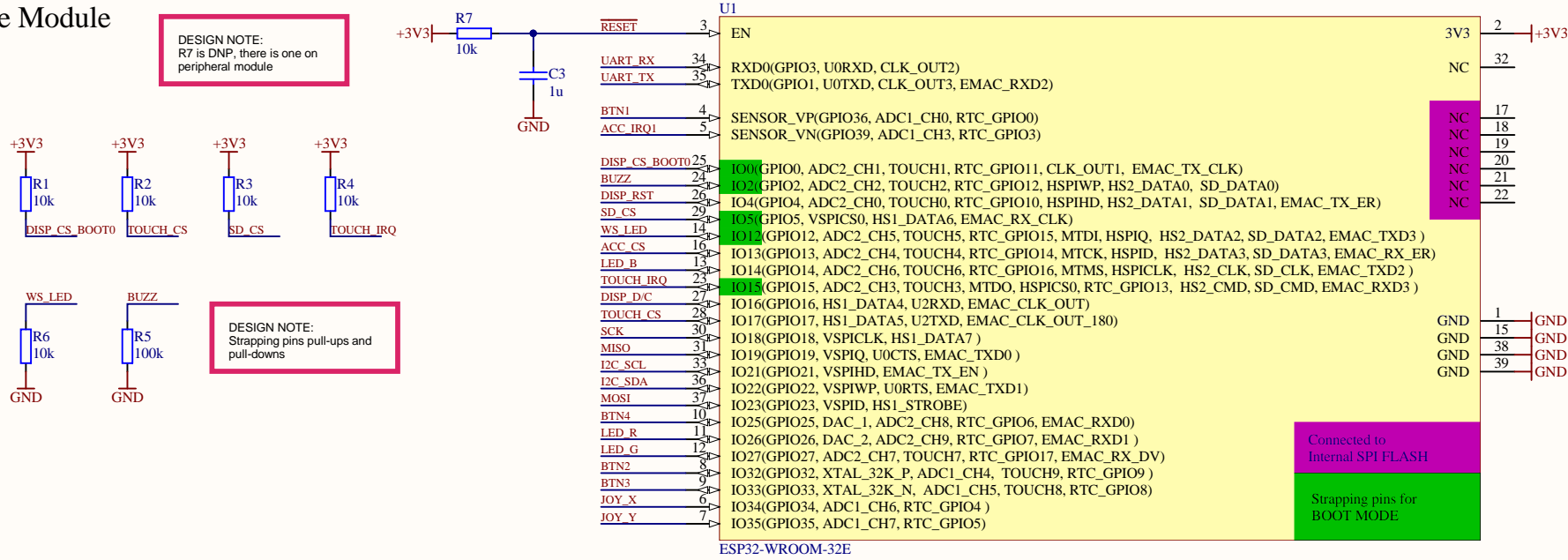
Format: A4
------------



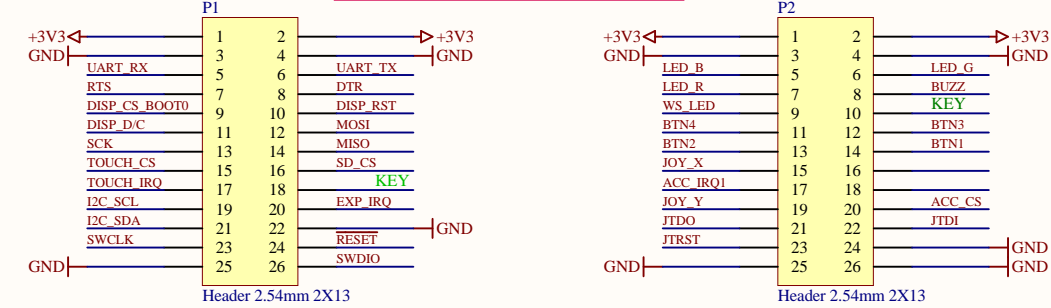
This document is the exclusive property of Byte Lab d.o.o. Company and it should not be distributed or reproduced into any other format without prior permission from Byte Lab d.o.o.

# ESP32 Core Module

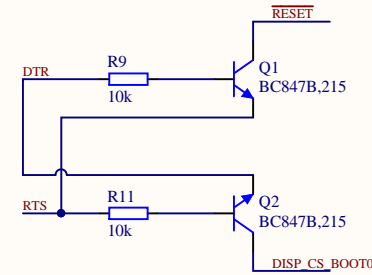
DESIGN NOTE:  
R7 is DNP, there is one on peripheral module



DESIGN NOTE:  
- P1 (pin 18) and P2 (pin 16) are meant to be used for keying.

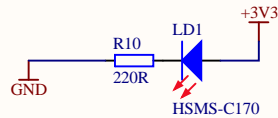


## Flashing circuit

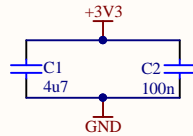


Auto program circuit			
DTR	RTS	RST	GPIO0
1	1	1	1
0	0	1	1
1	0	1	1
0	1	1	0

## Activation status LED



## Decoupling capacitors



Title:  
**02\_MCU.SchDoc**

Project:  
100212\_BL-DK-core-module-esp32.PrjPCB

Company:  
Byte Lab Grupa d.o.o.

Medarska 69/1, 10000 Zagreb, Croatia  
www.byte-lab.com

Author:  
M. Hamin

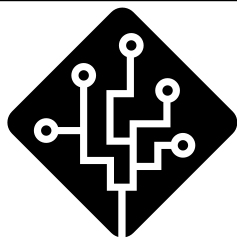
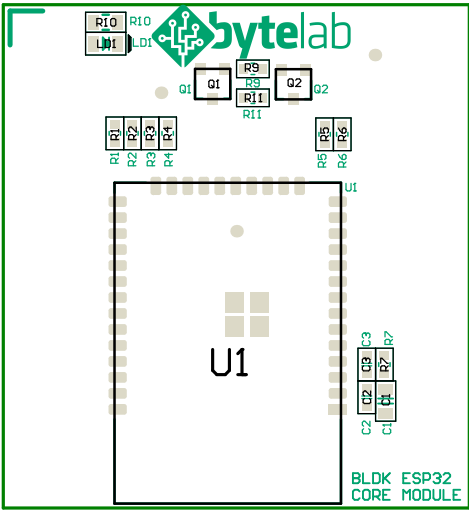
Acceptor:  
C. Lusetic

Sheet x of y

Rev: 12

Format: A4

This document is the exclusive property of Byte Lab Grupa d.o.o. Company and it should not be distributed or reproduced into any other format without prior permission from Byte Lab Grupa d.o.o.



Byte Lab Grupa d.o.o.  
Medarska 69/1  
10000 Zagreb  
Croatia

	SIGNATURE	TITLE:  <div>Assembly drawing designators - TOP</div>
AUTHOR:	M. Hamin	
ACCEPTOR:	J. Puskar	
		BOM NO.:  <div>3000-667-001</div>
DESIGN DOCUMENT NAME: <div>100212 BL-DK Core Module ESP32 Rev. 12</div>		FORMAT:  <div>A4</div>

