

X kon	pin MCU	funkce dle datasheetu	UART	I2C	SPI	CAN	TIMER	ADC	DAC
X00	PB1	TIM1_CH3N, TIM3_CH4, TIM8_CH3N, LCD_R6, OTG_HS_ULPI_D2, ETH_MII_RXD3, EVENTOUT, ADC12_IN8					TIM1_CH3,TIM3_CH4,TIM8_CH3	ADC12_IN8	
X01	PB0	TIM1_CH2N, TIM3_CH3, TIM8_CH2N, LCD_R3, OTG_HS_ULPI_D1, ETH_MII_RXD2, EVENTOUT, ADC12_IN9					TIM1_CH2,TIM3_CH3,TIM8_CH2	ADC12_IN9	
X02	PE9	TIM1_CH1, FMC_D6, EVENTOUT					TIM1_CH1	X	
X03	PB2	EVENTOUT						X	
X04	PA3	TIM2_CH4, TIM5_CH4, TIM9_CH2, USART2_RX, OTG_HS_ULPI_D0, ETH_MII_COL, LCD_B5, EVENTOUT, ADC123_IN3	USART2_RX				TIM2_CH4,TIM5_CH4,TIM9_CH2	ADC123_IN3	
X05	PA6	TIM1_BKIN, TIM3_CH1, TIM8_BKIN, SPI1_MISO, TIM13_CH1, DCMI_PIXCLK, LCD_G2, EVENTOUT, ADC12_IN6			SPI1_MISO		TIM1_BKIN,TIM3_CH1,TIM8_BKIN	ADC12_IN6	
X06	PB6	TIM4_CH1, I2C1_SCL, USART1_TX, CAN2_TX, FMC_SDNE1, DCMI_D5, EVENTOUT	USART1_TX	I2C1_SCL		CAN2_TX	TIM4_CH1	X	PH
X07	PB7	TIM4_CH2, I2C1_SDA, USART1_RX, FMC_NL, DCMI_VSYNC, EVENTOUT	USART1_RX	I2C1_SDA				X	
X08	PA15	JTDI, TIM2_CH1/TIM2_ETR, SPI1_NSS, SPI3_NSS/I2S3_WS, EVENTOUT			SPI1_NSS			X	
X09	PD9	USART3_RX, FMC_D14, EVENTOUT	USART3_RX					X	
X10	PB3	JTDO/TRACESWO, TIM2_CH2, SPI1_SCK, SPI3_SCK/I2S3_CK, EVENTOUT			SPI1_SCK			X	
X11	PD8	USART3_TX, FMC_D13, EVENTOUT	USART3_TX					X	
X12	PB4	NJTRST, TIM3_CH1, SPI1_MISO, SPI3_MISO, I2S3ext_SD, EVENTOUT			SPI1_MISO			X	
X13	PB9	TIM4_CH4, TIM11_CH1, I2C1_SDA, SPI2_NSS/I2S2_WS, CAN1_TX, SDIO_D5, DCMI_D7, LCD_B7, EVENTOUT		I2C1_SDA		CAN1_TX	TIM4_CH4, TIM11_CH1	X	
X14	PB5	TIM3_CH2, I2C1_SMBA, SPI1_MOSI, SPI3_MOSI/I2S3_SD, CAN2_RX, OTG_HS_ULPI_D7, ETH_PPS_OUT, FMC_SDCKE1, DCMI_D10, EVENTOUT			SPI1_MOSI	CAN2_RX	TIM3_CH2	X	
X15	PB8	TIM4_CH3, TIM10_CH1, I2C1_SCL, CAN1_RX, ETH_MII_TXD3, SDIO_D4, DCMI_D6, LCD_B6, EVENTOUT		I2C1_SCL		CAN1_RX	TIM4_CH3,TIM10_CH1	X	

primární tak to budeme držet na Shieldch
sekundární alternativní konfigurace pro uživatele

Současný procesor je z desky IODAG3E_170725 a je to STM32F437IIH6 (UFBGA176+25)

Y kon	pin MCU	funkce dle datasheetu	UART	USART	I2C	SPI	CAN	TIMER	ADC	DAC
Y00	PC10	SPI3_SCK/I2S3_CK, USART3_TX,USART4_TX,SDIO_D2, DCMI_D8,LCD_R2,EVENTOUT	UART4_TX	USART3_TX		SPI3_SCK			X	
Y01	PC11	I2S3ext_SD,SPI3_MISO, USART3_RX, UART4_RX,SDIO_D3,DCMI_D4,EVENTOUT	UART4_RX	USART3_RX		SPI3_MISO			X	
Y02	PD0	CAN1_RX,FMC_D2,EVENTOUT					CAN1_RX		X	
Y03	PC12	SPI3_MOSI/I2S3_SD,USART3_CK,UART5_TX,SDIO_CK,DCMI_D9,EVENTOUT	UART5_TX			SPI3_MOSI			X	
Y04	PD1	CAN1_TX,FMC_D3,EVENTOUT					CAN1_TX		X	
Y05	PI0	TIM5_CH4,SPI_NSS/I2S2_WS,FMC_D24,DCMI_D13, LCD_G5,EVENTOUT						TIM5_CH4	X	
Y06	PC9	MCO2, TIM3_CH4,TIM8_CH4, I2C3_SDA,I2S_CKIN,SDIO_D1,DCMI_D3,EVENTOUT			I2C3_SDA			TIM3_CH4,TIM8_CH4	X	
Y07	PA8	MCO1,TIM1_CH1,I2C3_SCL,USART1_CK,OTG_FS_SOF,LCD_R6,EVENTOUT			I2C3_SCL			TIM1_CH1	X	
Y08	PC8	TIM3_CH3, TIM8_CH3,USART6_CK,SDIO_D0,DCMI_D2,EVENT OUT		USART6_CK				TIM3_CH3, TIM8_CH3	X	
Y09	PC7	TIM3_CH2,TIM8_CH2,I2S3_MCK,USART6_RX,SDIO_D7,DCMI_D1,LCD_G6,EVENTOUT		USART6_RX				TIM3_CH2,TIM8_CH2	X	
Y10	PC6	TIM3_CH1,TIM8_CH1,I2S2_MCK,USART6_TX,SDIO_D6,DCMI_D0,LCD_HSYNC,EVENTOUT		USART6_TX				TIM3_CH1,TIM8_CH1	X	
Y11	PG8	SPI6_NSS,USART6_RTS,ETH_PPS_OUT,FMC_SDC LK,EVENTOUT		USART6_RTS		SPI6_NSS			X	
Y12	PH12	TIM5_CH3,FMC_D20,DCMI_D3,LCD_r6,EVENTOUT						TIM5_CH3	X	
Y13	PH11	TIM5_CH2,FMC_D19,DCMI_D2,LCD_R5,EVENTOUT						TIM5_CH2	X	

Y kon	pin MCU	funkce dle datasheetu	UART	USART	I2C	SPI	CAN	TIMER	ADC	DAC
Y14	PH10	TIM5_CH1,FMC_D18,DCMI_D1,LCD_R4,EVENTOUT						TIM5_CH1	X	
Y15	PG13	SPI6_CLK,USART6_CTS,ETH_MII_TXD0/ETH_RMII_TXD0,FMA_A24,EVENTOUT		USART6_CTS		SPI6_CLK			X	
Y16	PG14	SPI6_MOSI,USART6_TX,ETH_MII_TXD1/ETH_RMII_TXD1,FMC_A25,EVENTOUT		USART6_TX		SPI6_MOSI			X	
Y17	PG12	SPI6_MISO,USART6_RTS,LCD_B4,FMC_NE4,LCD_B1,EVENTOUT		USART6_RTS		SPI6_MISO			X	
Y18	PD2	TIM3_ETR,UART5_RX,SDIO_CMD,DCMI_D11,EVENTOUT	UART5_RX					TIM3_ETR	X	
Y19	PC0	OTG_HS_ULPI_STP,FMC_SDNWE,EVENTOUT							ADC123_IN10	
Y20	PH8	I2C3_SDA,FMC_D16,DCMI_HSYNC,LCD_R2,EVENT OUT			I2C3_SDA				X	
Y21	PH7	I2C3_SCL,SPI5_MISO,ETH_MII_RXD3,FMC_SDCKE1,DCMI_D9,EVENTOUT			I2C3_SCL				X	
Y22	PC3	SPI2_MOSI/I2S2_SD,OTG_HS_ULPI_NXT,ETH_MII_TX_CLK,FMCSDNE0,EVENTOUT							ADC123_IN13	
Y23	PA4	SPI_NSS,SPI3_NSS/I2S3_WS,USART2_CK,OTG_HS_SOF,DCMI_HSYNC,LCD_VSYNC,EVENTOUT				SPI_NSS, SPI3_NSS			ADC12_IN4	DAC_OUT1
Y24	PC2	SPI2_MISO,I2S2ext_SD,OTG_HS_ULPI_DIR,ETH_MII_TXD2,FMC_SDNE0,EVENT OUT							ADC123_IN12	DAC_OUT2
Y25	PA5	TIM2_CH1/TIM2_ETR,TIM8_CH1N,SPI1_SCK,OTGHS_ULPI_CK,EVENTOUT				SPI1_SCK		TIM2_CH1/TIM2_ETR ,TIM8_CH1N	ADC12_IN5	
Y26	PB10	TIM2_CH3,I2C2_SCL,SPI2_SCK/I2S2_CK,USART3_TX,OTG_HS_ULPI_D3,ETH_MII_RX_ER,LCD_G4,EVENTOUT		USART3_TX				TIM2_CH3,	X	