# CprE 381 – Computer Organization and

# Assembly-Level Programming

# Lab-03 Report

Student Name Colby McKinley

## Section / Lab Time Section 5 / W 6:10 – 8:00p

***Submit a typeset pdf version of this on Canvas by the due date (i.e., the start of your next lab section). Refer to the highlighted language in the Lab-03 instructions for the context of the following questions****.*

1. [Prelab] At the end of Chapter 5, answer question 5. At the end of Chapter 7, answer exercise 2.

See /Prelab3.pdf

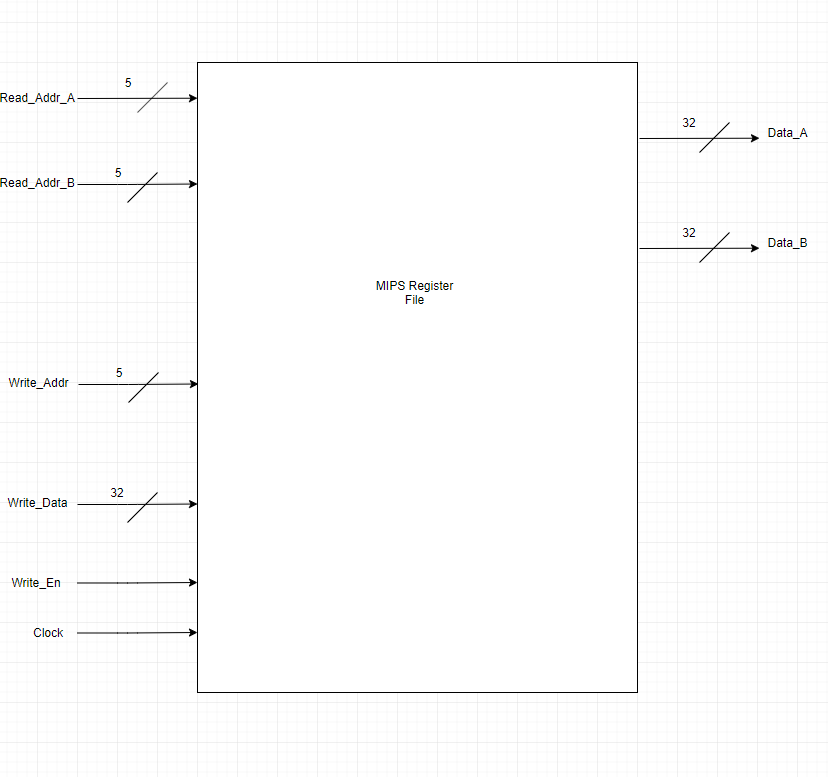
1. [Prelab] In your Lab #3 report PDF, provide the Canvas group name for your project team, and a listing of its members. On a scale of 1-10, how comfortable with VHDL does each team member currently feel?

Group 4

Colby – 5

Tyler – 7

1. [Part 1 (a)] Draw the interface description for the MIPS register file. Which ports do you think are necessary, and how wide (in bits) do they need to be?



1. [Part 1 (b)] Create an N-bit register using this flip-flop as your basis.
2. [Part 1 (c)] Waveform.
3. [Part 1 (d)] What type of decoder would be required by the MIPS register file and why?

In MIPS since there are 32 available registers, a 5 to 32 decoder is required.

1. [Part 1 (e)] Waveform.
2. [Part 1 (f)] In your write-up, describe and defend the design you intend on implementing for the next part.

I will use each read address to as a select line into a mux to indexing into one of 32 available registers.

1. [Part 1 (g)] Waveform.



1. [Part 1 (h)] Draw a (simplified) schematic for the MIPS register file, using the same top-level interface ports as in your solution for part a), and using only the VHDL components you have created in parts (b), (e), and (g).
2. [Part 1 (i)] Waveform.
3. [Part 2 (b)] Draw a schematic of the simplified MIPS processor datapath consisting only of the component described in part (a) and the register file from problem (1).
4. [Part 2 (c)] Include in your report waveform screenshots that demonstrate your properly functioning design.
5. [Feedback] You must complete this section for your lab to be graded. Write down the first response you think of; I expect it to take roughly 5 minutes (do not take more than 10 minutes).
   1. How many hours did you spend on this lab?

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| --- | --- | --- |
| **Task** | **During lab time** | **Outside of lab time** |
| Reading lab |  |  |
| Pencil/paper design |  |  |
| VHDL design |  |  |
| Assembly coding |  |  |
| Simulation |  |  |
| Debugging |  |  |
| Report writing |  |  |
| Other: |  |  |
| Total |  |  |

* 1. If you could change one thing about the lab experience, what would it be? Why?
  2. What was the most interesting part of the lab?