

# 1 Multiplying numbers with *MUL* and *IMUL*

- **MUL** → Multiplies unsigned numbers.
- **IMUL** → Multiplies signed numbers.

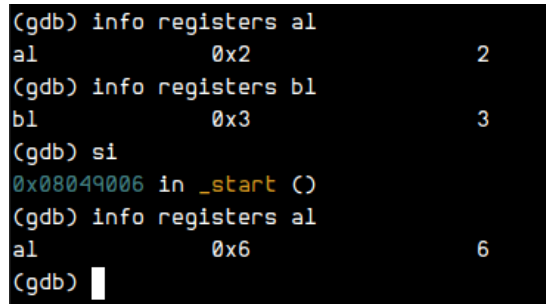
## 1.1 Using the **MUL** instruction

Assembly program to multiply two numbers using **MUL**:

```
1 section .data
2
3 section .text
4     global _start
5     _start:
6         MOV al, 2
7         MOV bl, 3
8         MUL bl
9
10        MOV eax, 1
11        INT 80h
```

The **MUL** instruction requires only one register. The reason is that the register **eax** is automatically used by multiplication and division instructions meaning that we don't need to specify it explicitly.

Here is an image showing the value in **al** before and after multiplication:



The screenshot shows a GDB session with the following commands and output:

```
(gdb) info registers al
al            0x2            2
(gdb) info registers bl
bl            0x3            3
(gdb) si
0x00049006 in _start ()
(gdb) info registers al
al            0x6            6
(gdb)
```

Figure 1: Values of **al**

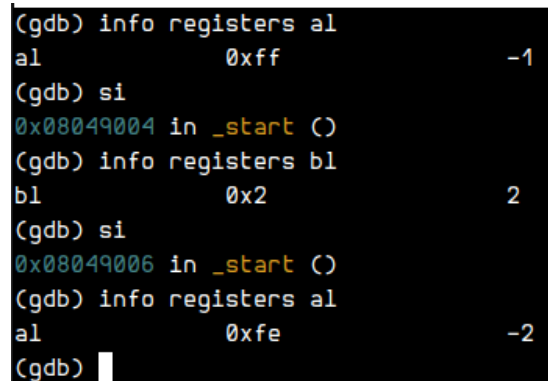
What happens if the result of the multiplication is larger than the register?

**Program:**

```
1 section .data
2
3 section .text
4     global _start
5     _start:
6         MOV al, 0xFF
7         MOV bl, 2
8         MUL bl
9
10        MOV eax, 1
11        INT 80h
```

**0xFF** in 8-bit representation is  $-1$  and in 16-bit representation is 255. Now, we store this value in the register **al**.

When the multiplication operations is performed then we get the following result:



```
(gdb) info registers al
al                0xff                -1
(gdb) si
0x08049004 in _start ()
(gdb) info registers bl
bl                0x2                 2
(gdb) si
0x08049006 in _start ()
(gdb) info registers al
al                0xfe                -2
(gdb)
```

Figure 2: Negative numbers

So after performing multiplication we see that  $-2$  is stored in **al**. We know that  $-2$  can be represented in 8-bit binary as 11111110.

Now, if we check what is stored in the higher-bit register **ah**, then we will get the following:

```
(gdb) info registers ah
ah          0x1          1
(gdb) info registers ax
ax          0x1fe        510
(gdb)
```

Figure 3: Value in **ah** and 16-bit representation of  $-2$

Now, after that we check what is the value stored in 16-bits that is register **ax**. We get 510.

Now the binary representation of 510 in 16-bits is:

00000001 11111110

The lower 8-bits are in **al** and higher 8-bits are in the register **ah**.

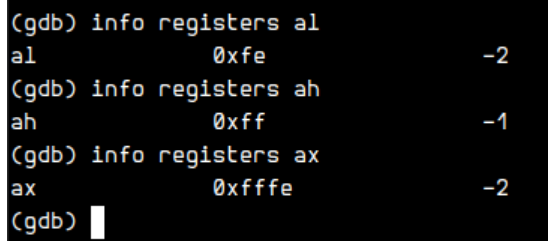
## 1.2 Using the IMUL instruction

Program:

```
1 section .data
2
3 section .text
4     global _start
5
6     _start:
7         MOV al, 0xFF
8         MOV bl, 2
9         IMUL bl
10
11         MOV eax, 1
12         INT 80h
```

When we multiply two  $n$ -bit numbers, the product is  $2 * n$ -bits long. Keeping that in mind, when 2 is multiplied to  $-1$  then we get the product as  $-2$ . This product is stored in the register **al** and also in **ah** because the product is 16(i.e  $2 * 8$ )-bits long.

This is shown in the image below:



```
(gdb) info registers al
al            0xfe            -2
(gdb) info registers ah
ah            0xff            -1
(gdb) info registers ax
ax            0xffff          -2
(gdb)
```

Figure 4: Registers: **al**, **ah** and **ax**

Basically this is what is stored in **ax**:

11111111 11111110

The lower 8-bits are stored in **al** and the higher 8-bits are stored in **ah**.