

16.369MHz TCXO
drifts -180Hz @ -50°C
then -350Hz @ -60°C

3V3
IC1
4
VCC
C2
1µF
2
GND
OUT
3
16_TCXO
1
NC
1k
TG-5006CJ

AX5243 Radio 0

434MHz, +16dBm TX, RX

VDD 2.0-3.6V

IC3

16 VDD_IO

17 VDD_ANA

18 GND

19 CLKp_0

20 CLKn_0

9 SYSCLK

10 SEL

11 SCLK

12 MISO

13 MOSI

14 IRQ

15 TXEN

17 GPADC1

18 GPADC2

AX5243

AX_SYSCLK_0

AX_SEL_0

AX_SCLK_0

AX_MISO_0

AX_MOSI_0

AX_IRQ_0

ANTp_0

ANTn_0

50 Ohm

434MHz, +16dBm TX, RX

Pin 1 connection diagram for the P1 header. The diagram shows a 40-pin header with pins 1 through 39. Pins 1, 3, 5, 7, 9, 11, 13, 15, 17, 19, 21, 23, 25, 27, 29, 31, 33, 35, 37, and 39 are connected to various functions. Pins 2, 4, 6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30, 32, 34, 36, 38, and 40 are connected to other functions. The diagram includes labels for VDD, GND, and various peripheral functions like LAN_LED, DATA_LED_0, DATA_LED_1, AX_MOSI, AX_MISO, AX_SCLK, AX_SY, AX_IRQ, AX_SE, and AX_IR.

Pin	Function
1	VDD_(3.3V)
3	GPIO2/I2C1_SDA
5	GPIO3/I2C1_SCL
7	GPIO4/GPCLK0
9	GND
11	GPIO17/SPI1_CЕ1
13	GPIO27
15	GPIO22
17	VDD_(3.3V)
19	GPIO10/SPI0_MOSI
21	GPIO9/SPI0_MISO
23	GPIO11/SPI0_SCLK
25	GND
27	GPIO0/ID_SD
29	GPIO5
31	GPIO6
33	GPIO13/PWM1
35	GPIO19/SPI1_MISO
37	GPIO26
39	GND
2	VPP_(5v)
4	VPP_(5v)
6	GND
8	GPIO14/UART_TXD
10	GPIO15/UART_RXD
12	GPIO18/SPI1_CЕ0
14	GND
16	GPIO23
18	GPIO24
20	GND
22	GPIO25
24	GPIO8/SPI0_CЕ0
26	GPIO7/SPI0_CЕ1
28	GPIO1/ID_SC
30	GND
32	GPIO12/PWM0
34	GND
36	GPIO16/SPI1_CЕ2
38	GPIO20/SPI1_MOSI
40	GPIO21/SPI1_SCLK

The diagram shows the LED module circuit. It consists of four LEDs: DATA_LED_1, DATA_LED_0, INTERNET_LED, and LAN_LED. Each LED is connected to a 330Ω resistor (R3, R4, R5, R6). The LEDs are labeled D3, D4, D5, and D6. The circuit is powered by GND and a 5V supply.

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