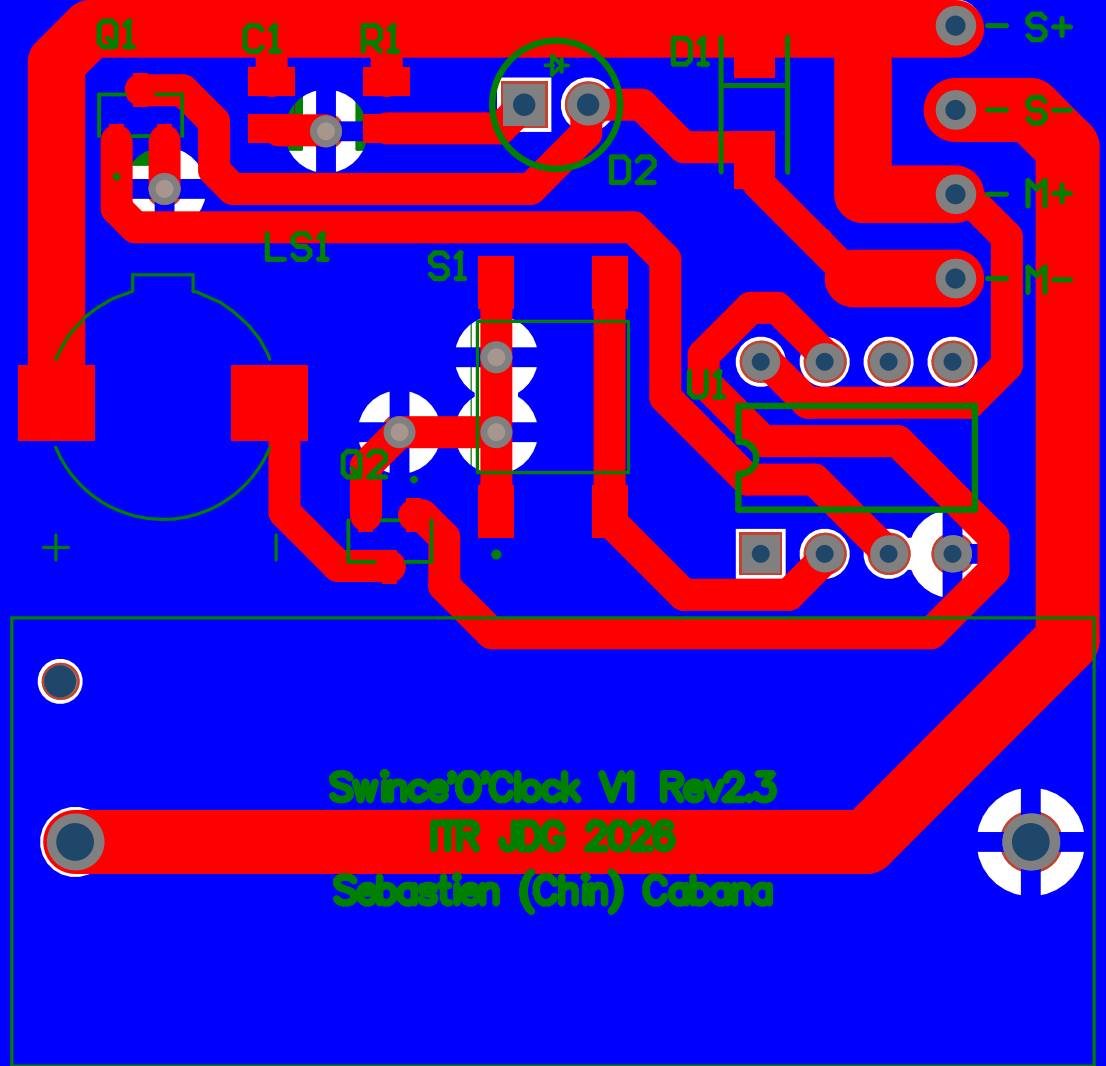
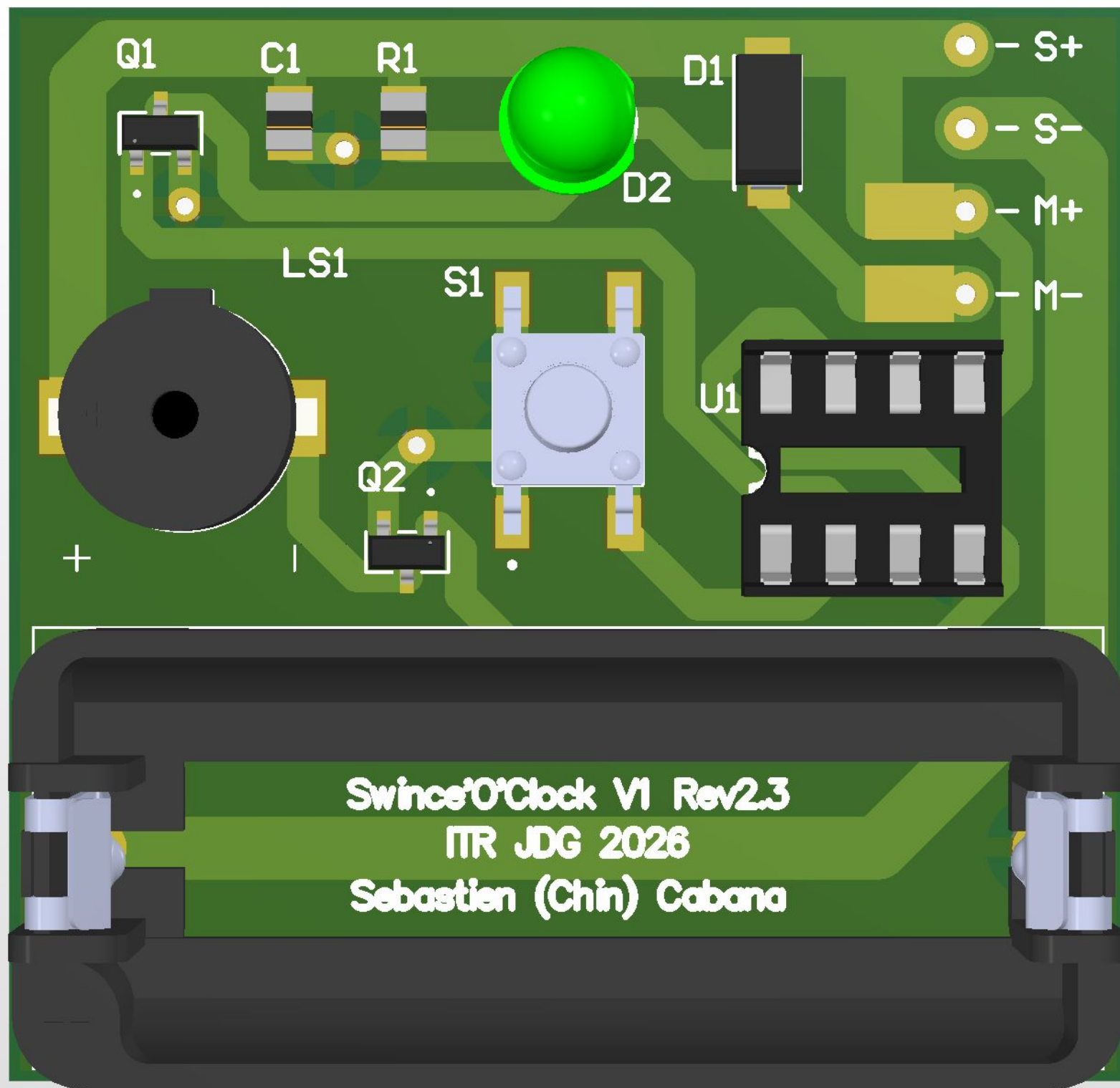
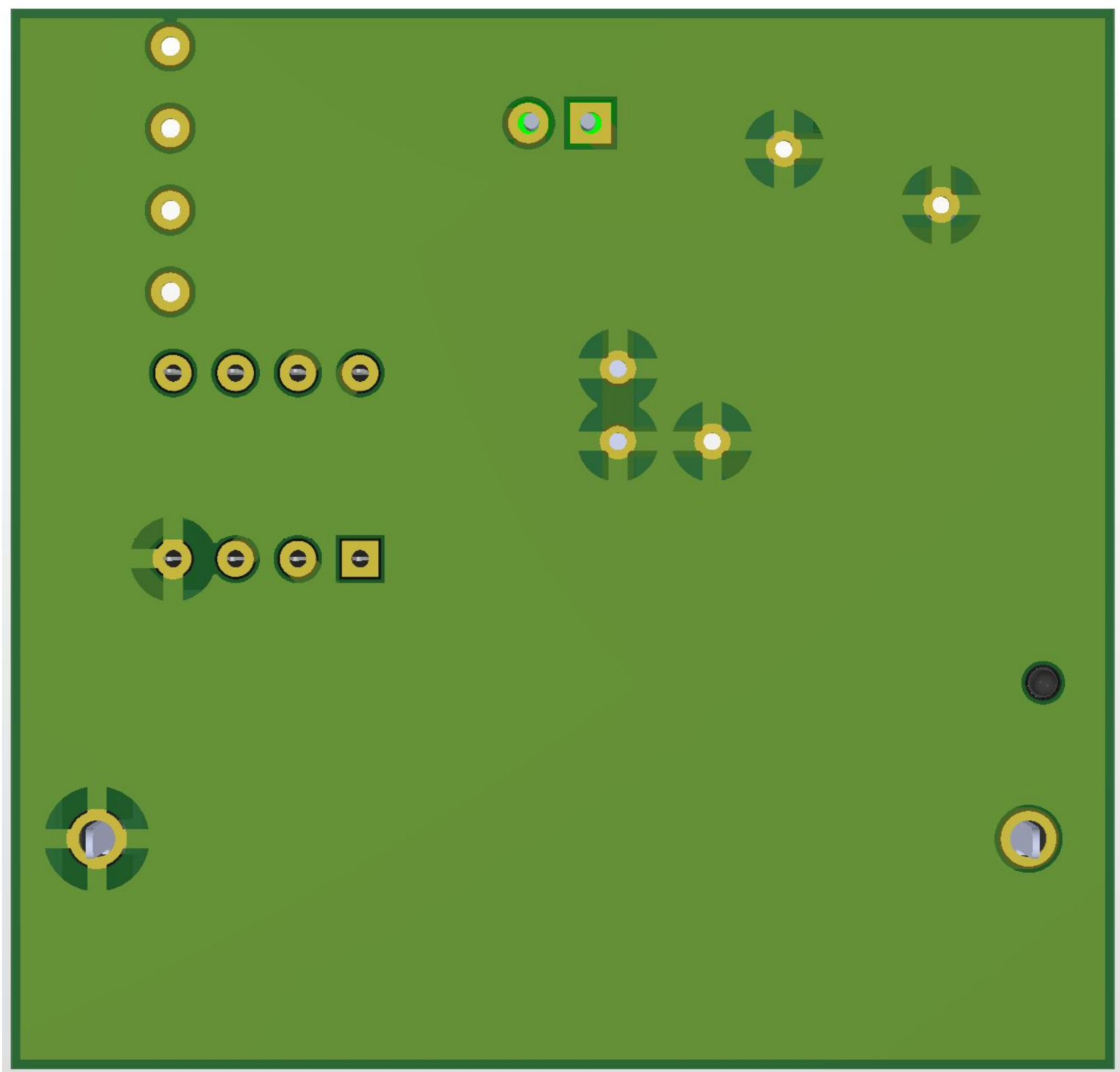


Title		
Swince'O'Clock - Random alarm based on ATtiny85V		
Size	Number	Revision
Letter	ITR JDG 2026	2.3
Date:	11-25-2025	Sheet 2 of 2
File:	C:\Users\...\final-v1.SchDoc	Drawn By: Sebastien Cabana







DigiKey Part Number	Comment	Description	Designator	Value	Quantity	Footprint	MountingType
BH123A-ND	BH123A	Battery Holder (Open) CR123A 1 Cell PC Pin	BT1		1	BAT_BH123A	Through Hole
	Cap-SM	Capacitor - Surface Mount standard pads	C1	100nF	1	SMD-0805	Surface Mount
GF1JFSTR-ND	GF1J	Diode 600 V 1A Surface Mount DO-214AC (SMA)	D1		1	DIOM5426X300N	Surface Mount
	LED	Typical LED	D2		1	LED_GREEN	Through Hole
923345-05-C	Jumper-EXT-22AWG	1 Point Jumper Wire 22 AWG; For External Connections	J1, J2, J3, J4		4	Jumper-EXT-22AWG	Through Hole
668-SMTB-0927-TW-RCT-ND	SMTB-0927-TW-R	Buzzers Transducer, Externally Driven Electromechanical/Magnetic 3 V 80mA 2.7kHz 95dB @ 3V	LS1		1	SMTB-0927-TW-R	Surface Mount
668-HD-EMB1205-2-LW30-R-ND	HD-EMB1205-2-LW30-R	DC Motor Vibration, ERM (Haptic) 13000 RPM 3VDC	M1		1	HD-EMB1205-2-LW30-R	Through Hole
BSS138LT1GOSCT-ND	BSS138LT1G	N-Channel 50 V 200mA (Ta) 225mW (Ta) Surface Mount SOT-23-3 (TO-236)	Q1, Q2		2	SOT-23-3	Surface Mount
	Res-SM	Resistor - Surface Mount standard pads	R1	100	1	SMD-0805	Surface Mount
2223-TS04-66-70-BK-260-SMT-ND	Pushbutton	6 x 6 mm, 7 mm Actuator Height, 260 gf, Black, Surface Mount, SPST, Tactile Switch	S1		1	SW_TS04-66-70-BK-260-SMT	Surface Mount
2223-SLW-195557-5A-N-D-ND	Switch	Slide Switch SPDT Panel Mount	S2		1	SW_SLW-195557-5A-N-D	Panel Mount
AE9986-ND	DIP8	8 (2 x 4) Pos DIP, 0.3" (7.62mm) Row Spacing Socket Tin Through Hole	U1		1	DIP-8-Socket	Through Hole

Design Rules Verification Report

Filename : C:\Users\sebas\OneDrive - Université du Québec à Trois-Rivières\Implication\JDG\2026\Swince'O'Clock\Swince'O'Clock\final-v1.PcbDo

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=10mil) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=10mil) (Max=200mil) (Preferred=35mil) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Acute Angle Constraint (Minimum=30.000) (All)	0
Hole Size Constraint (Min=15.748mil) (Max=314.961mil) (All AND ((HoleSize >= 2) AND (HoleSize <= 3) OR (HoleSize	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=10mil) (All),(All)	0
Silk To Solder Mask (Clearance=10mil) (Disabled)(IsPad),(All)	0
Silk to Silk (Clearance=10mil) (Disabled)(All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Board Clearance Constraint (Gap=0mil) (All)	0
RoomDefinition (Bounding Region = (0mil, 0mil, 2480.315mil, 2480.315mil) (False)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Total	0