Evaluation Flow

Iccad2015_evaluate.pl

Check_overlap.py

Check if macro location is legal

↓

Check if all macros are defined in [circuit].fp

↓

Read macro blocks location in [circuit].fp

↓

Generate three files : 1). [circuit]Eval.aux, 2). [circuit]Eval.scl, 3). [circuit]Eval.pl

↓

Perform placement (NTUplace3)

↓

Perform global router (NCTUgr)

↓

evaluate_routability.pl

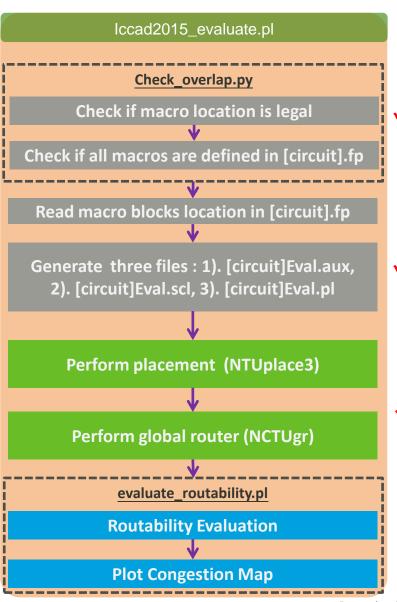**Routability Evaluation**

↓

Plot Congestion Map

✓**Stage 1 – Validation**
  ▪ Read macro location from **[circuit].fp**
  ▪ Calculate minimum spanning area & check for overlap
  ▪ Write **[circuit].pl** with new macro location

✓**Stage 2 – Evaluation : Perform academic place and route**
  ▪ Execute academic placer : NTUplace3 (approximately 30 min)
  ▪ Execute academic router : NCTUgr (less than 10 minutes)

✓**Stage 3 – Assessment : iccad2012_evaluate_solution.pl**
  ▪ Routability assessment in terms ACE
  ▪ Plot congestion map

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