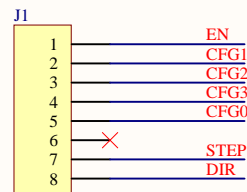
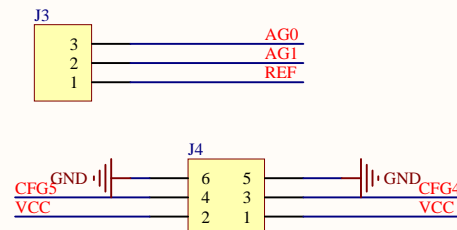


Enable the spreadcycle mode !



## Standalone Operation(SPI\_MODE=GND) STP/DIR MODE

### CFG6/EN

GND -> Driver enable

Vio -> Driver disable

Open-> Driver enable with ramp down from 100% to 34% after about 3s

### CFG2 CFG1 Steps Interpolation Chopper Mode

GND	GND	1	NO	spreadcycle
GND	Vio	2	NO	spreadcycle
GND	Open	2	Yes to 256	spreadcycle
Vio	GND	4	NO	spreadcycle
Vio	Vio	16	NO	spreadcycle
Vio	Open	4	Yes to 256	spreadcycle
Open	GND	16	Yes to 256	spreadcycle
Open	Vio	4	Yes to 256	stealthchop
Open	Open	16	Yes to 256	stealthchop

## Standalone Operation(SPI\_MODE=VIO) SPI MODE

### CFG4 ->GND

dcStep enable input (SPI\_MODE=1) - tie to GND for normal operation (no dcStep) or Configuration input (SPI\_MODE=0) (tristate detection).

### CFG5 ->VIO

dcStep gating input for axis synchronization (SPI\_MODE=1) or Configuration input (SPI\_MODE=0) (tristate detection).

Title			
TMC2130模块原理图			
Size	Number	Revision	
A4	V1.1		
Date:	2018/7/4	Sheet of	J.K
File:	C:\Users\...\2130模块原理图.SchDoc	Drawn By:	