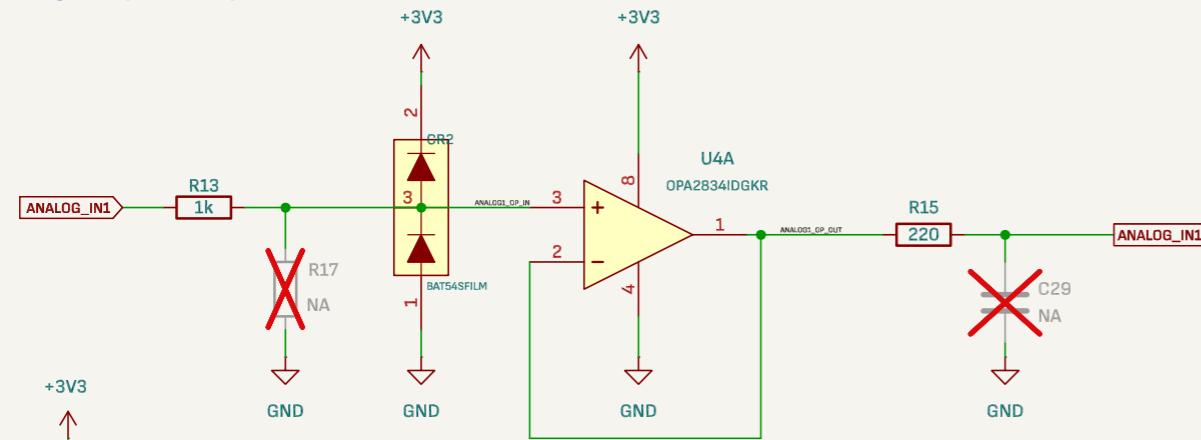


1 2 3 4 5 6

External Analog Inputs

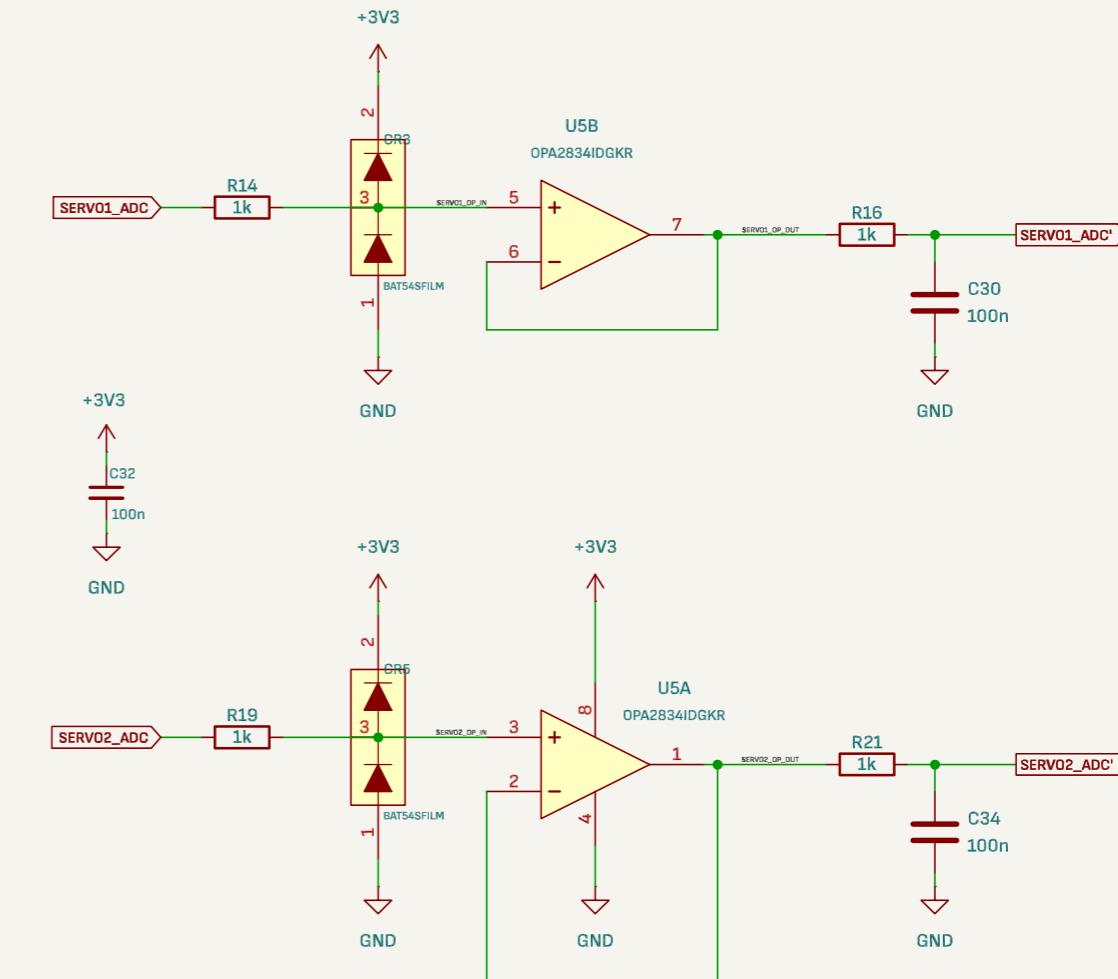
Analog Inputs will be buffered and clamped between 0V and 3.3V.
An additional resistor can be soldered to the input to form a voltage-divider.

The output capacitor can be chosen depending on the desired cut-off frequency.
It is given by: $f = 1 / (2\pi * R * C)$



Servos Analog

Low-Pass Cut-Off Frequency; ~1.5 kHz



LARS Hardware Interface
CAuDri e.V.

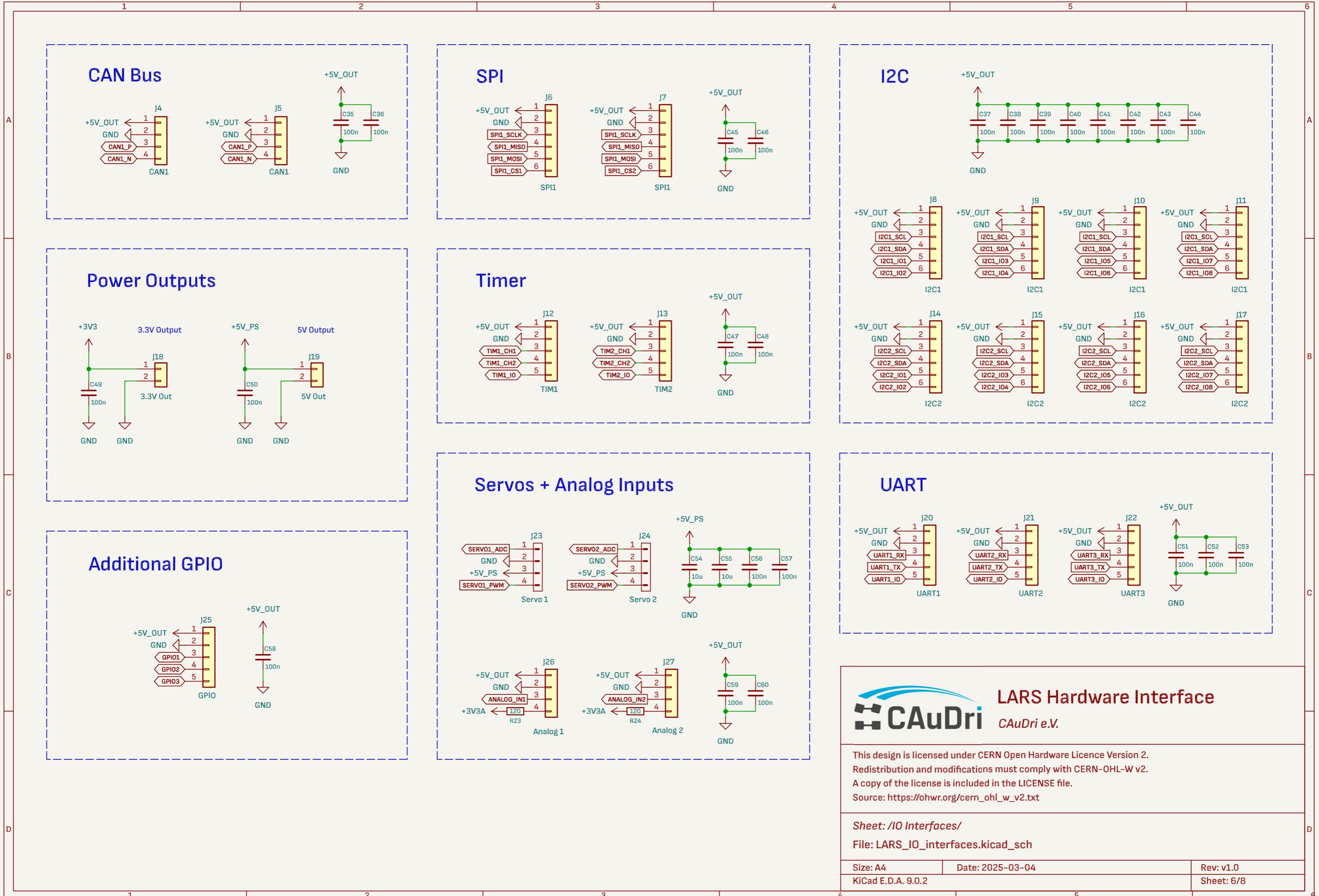
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Source: https://ohwr.org/cern_ohl_w_v2.txt

Sheet: /Analog/
File: LARS_Analog.kicad_sch

Size: A4 Date: 2025-03-04
KiCad E.D.A. 9.0.2

Rev: v1.0
Sheet: 5/8

1 2 3 4 5 6



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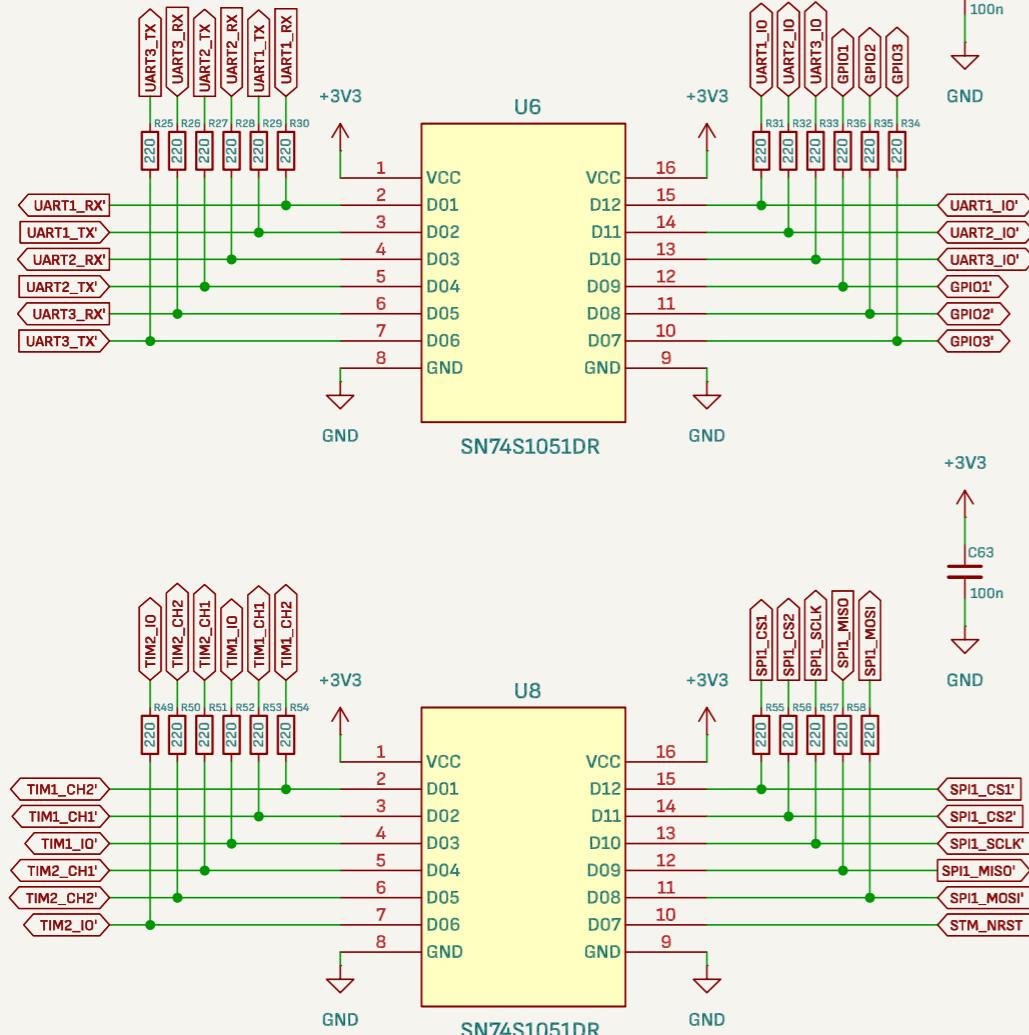
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Source: https://ohwr.org/cern_ohl_w_v2.txt

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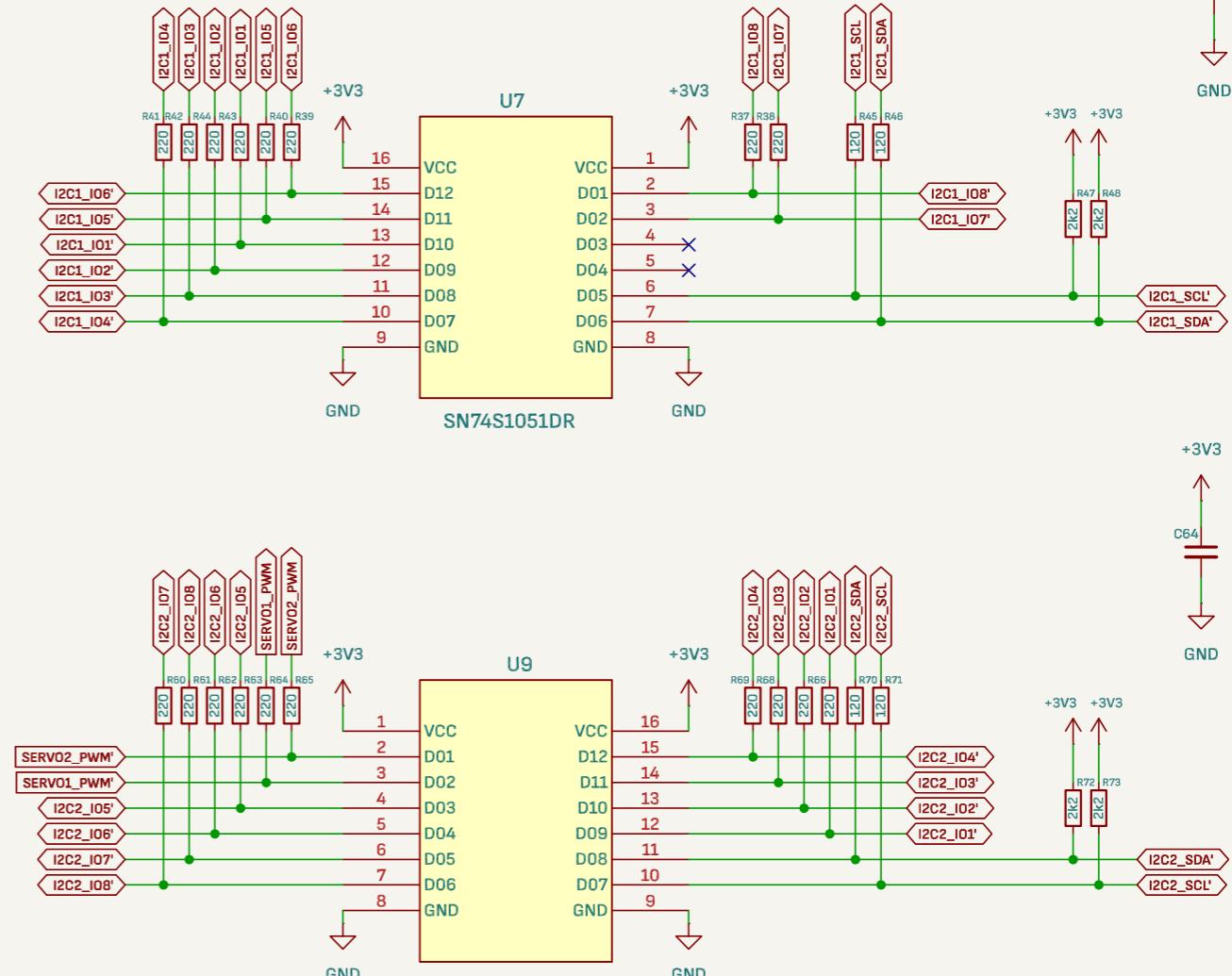
Size: A4 Date: 2025-03-04
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IO Protection



IO + I2C Protection



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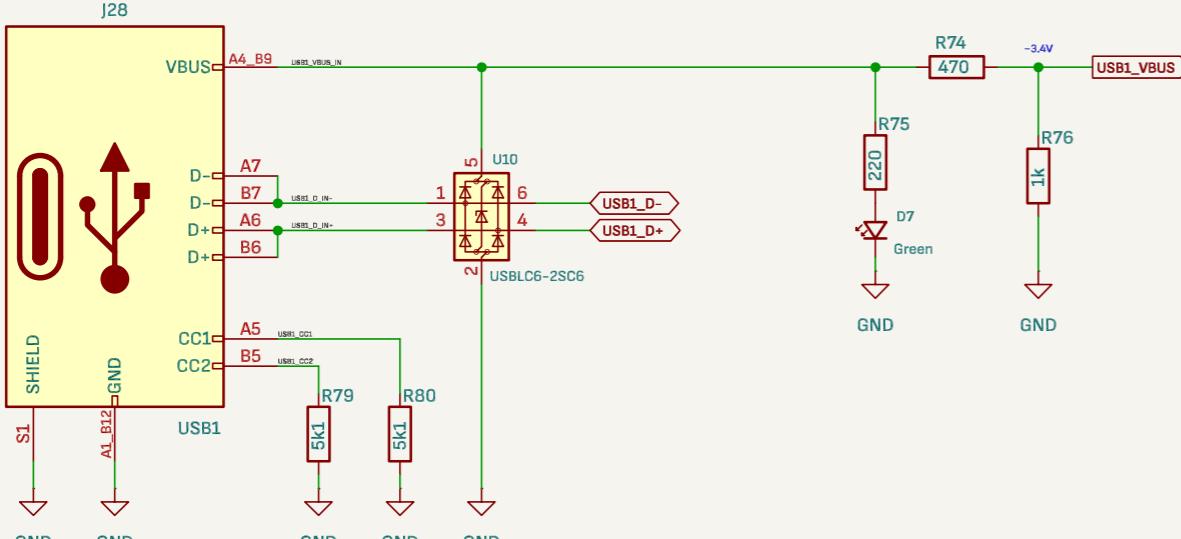
Size: A4 Date: 2025-03-04

KiCad E.D.A. 9.0.2

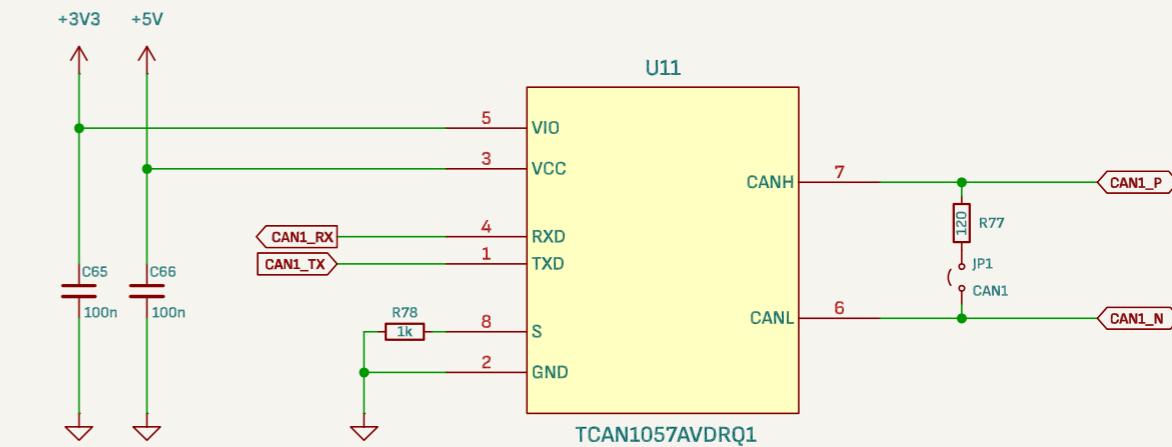
Rev: v1.0

Sheet: 7/8

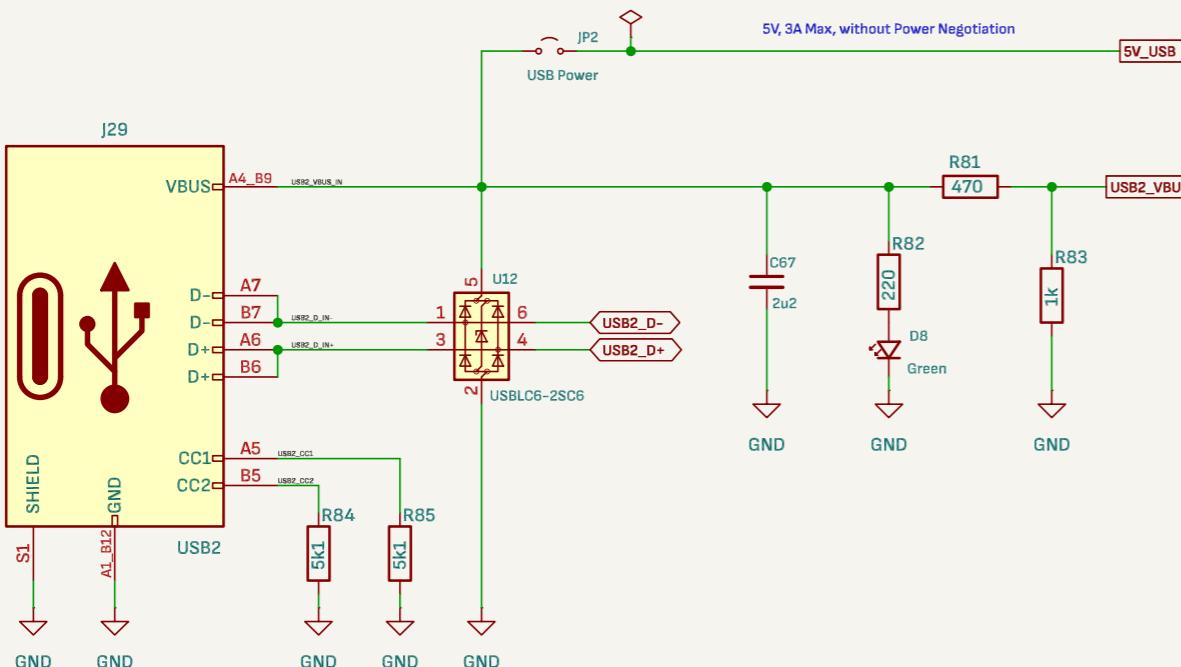
USB1 for Host Connection



CAN Transceiver



USB2 for Debugging and Power Supply



LARS Hardware Interface

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Sheet: /USB + CAN Bus/

File: LARS_Bus.kicad_sch

Size: A4 Date: 2025-03-04

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Rev: v1.0

Sheet: 8/8

