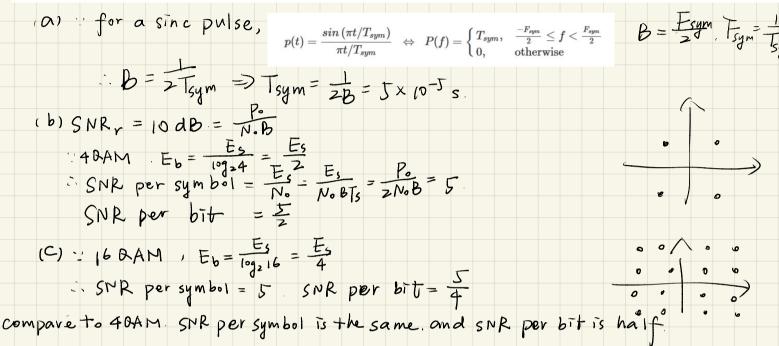
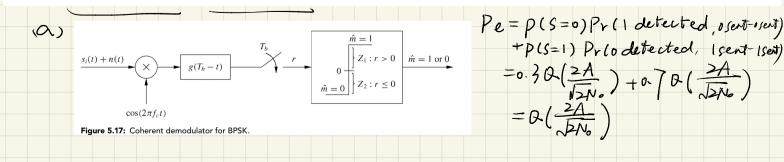
- **6-1.** Consider a system in which data is transferred at a rate of 100 bits per second over the channel.
 - (a) Find the symbol duration if we use a sinc pulse for signaling and the channel bandwidth is 10 kHz.
 - (b) Suppose the received SNR is 10 dB. Find the SNR per symbol and the SNR per bit if 4-QAM is used.
 - (c) Find the SNR per symbol and the SNR per bit for 16-QAM, and compare with these metrics for 4-QAM.



- **6-2.** Consider BPSK modulation where the a priori probability of 0 and 1 is not the same. Specifically, $p(s_n = 0) = 0.3$ and $p(s_n = 1) = 0.7$.
 - (a) Find the probability of bit error P_b in AWGN assuming we encode a 1 as $s_1(t) = A\cos(2\pi f_c t)$ and a 0 as $\widehat{s_2(t)} = -A\cos(2\pi f_c t)$ for A > 0, assuming the receiver structure is as shown in Figure 5.17.
 - (b) Suppose you can change the threshold value in the receiver of Figure 5.17. Find the threshold value that yields equal error probability regardless of which bit is transmitted that is, the threshold value that yields $p(\hat{m} = 0 \mid m = 1)p(m = 1) = p(\hat{m} = 1 \mid m = 0)p(m = 0)$.
 - (c) Now suppose we change the modulation so that $s_1(t) = A \cos(2\pi f_c t)$ and $s_2(t) = -B \cos(2\pi f_c t)$. Find A > 0 and B > 0 so that the receiver of Figure 5.17 with threshold at zero has $p(\hat{m} = 0 \mid m = 1)p(m = 1) = p(\hat{m} = 1 \mid m = 0)p(m = 0)$.
 - (d) Compute and compare the expression for P_b in parts (a), (b), and (c) assuming $E_b/N_0 = 10$ dB and $N_0 = .1$. For which system is P_b minimized?



(b)
$$P(\hat{m}=0|m=1) P(m=1) = P(\hat{m}=1|m=0) P(m=0)$$
 $P(\hat{m}=0|m=1) P(m=1) = P(\hat{m}=1|m=0) P(m=0)$
 $P(\hat{m}=0|m=0) P(\hat{m}=1|m=0) P(m=0)$
 $P(\hat{m}=0|m=0) P(m=0) P(m=0)$
 $P(\hat{m}=0) P(m=0) P(m=0)$
 $P(\hat{m}=0|m=0) P(m=$

6-3. Consider a BPSK receiver whose demodulator has a phase offset of ϕ relative to the transmitted signal, so for a transmitted signal $s(t) = \pm g(t) \cos(2\pi f_c t)$ the carrier in the demodulator of Figure 5.17 is $\cos(2\pi f_c t + \phi)$. Determine the threshold level in the threshold device of Figure 5.17 that minimizes probability of bit error, and find this minimum error probability.

$$\begin{array}{c|c}
\hline
S_i(t) + n(t) \\
\hline
S_i(t) + n(t)
\end{array}$$

$$\begin{array}{c|c}
\hline
M = 1 \\
\hline
0 \\
\hline
M = 0
\end{array}$$

$$\begin{array}{c|c}
\hat{m} = 1 \\
\hline
0 \\
\hline
Z_1 : r > 0
\end{array}$$

$$\hat{m} = 1 \text{ or } 0$$

$$\hat{m} = 0$$

$$\begin{array}{c|c}
\hline
Cos(2\pi f_c t)
\end{array}$$

Figure 5.17: Coherent demodulator for BPSK.

Sit)=
$$\pm g(t)$$
 as $(2\pi f \circ t)$: $A = 1$ ($(4\frac{1}{2})^{\frac{1}{2}}A > 0$)

Si = 1. Sz = -1

The phase offset ϕ . $r = r \cos \Delta \phi$.

Pe min = $O(\frac{d \min \cos \Delta \phi}{2N_0})$