

### Microwave Engineering (Lab)

### Lab 5: Design of Wilkinson Power Divider

Part 2

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**Tencent Meeting: 874-068-9694** 

设计指标:

频率: 0.9~1.1GHz

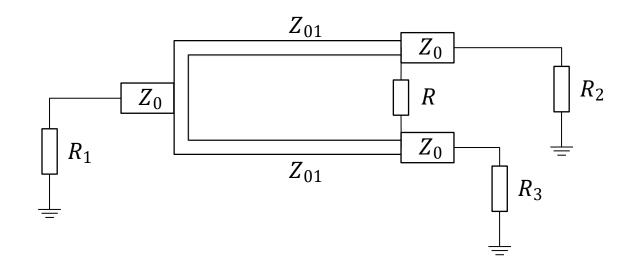
端口反射系数S11<-20dB, S22<-20dB

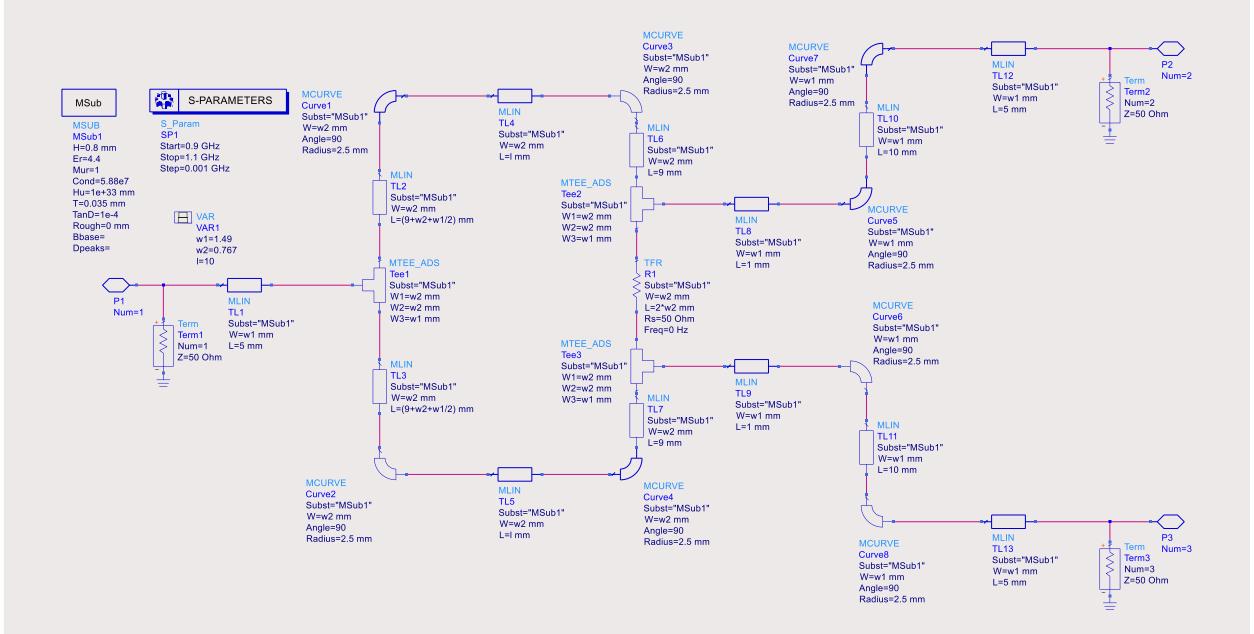
传输系数S21>-3.3dB

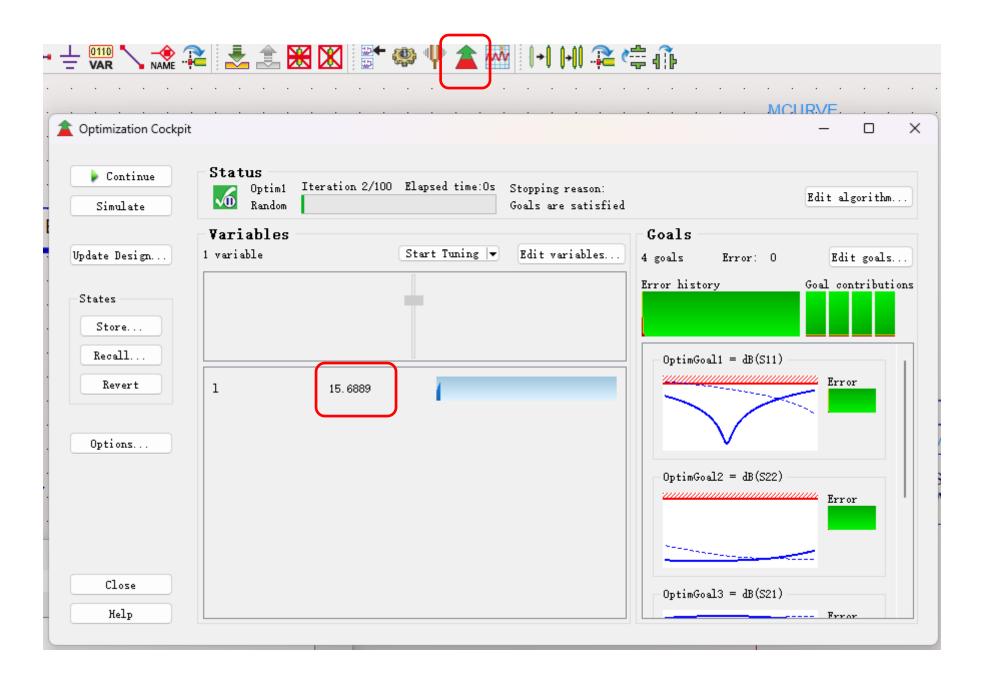
隔离度S32<-25dB

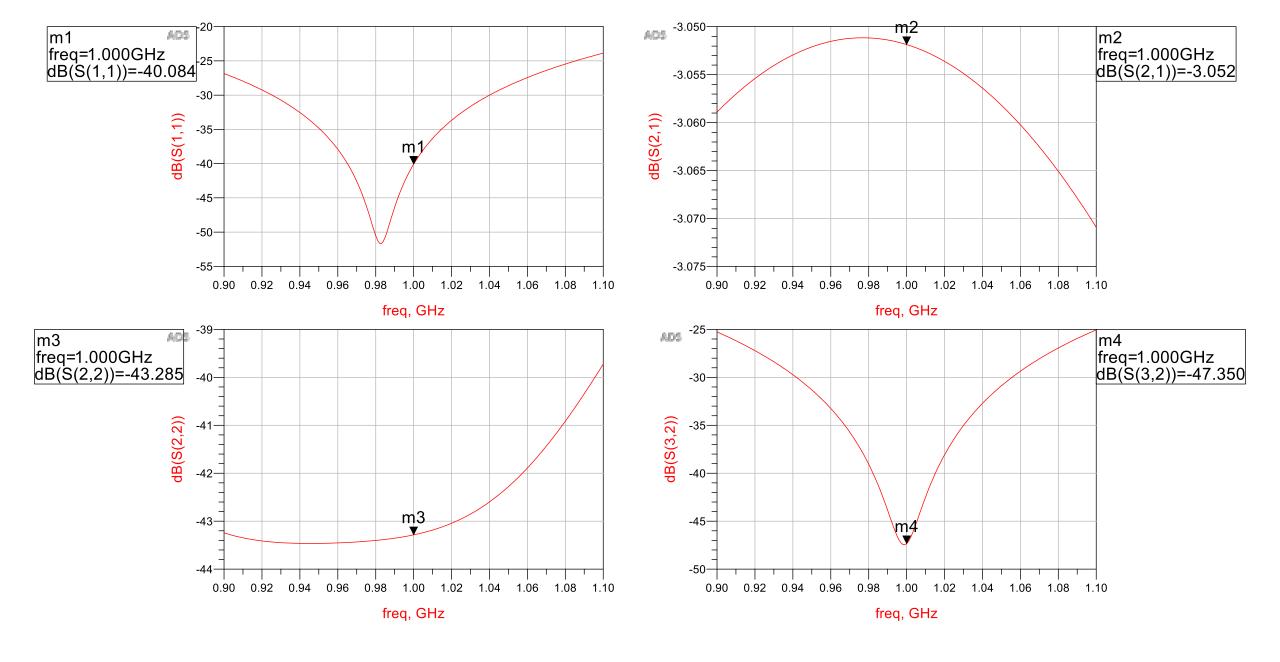
板材选择: FR4

厚度: 0.8mm

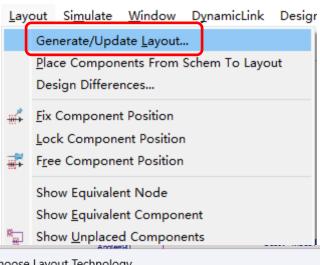


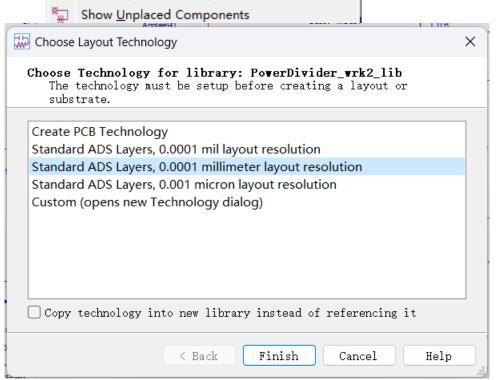


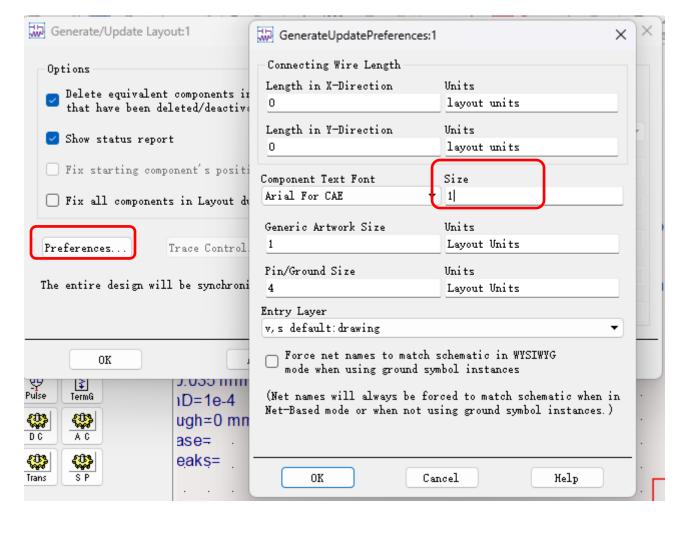


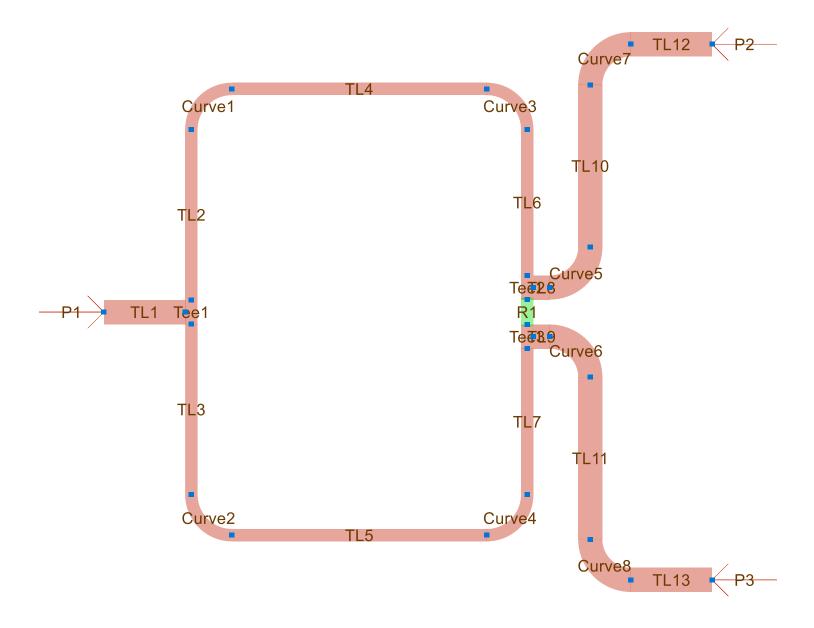


### **Generate Layout**







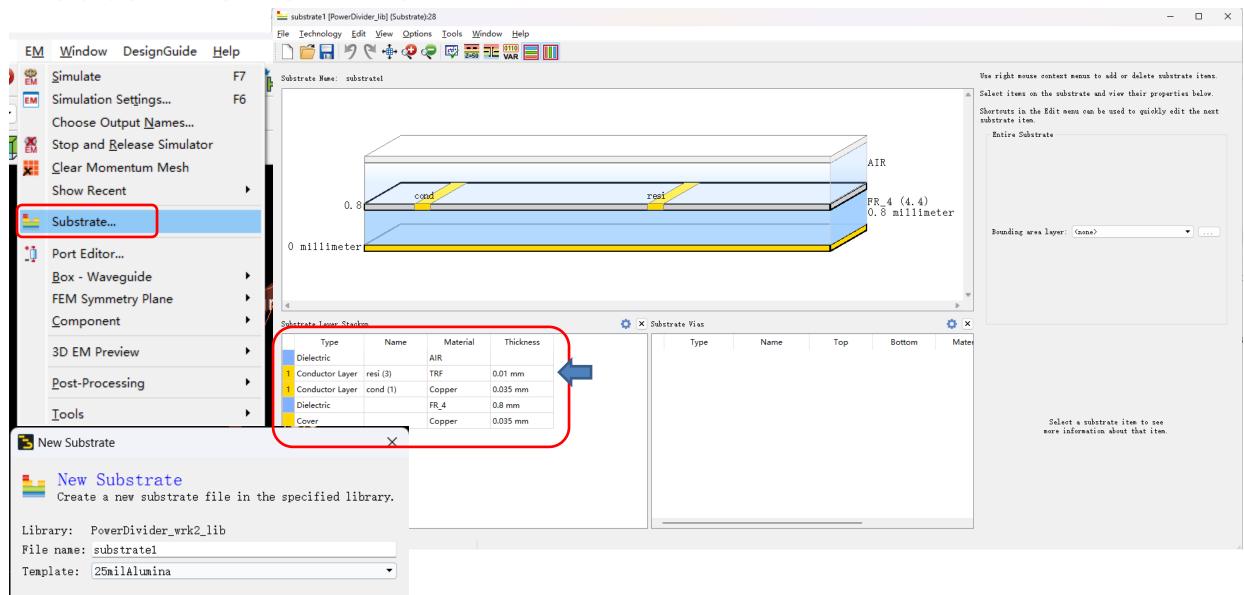


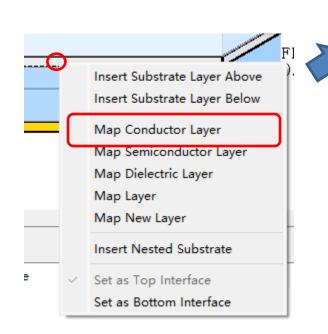
#### **Substrate Definition**

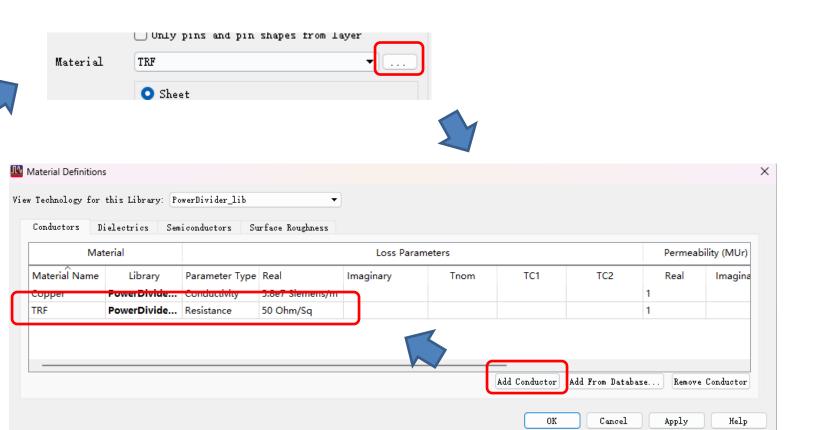
Create Substrate

Cancel

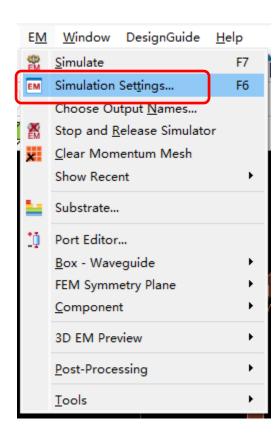
Help

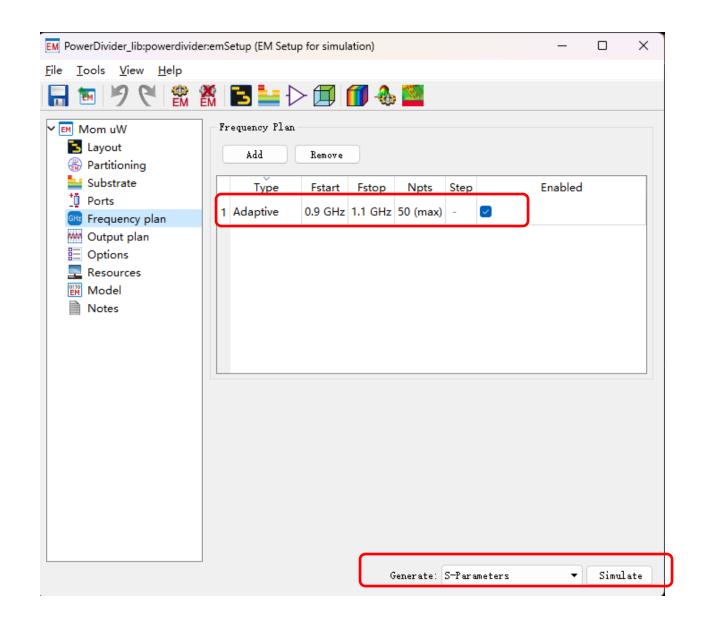


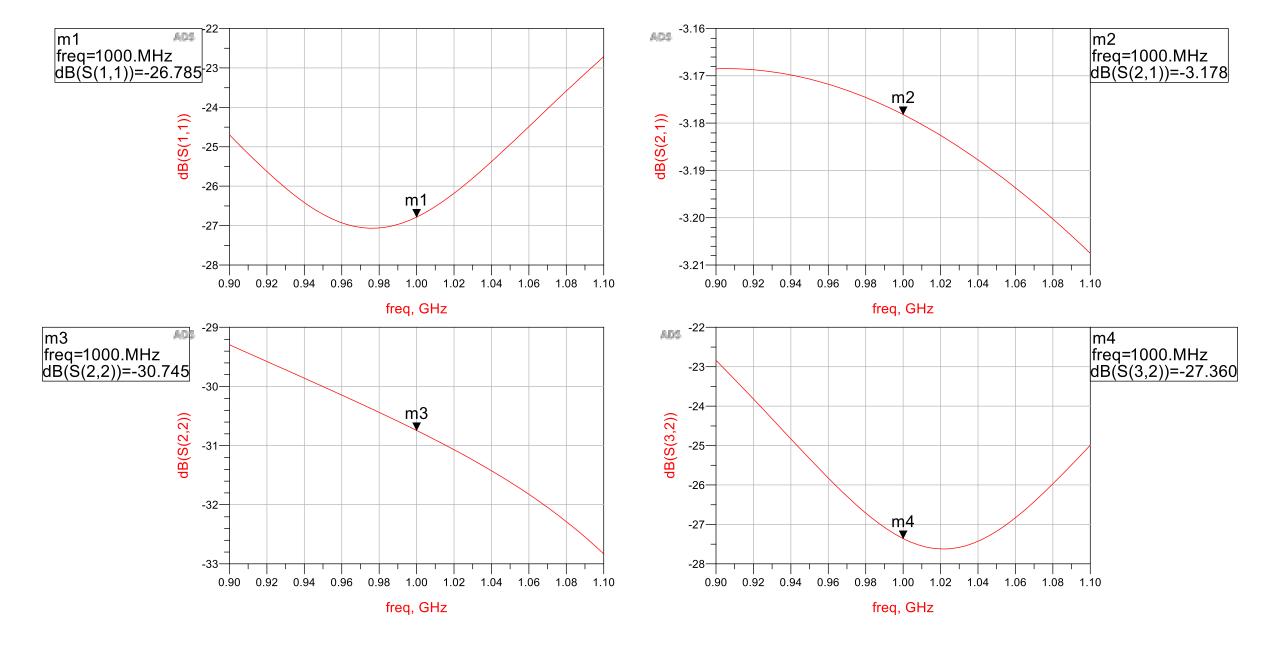




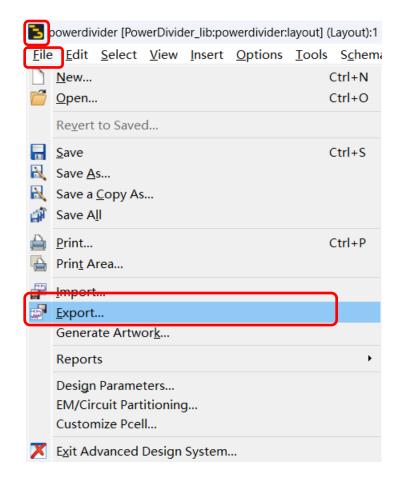
# **Layout Simulation**

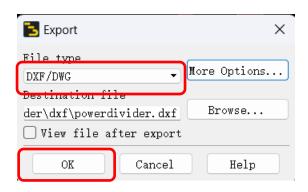




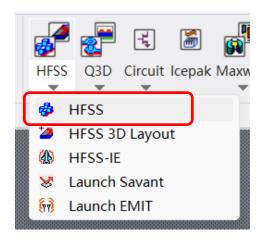


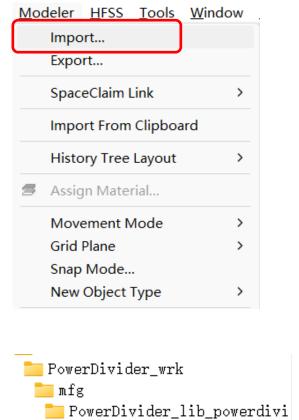
### **Export to HFSS**



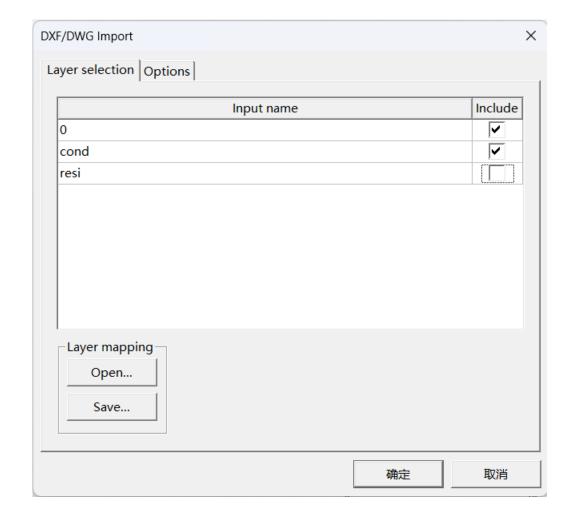


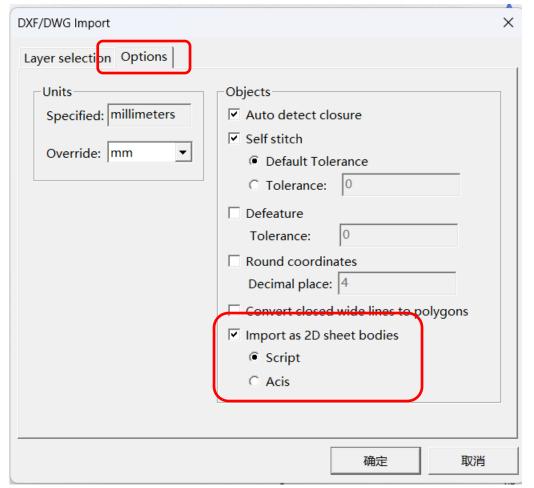
## In HFSS import model

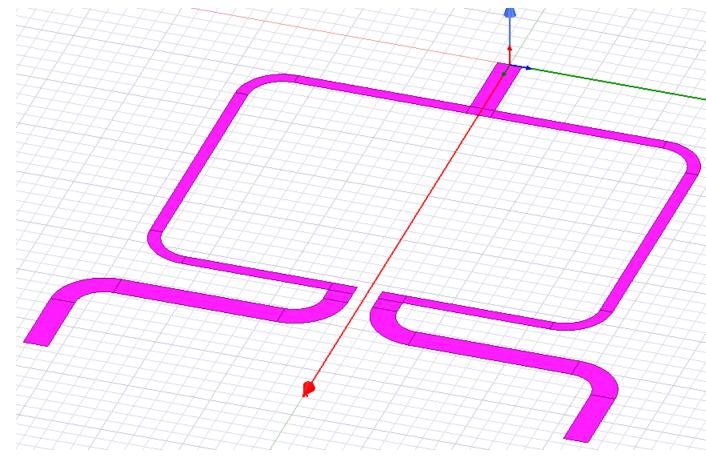




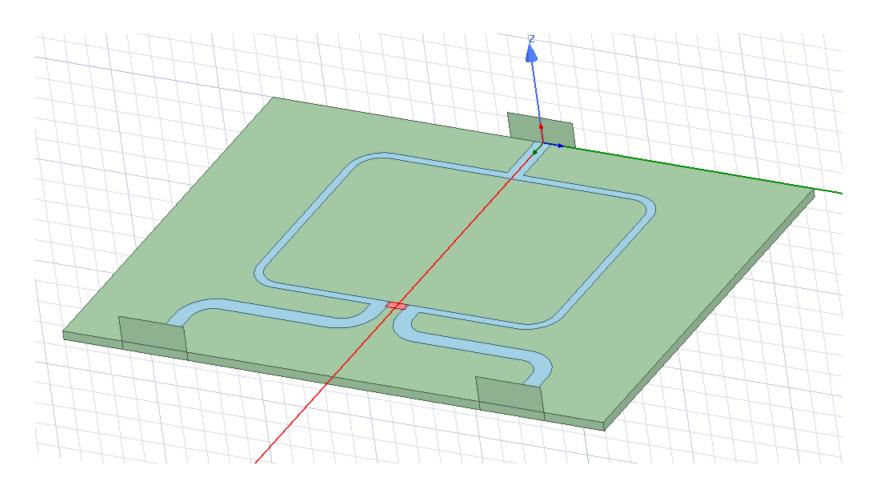
dxf

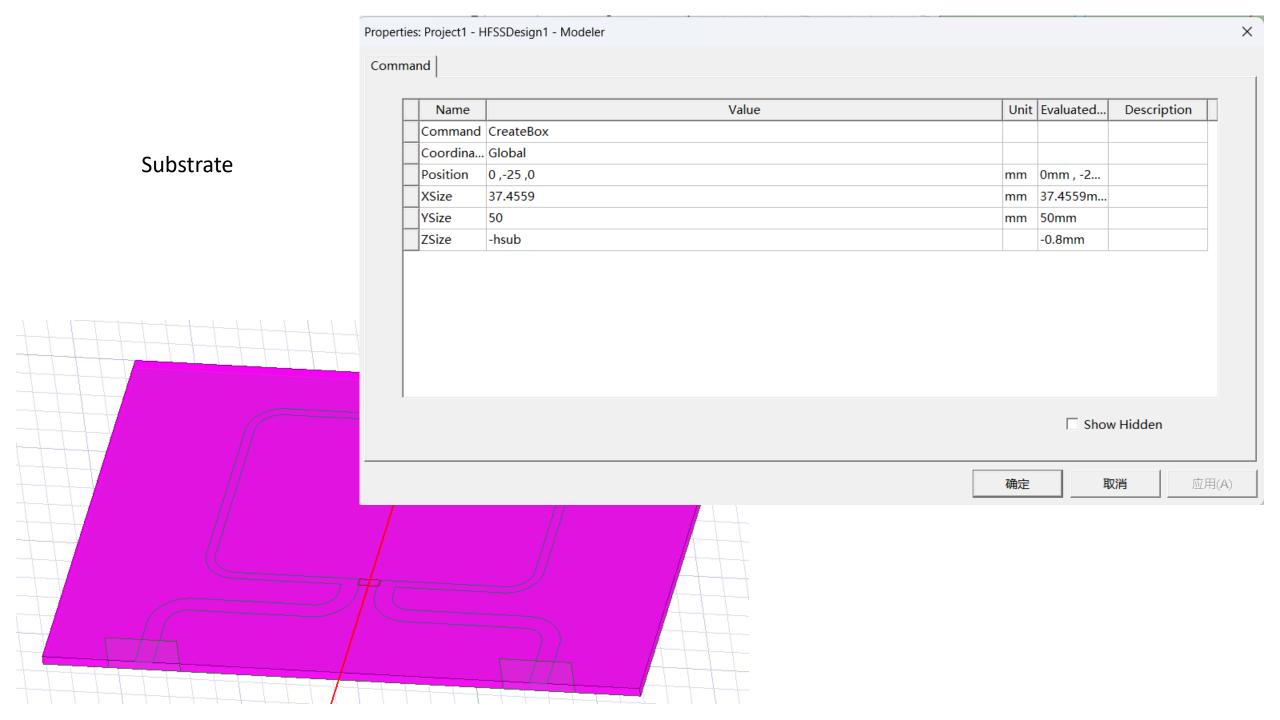


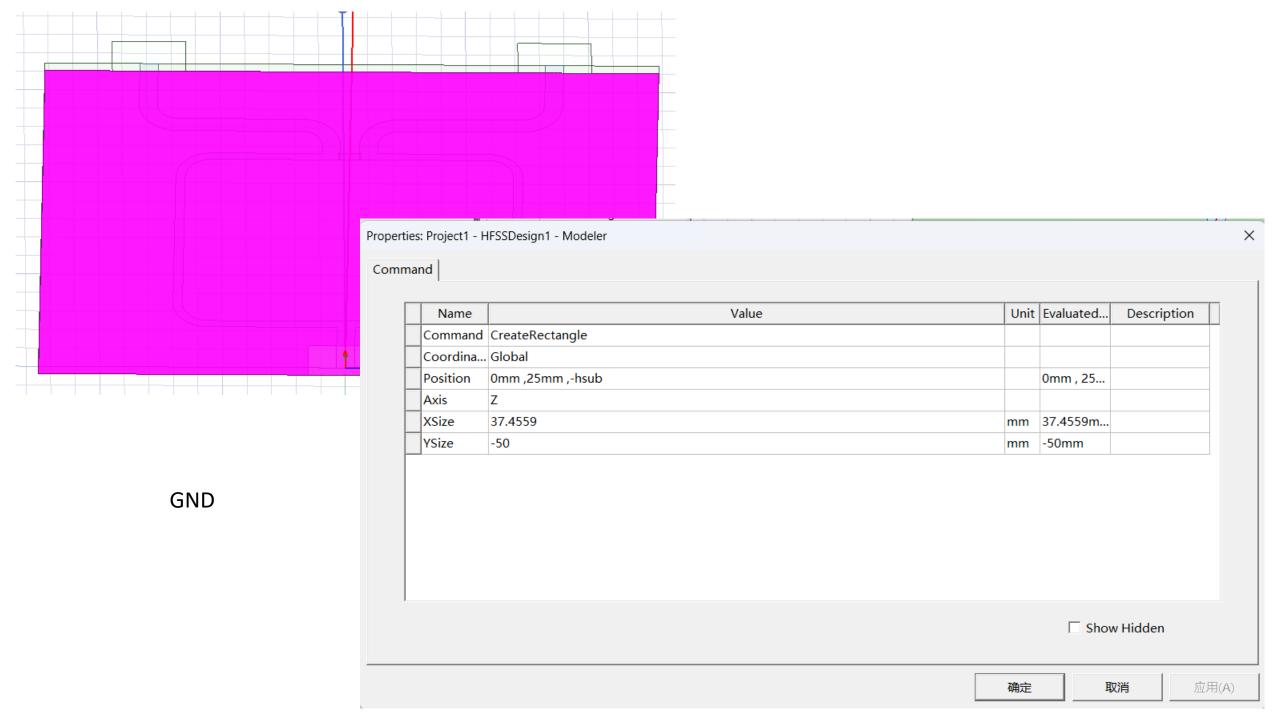


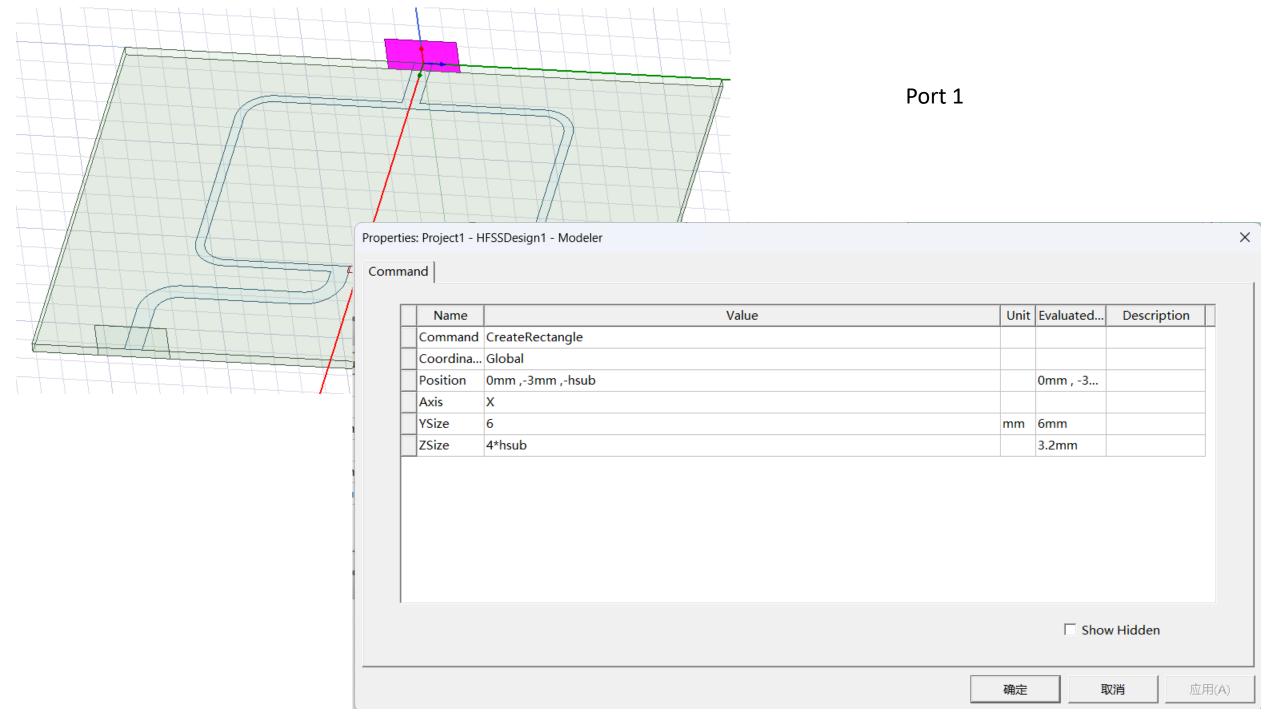


# **Draw Boxes and Rectangles**











Properties: Project1 - HFSSDesign1 - Modeler

Command

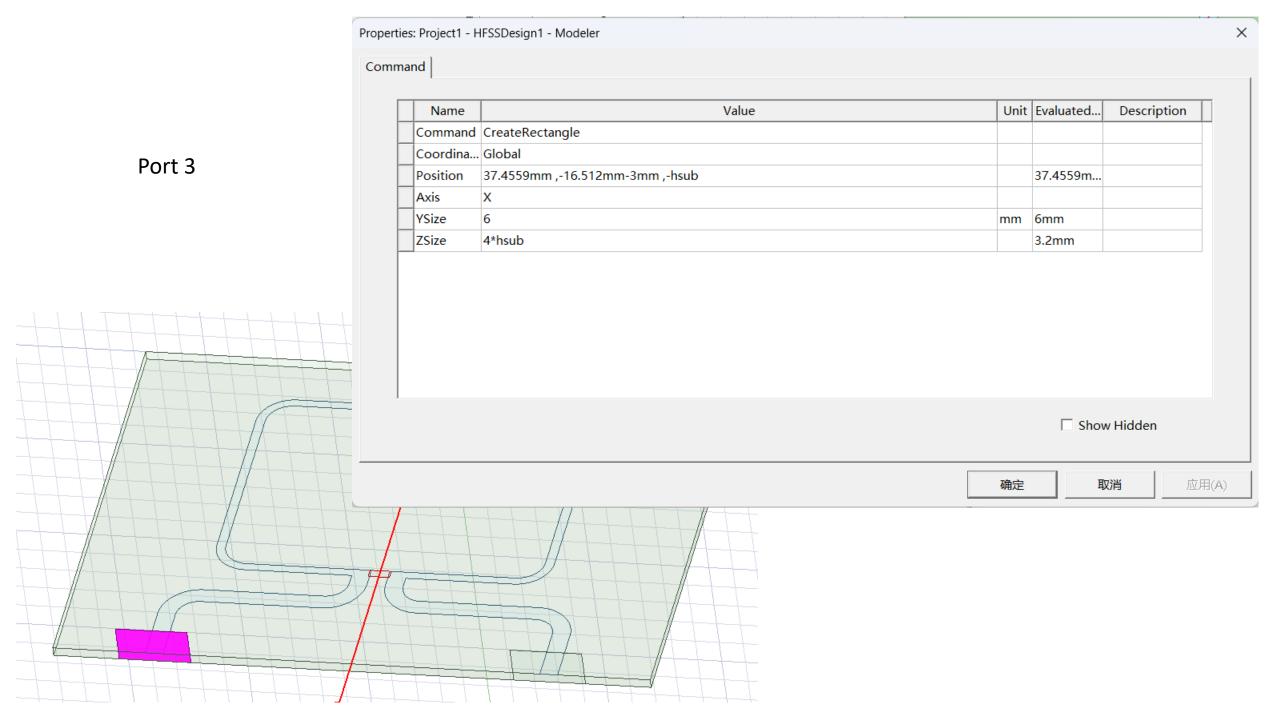
Name	Value	Unit	Evaluated	Description
Command	CreateRectangle			
Coordina	Global			
Position	37.4559mm ,16.512mm-3mm ,-hsub		37.4559m	
Axis	X			
YSize	6	mm	6mm	
ZSize	4*hsub		3.2mm	

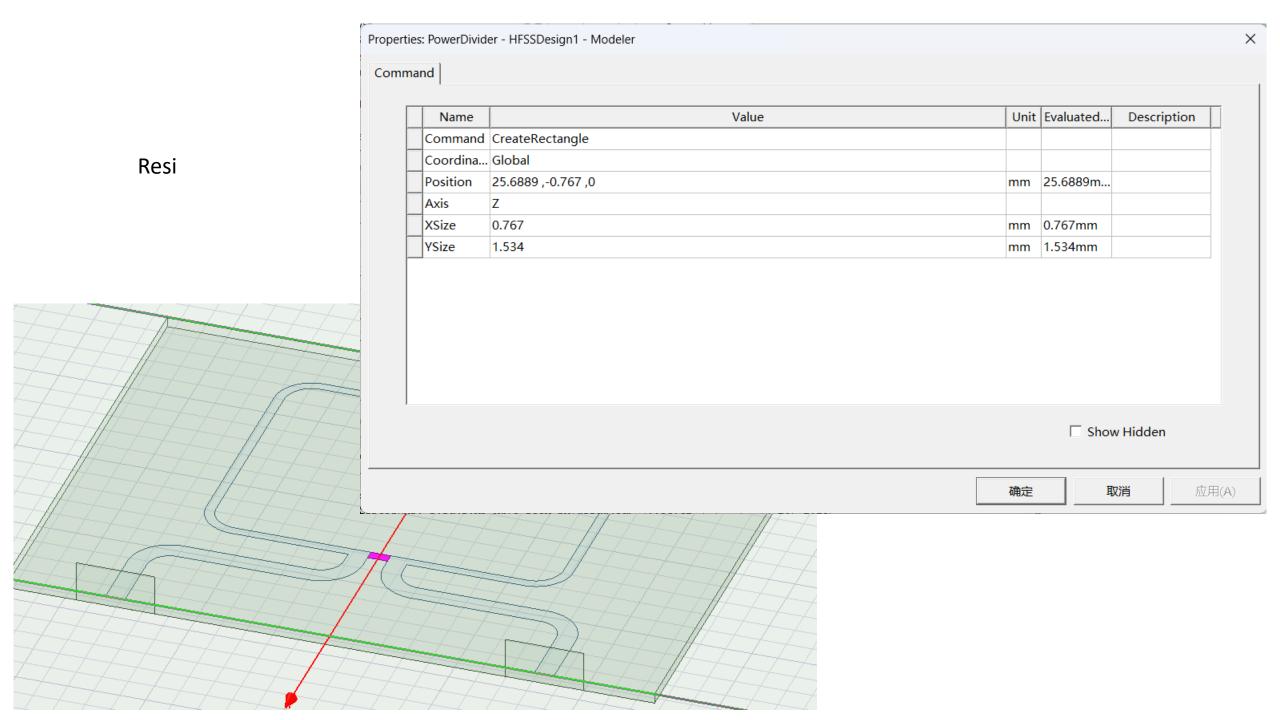
Show Hidden

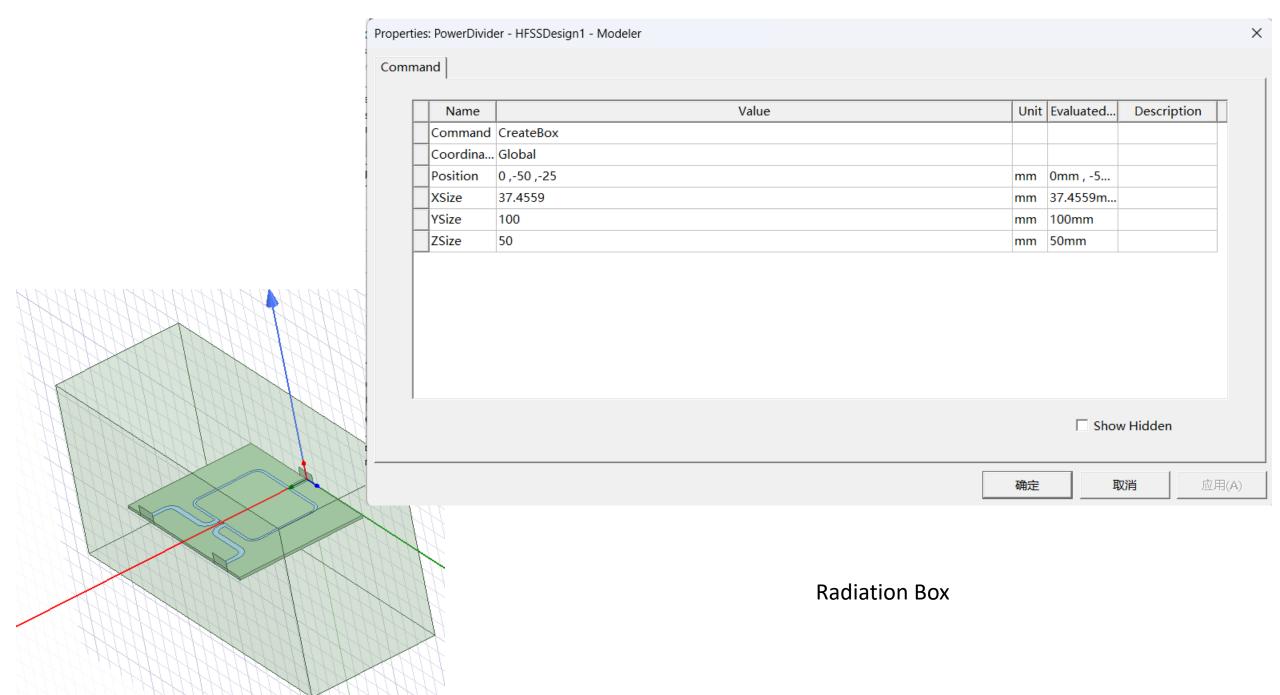
确定

取消

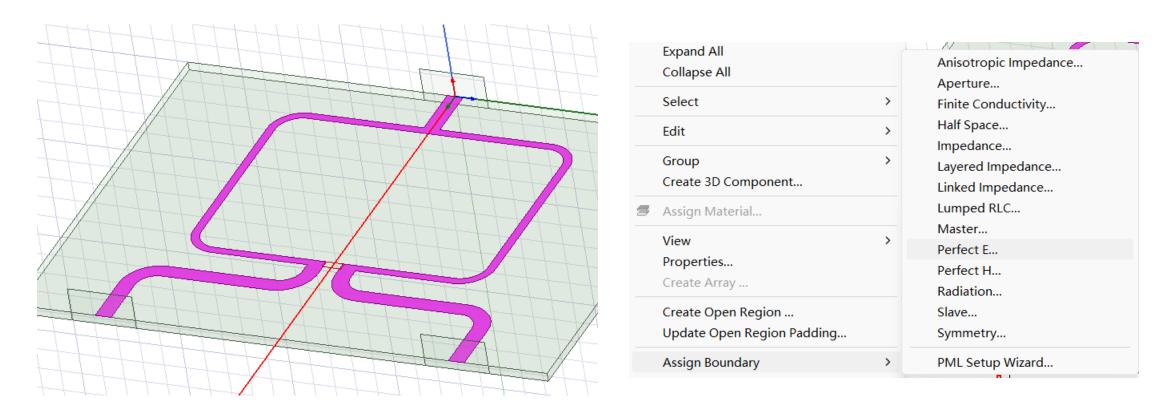
应用(A)



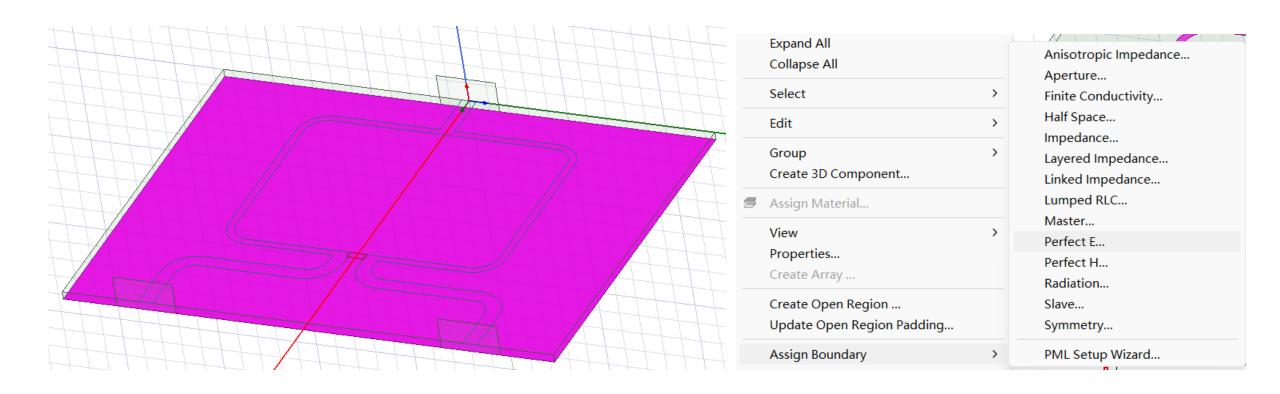




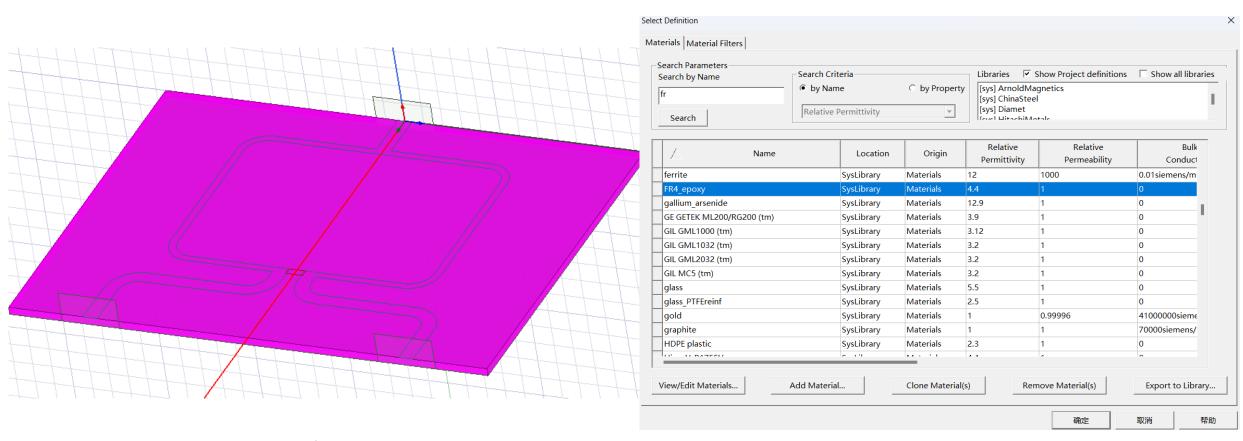
# Assign Material, Port and Boundary



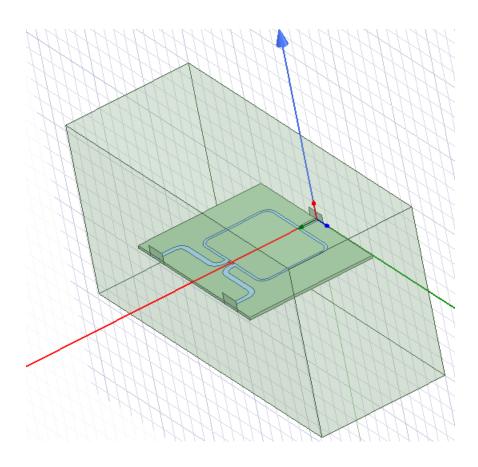
MicrostripLine



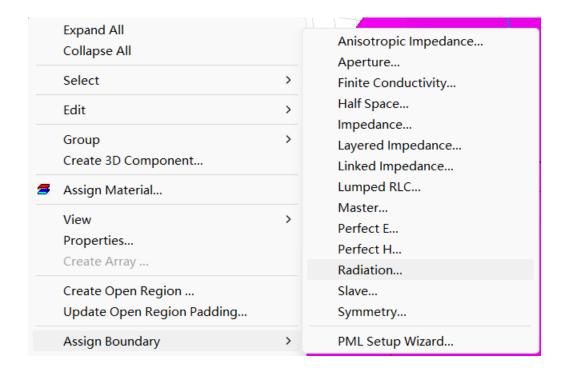
GND

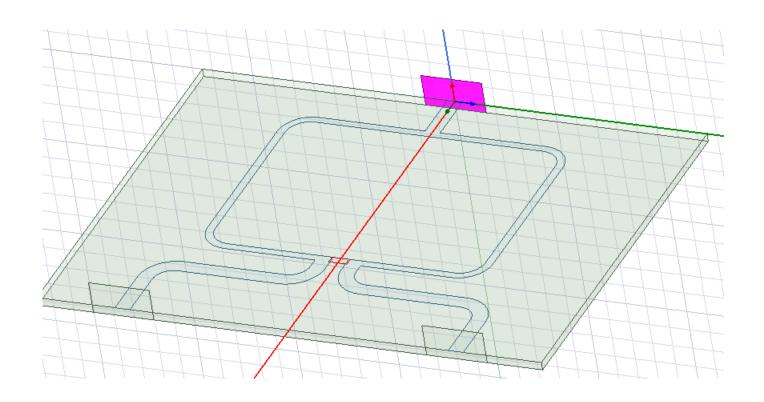


Substrate

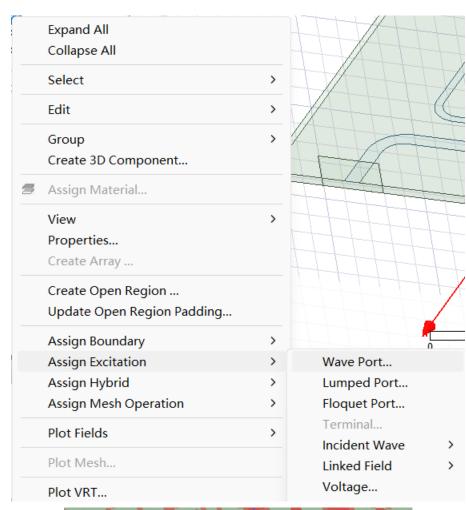


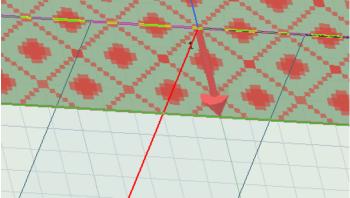
**Radiation Box** 

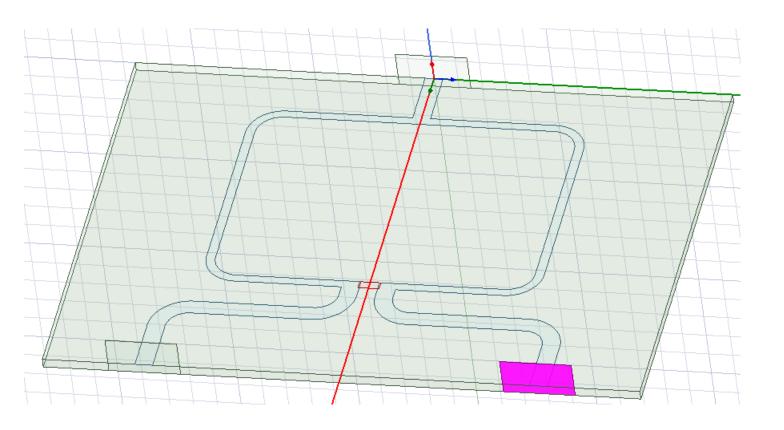




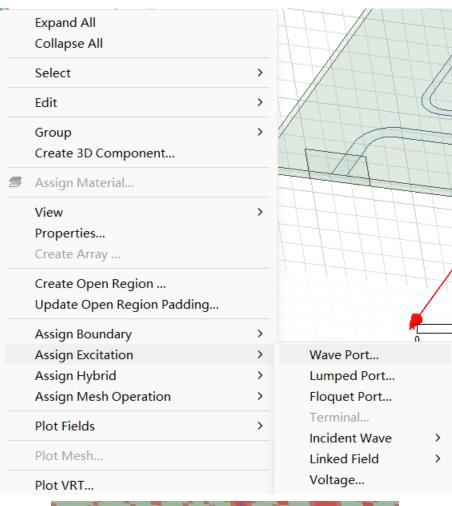
Port 1

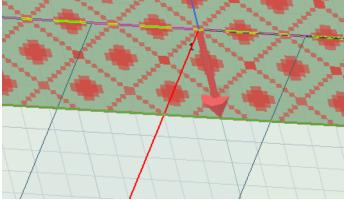


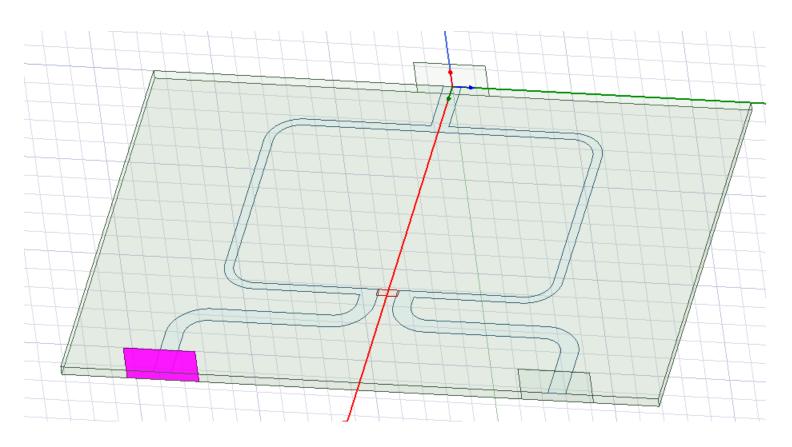




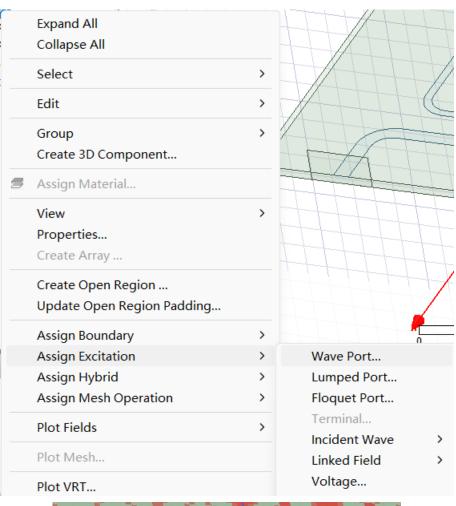
Port 2

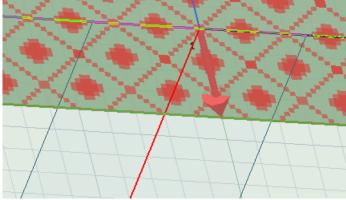


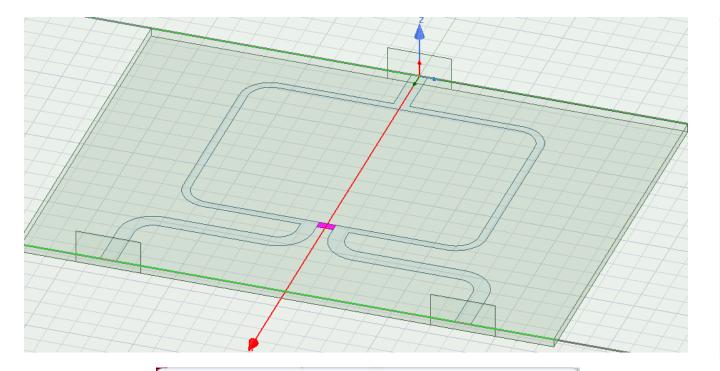




Port 3

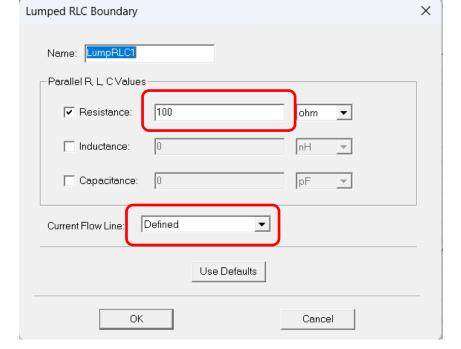


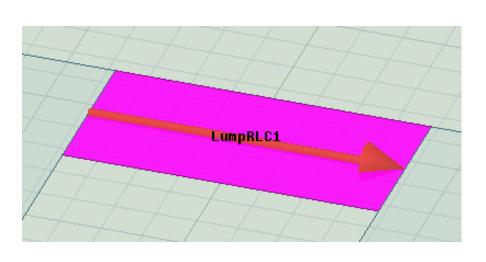




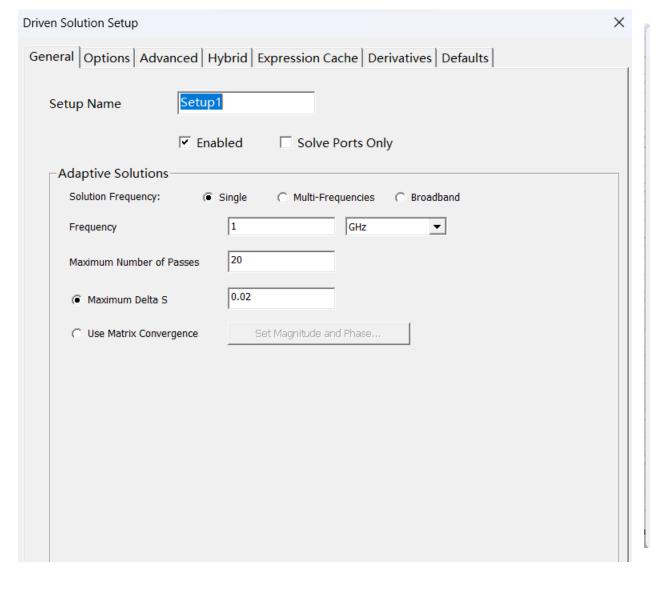
Expand All Collapse All		Anisotropic Impedance		
Select	>	Aperture Finite Conductivity		
Edit	>	Half Space		
Group Create 3D Component	>	Impedance Layered Impedance Linked Impedance		
Assign Material		Lumped RLC		
View Properties Create Array	>	Master Perfect E Perfect H Radiation		
Create Open Region Update Open Region Padding		Slave Symmetry		
Assign Boundary	>	PML Setup Wizard		

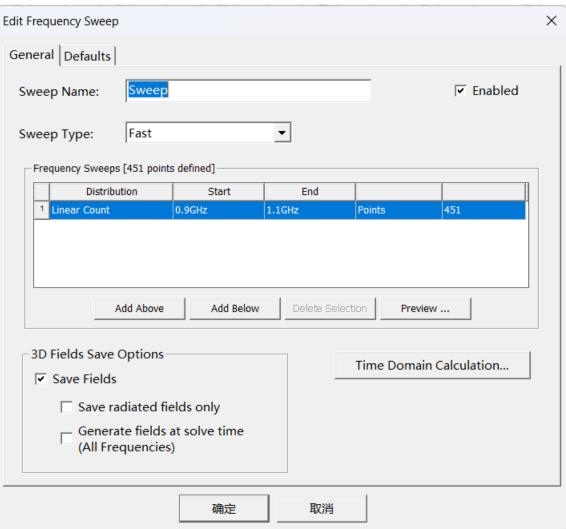


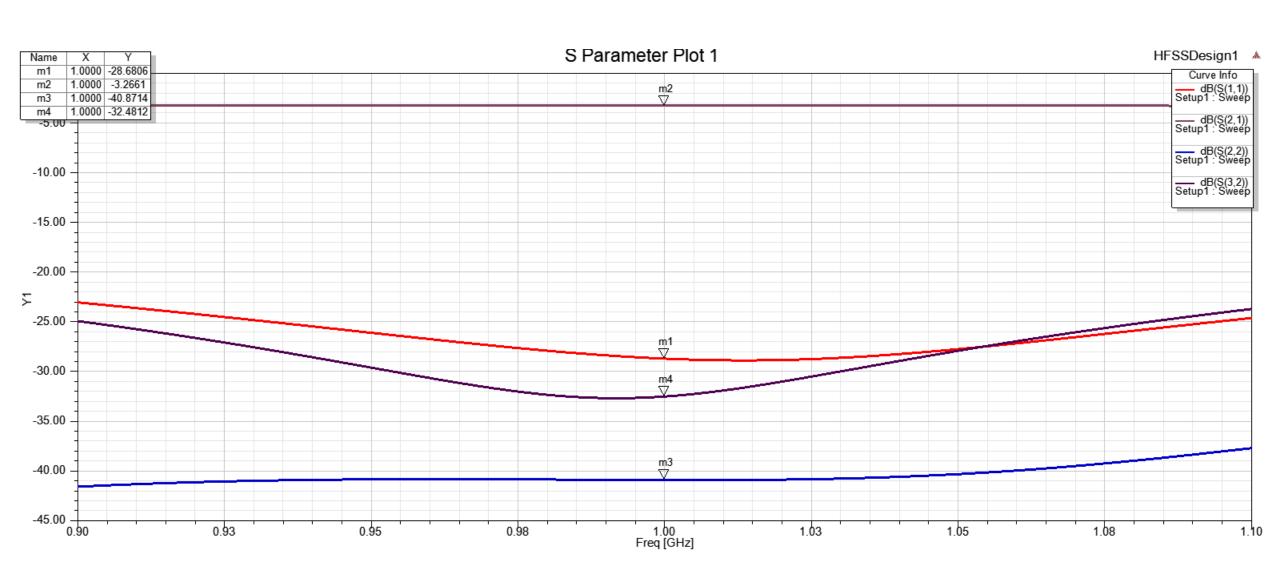




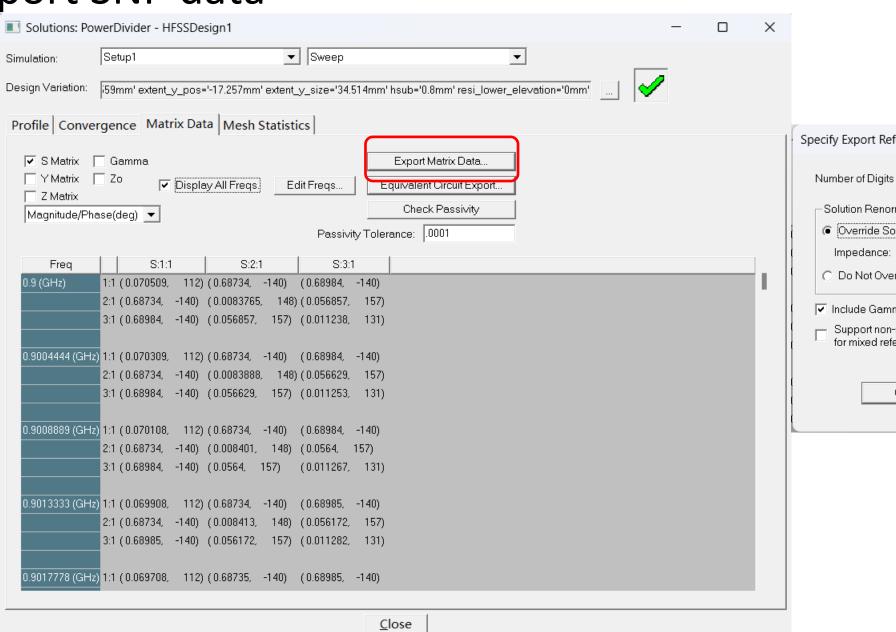
# **Analysis and Results**





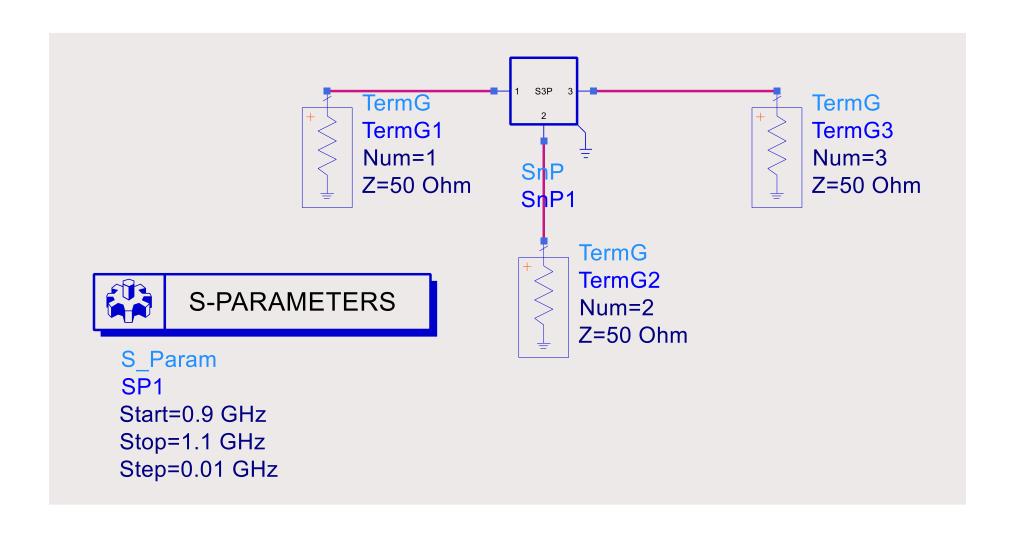


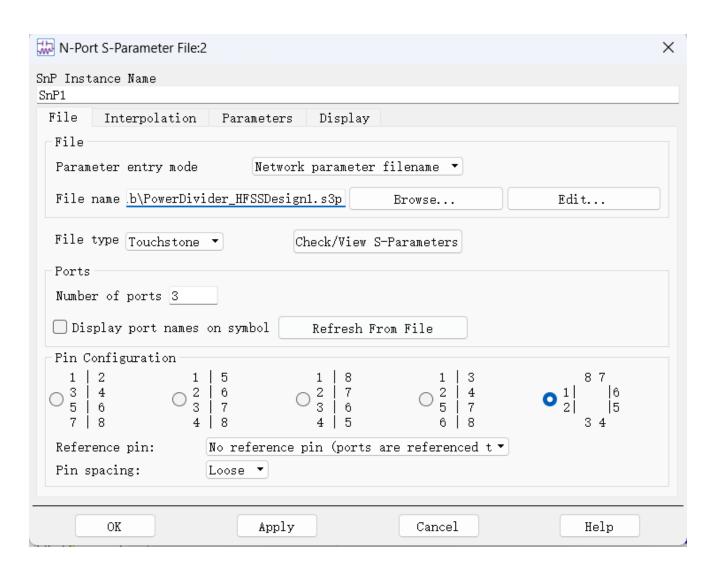
# **Export SNP data**

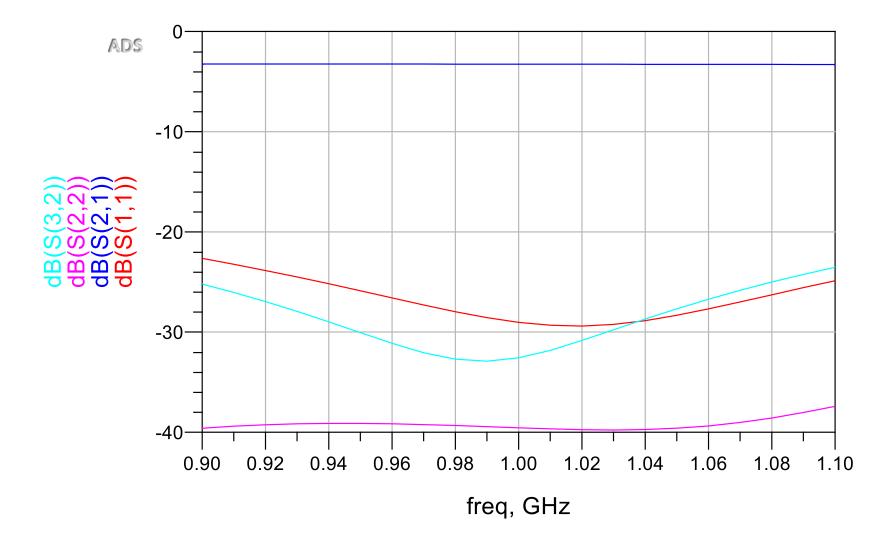


Specify Export Reference Impedance	×					
Number of Digits Precision: 15						
Solution Renormalization—						
Override Solution Renormalization						
Impedance: 50 Ohms						
C Do Not Override Solution Renormalization						
✓ Include Gamma and Impedance Comments						
Support non-standard Touchstone extensions for mixed reference impedances						
OK Cancel						

#### SNP data simulation in ADS







### Homework

- Wilkinson Power Divider design in ADS and HFSS
- Freq. 2.4 GHz
- Bandwidth: 200MHz
- Substrate: FR4, thickness: 1.6mm
- S11<-20dB, S21>-3.3dB, S22<-20dB and S32<-25dB</li>
- Optimization