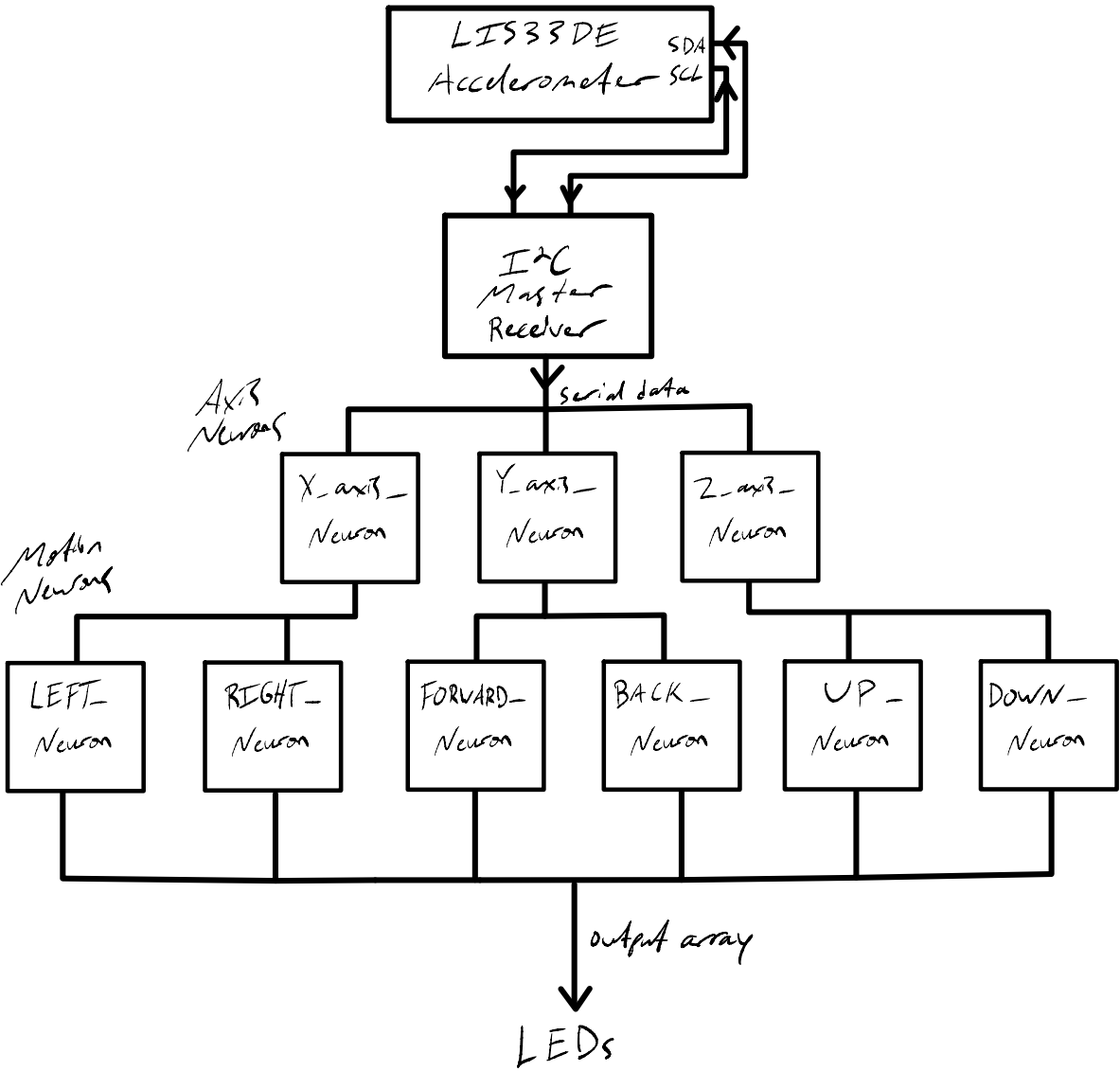
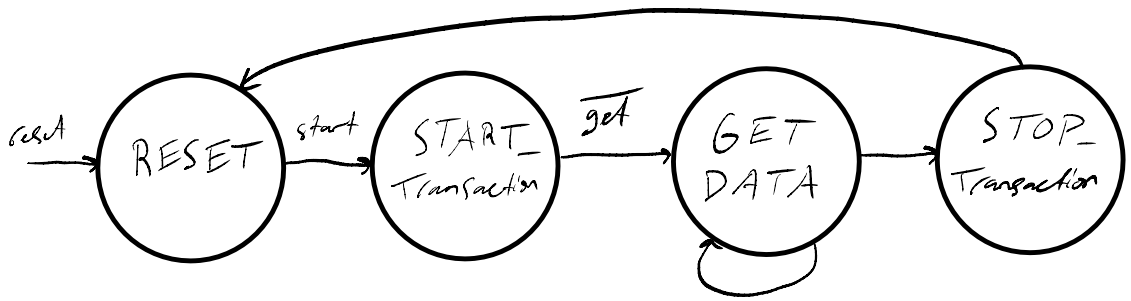


Christian Cherry
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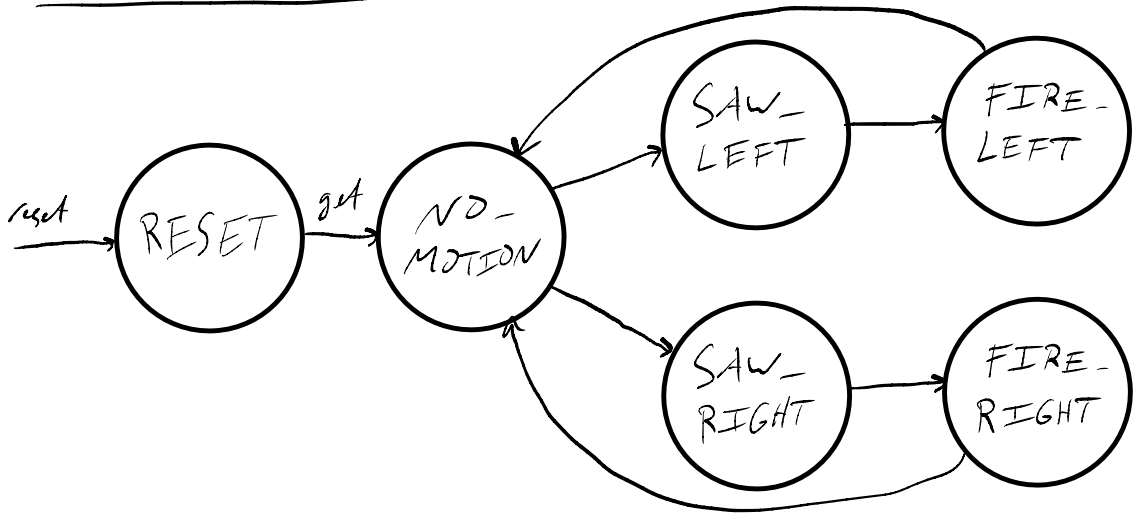
Simplified NPU Rough Schematic



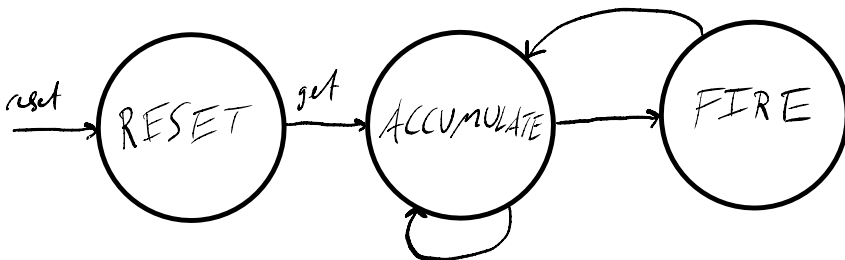
I²C Master General FSM



Ax3 Neurons General FSMs (X-axis example)



MAN Neurons General FSMs



Testbench Overview

Testbench will act as the accelerometer and generate a series of serial I2C data transactions corresponding to physical motions to replicate the data transfer of the LIS33DE Accelerometer. The design will output a 64 array corresponding to what motion it thinks it saw, and the TB will take this output and compare it against the actual motion it input to the dev.