[2024-1 Digital Control] Chapter 8. Digital Controller Design

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Introduction

8.2. Control System Specifications

We have learned about $\begin{cases} \text{discrete-time} \\ \text{sampled-data} \end{cases}$

system, modeled by

Transfer function

$$G^{\mathsf{d}}(z) = \frac{b_{n-1}z^{n-1} + \dots + b_0}{z^n + a_{n-1}z^{n-1} + \dots + a_0}$$

$$\begin{split} \mathbf{x}^{\mathsf{d}}(k+1) &= \mathbf{A}^{\mathsf{d}}\mathbf{x}^{\mathsf{d}}(k) + \mathbf{B}^{\mathsf{d}}u^{\mathsf{d}}(k), \\ y^{\mathsf{d}}(k) &= \mathbf{C}^{\mathsf{d}}\mathbf{x}(k) \end{split}$$

and the related concepts, such as stability.

In Chapters 8-9, 11, we will study

- ► (Chapter 8) controller design with transfer function models, including phase-lag, phase-lead, lag-lead compensators, and PID controllers.
- ► (Chapter 9) controller design with state-variable model, including state-feedback control, state observer.
- ► (Chapter 11) optimal controller design for state-feedback control (i.e., best among candidates)

Types of spec.: Steady-state accuracy

8.2. Control System Specifications

Remind that for the unit-step input whose z-transform is computed by

$$R^{\mathsf{d}}(z) = \frac{z}{z-1} = \frac{1}{1-z^{-1}}$$

one has the steady-state error of the closed-loop system as

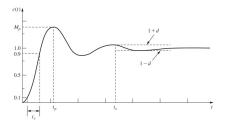
$$\begin{split} e_{\mathrm{ss}}^{\mathbf{d}} &= \lim_{k \to \infty} e^{\mathbf{d}}(k) \\ &= \lim_{z \to 1} (z-1) E^{\mathbf{d}}(z) = \lim_{z \to 1} \frac{(z-1) R^{\mathbf{d}}(z)}{1 + G^{\mathbf{d}}(z)} = \frac{1}{1 + \lim G^{\mathbf{d}}(z)} \\ &= \begin{cases} \mathrm{constant} \neq 0, & \text{if } N = 0 \\ 0, & \text{if } N \geq 1 \end{cases} \end{split}$$

Control objective: Make the steady-state error $e_{\rm ss}^{\rm d}$ as small as possible.

Types of spec.: Transient response

8.2. Control System Specifications

Control objective: Make the transient response fast enough and have little oscillation.



Parameters that specify the transient response:

- Rise time t_r : The time required for the step response to rise from 10 percent to 90 percent of the final value.
- ▶ Settling time t_s : The time required for the response to settle to within a certain percent of the final value.
- lacktriangle Time-to-peak overshoot H_p and peak overshoot M_p

Types of spec.: Transient response

8.2. Control System Specifications

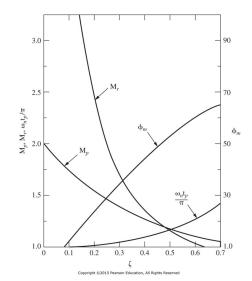
The figure represents the relation between

- ightharpoonup rise time t_r
- ightharpoonup settling time t_s
- ightharpoonup time-to-peak overshoot t_p
- ightharpoonup peak overshoot M_p

of the step response of

$$G_p(s) = \frac{\omega_n^2}{s^2 + 2\zeta\omega_n s + \omega_n^2}$$

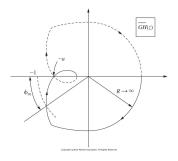
- $\triangleright \omega_n$: natural frequency
- ζ: damping ratio.



Types of spec.: Gain and phase margins

8.2. Control System Specifications

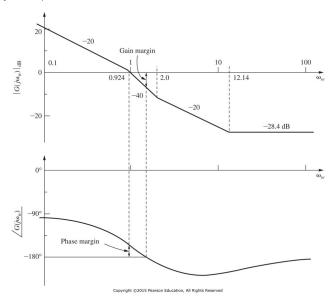
- ▶ Phase-crossover point = The point of $\angle G^{\mathsf{d}}(\mathrm{e}^{j\omega T}) = -\pi$.
- ▶ Gain-crossover point = The point of $|G^{\mathsf{d}}(\mathrm{e}^{j\omega T})| = 1$
- ► Gain margin $a = -20 \log |G^{\mathsf{d}}(e^{j\omega T})| dB$ at the phase-crossover point
- ▶ Phase margin $\phi_m = \angle G^{\mathsf{d}}(\mathrm{e}^{j\omega T}) (-\pi)$ at the gain-crossover point



Control objective: Make the gain and phase margins of the system as large as possible.

Types of spec.: Gain and phase margins

8.2. Control System Specifications

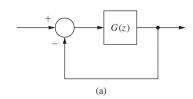


Types of spec.: Sensitivity function

8.2. Control System Specifications

Consider the closed-loop transfer function given by

$$T^{\mathsf{d}}(z) = \frac{G^{\mathsf{d}}(z)}{1 + G^{\mathsf{d}}(z)}.$$



The sensitivity function of T^d with respect to G^d is given by

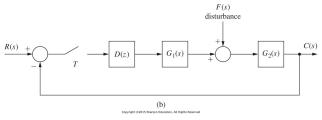
$$\begin{split} S^{\mathsf{d}}(z) &= \frac{\Delta T^{\mathsf{d}}/T^{\mathsf{d}}}{\Delta G^{\mathsf{d}}/G^{\mathsf{d}}} = \frac{\partial T^{\mathsf{d}}}{\partial G^{\mathsf{d}}} \frac{G^{\mathsf{d}}}{T^{\mathsf{d}}} \\ &= \frac{1 + G^{\mathsf{d}} - G^{\mathsf{d}}}{(1 + G^{\mathsf{d}})^2} \frac{G^{\mathsf{d}}}{G^{\mathsf{d}}/(1 + G^{\mathsf{d}})} = \frac{1}{1 + G^{\mathsf{d}}(z)}. \end{split}$$

Control objective: Make $|S^{\rm d}(e^{j\omega T})|$ as small as possible (at least in the frequency range of interest).

Types of spec.: Disturbance rejection

8.2. Control System Specifications

Disturbance? A signal generated from an external source that needs to be compensated by a controller.



In the system above, if $r^{\rm d}(k)\equiv 0,$ then the output $C^{\rm d}(z)$ is computed by

$$C^{\mathsf{d}}(z) = \frac{\overline{G_2 F}^{\mathsf{d}}(z)}{1 + D^{\mathsf{d}}(z)\overline{G_1 G_2}^{\mathsf{d}}(z)}$$

Control objective: Make $|C^{\mathsf{d}}(e^{j\omega T})|$ small enough in the frequency range where the disturbance f(t) is dominant.

Types of spec.: Control effort

8.2. Control System Specifications

The control effort is the input $u^{\mathbf{d}}(k)$ of the plant used for control of a system.

Control objective: Minimize the (squared) energy of the control effort

$$J=\int_0^{t_f}|u(t)|^2\mathrm{d}t, \qquad \qquad \text{in continuous time,}$$
 or
$$J=\sum_{i=0}^{N_f}|u^{\mathsf{d}}(i)|^2, \qquad \qquad \text{in discrete time.}$$

Note: A generalization of J has the form

$$J = \sum_{i=0}^{N_f} \mathbf{x}^{\mathsf{d}}(i)^{\mathsf{T}} \mathbf{Q} \mathbf{x}^{\mathsf{d}}(i) + u^{\mathsf{d}}(i)^{\mathsf{T}} \mathbf{R} u^{\mathsf{d}}(i)$$

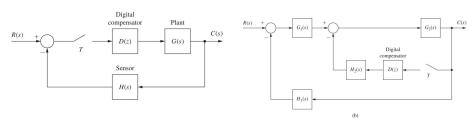
where $\mathbf{Q} \geq 0$ and $\mathbf{R} > 0$. (We will go further in Chapter 11.)

Two types of compensation

8.3. Compensation

We will design a digital controller $D^{d}(z)$ (or equivalently, compensator)

- for single-input singpue-output systems, and
- with the control objectives above taken into account.
- 1. Cascade (or series) compensation:
- 2. Feedback (or parallel) compensation:



We for now consider the first-order compensator

$$D^{\rm d}(z)=K_{\rm C}rac{z-z_0}{z-z_{
m P}}$$
 where $K_{
m C}$, z_0 and $z_{
m p}$ are needed to be selected.

Frequency response of the first-order compensator

8.3. Compensation

Since $D^{\mathrm{d}}(z)$ is of first order, its frequency response will have the form

$$D^{\mathsf{w}}(\mathbf{w}) = a_0 \frac{1 + \mathbf{w}/\omega_{\mathbf{w}0}}{1 + \mathbf{w}/\omega_{\mathbf{w}p}}$$

where a_0 is DC gain, $\omega_{\rm w0}$ and $\omega_{\rm wp}$ are design parameters.

- ▶ IF $\omega_{w0} < \omega_{wp}$, THEN the compensation is called phase lead;
- ▶ IF $\omega_{\rm w0} > \omega_{\rm wp}$, THEN the compensation is called phase lag.

Note: Once a_0 , $\omega_{\rm w0}$ and $\omega_{\rm wp}$ are determined, then

$$D^{\mathsf{d}}(z)|_{z=\frac{1+(T/2)w}{1-(T/2)w}} = a_0 \frac{1+w/\omega_{w0}}{1+w/\omega_{wp}}$$

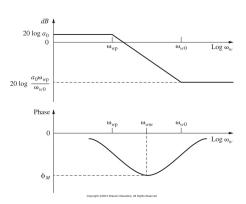
with

$$K_{\rm C} = a_0 \left(\frac{\omega_{\rm wp}(\omega_{\rm w0} + 2/T)}{\omega_{\rm w0}(\omega_{\rm wp} + 2/T)} \right), \quad z_0 = \frac{2/T - \omega_{\rm w0}}{2/T + \omega_{\rm w0}}, \quad z_{\rm p} = \frac{2/T - \omega_{\rm wp}}{2/T + \omega_{\rm wp}}.$$

Frequency response of phase-lag filter

8.4. Phase-lag Compensation

- ightharpoonup DC gain = a_0 ;
- High-frequency gain $= a_0 \frac{\omega_{\rm wp}}{\omega_{\rm w0}}$;
- Maximum phase shift = ϕ_M (satisfying $-90^{\circ} < \phi_M < 0^{\circ}$)

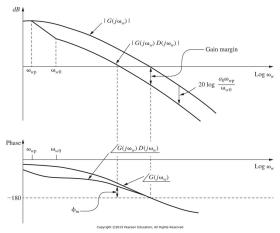


Design objective for phase-lag compensation

8.4. Phase-lag Compensation

Design objective: Set the phase margin as a given value ϕ_m .

Idea: Decrease the magnitude $|G^{\rm w}(j\omega_{\rm w})D^{\rm w}(j\omega_{\rm w})|$ of the resulting open-loop transfer function.



Design steps for phase-lag compensator

8.4. Phase-lag Compensation

Design objective: Set the phase margin as a given value ϕ_m .

- ▶ Step 1: Determine ω_{w1} at which $\angle G(j\omega_{w1}) \approx -180^\circ + \phi_m + 5^\circ$. Note: This ω_{w1} will be the gain-crossover frequency of the controlled system.
- ► Step 2: Choose $\omega_{w0} = 0.1\omega_{w1}$.
- ▶ Step 3: Set ω_{wp} as

$$\omega_{\rm wp} = \frac{\omega_{\rm w0}}{a_0 |G(j\omega_{\rm w1})} = \frac{0.1\omega_{\rm w1}}{a_0 |G(j\omega_{\rm w1})}$$

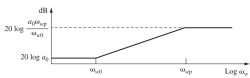
(so that $|G^{\mathsf{w}}(j\omega_{\mathrm{w}1})D^{\mathsf{w}}(j\omega_{\mathrm{w}1})|=1$).

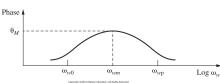
Example: See Example 8.1.

Frequency response of phase-lead filter

8.5. Phase-lead Compensation

- ightharpoonup DC gain = a_0 ;
- $\blacktriangleright \text{ High-frequency gain} = a_0 \frac{\omega_{\text{wp}}}{\omega_{\text{w0}}}$
- ▶ Maximum phase shift = θ_M





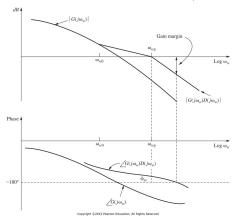
Design objective for phase-lead compensation

8.5. Phase-lead Compensation

Design objective: Set the phase margin as a given value ϕ_m .

Idea: Increase the angle $\angle G^{\mathsf{w}}(j\omega_{\mathrm{w}})D^{\mathsf{w}}(j\omega_{\mathrm{w}})$ of the resulting open-loop transfer function.

(We skip the detailed guideline for controller design...)

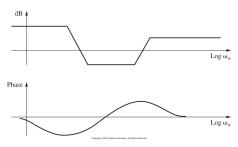


Lag-lead compensator

8.7. Lag-lead Compensation

A lag-lead compensator

= A phase-lag compensator \times A phase-lead compensator



Overview of PID control

8.8. Integration and Differentiation Filters

A PID controller in s-domain has the form

$$D_{\rm PID}(s) = K_{\rm P} + \frac{K_{\rm I}}{s} + K_{\rm D}s \qquad \qquad \text{(= Ideal version)}$$
 or
$$D_{\rm PID}(s) = K_{\rm P} + \frac{K_{\rm I}}{s} + K_{\rm D}\frac{s}{T_{\rm F}s+1} \qquad \text{(= Modified version with LPF)}.$$

In the time-domain, the resulting control input is given by (e.g., ideal version):

$$m_{\text{PID}}(t) = K_{\text{P}} \cdot e(t) + K_{\text{I}} \cdot \int_0^t e(\tau) d\tau + K_{\text{D}} \dot{e}(t).$$

Note: Each terms play the role of:

- ightharpoonup P term $K_{
 m P}$: The present control effort for reducing the tracking error
- ▶ I term $\frac{K_{\rm I}}{s}$: The accumulated control effort with the history of the tracking error taken into account
- ▶ D term K_Ds: The predictive control effort to improve the tendency of the future trajectory.

Numerical integration of a continuous-time signal

8.8. Integration and Differentiation Filters

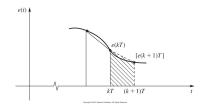
Let $p_{\rm I}^{\rm d}(k)$ be a numerical integral of a continuous-time signal e(t).

► Forward Euler method:

$$p_{\mathrm{I}}^{\mathsf{d}}(k+1) = p_{\mathrm{I}}^{\mathsf{d}}(k) + T \cdot e^{\mathsf{d}}(k) \qquad \qquad \mathsf{where} \ e^{\mathsf{d}}(k) := e(kT)$$

- Backward method
- Trapezoidal rule (= Tustin's method, bilinear transformation):

$$p_{\rm I}^{\sf d}(k+1) = p_{\rm I}^{\sf d}(k) + \frac{T}{2} (e^{\sf d}(k+1) + e^{\sf d}(k))$$



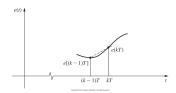
Numerical differentiation of a continuous-time signal

8.8. Integration and Differentiation Filters

Let $p_{\mathrm{D}}^{\mathrm{d}}(k)$ be a numerical derivative of of a continuous-time signal e(t).

- ► Forward Euler method
- ► Backward Euler method:

$$p_{\rm D}^{\sf d}(k) = \frac{e^{\sf d}(k) - e^{\sf d}(k-1)}{T}$$

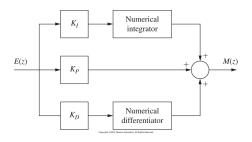


► Tustin's method:

$$\frac{p_{\rm D}^{\rm d}(k) + p_{\rm D}^{\rm d}(k-1)}{2} = \frac{e^{\rm d}(k) - e^{\rm d}(k-1)}{T}$$

Discrete-time PID control law

8.9. PID Controllers



A discretized version of the PID controller above is

$$m_{\mathrm{PID}}^{\mathbf{d}}(k) = \begin{cases} + \text{ P term} \\ + \text{ I term} \\ + \text{ D term} \end{cases} = \begin{cases} + K_{\mathrm{P}} \cdot e^{\mathbf{d}}(k) \\ + K_{\mathrm{I}} \cdot \text{(numerical integration of } e(t) \text{)} \\ + K_{\mathrm{D}} \cdot \text{(numerical differentiation of } e(t) \text{)} \end{cases}$$

where $K_{\rm P}>0$, $K_{\rm I}>0$, and $K_{\rm D}>0$ are design parameters.

Frequency response of numerical integration via B.T.

8.9. PID Controllers

From the previous slides, we have

$$\begin{array}{ll} \text{(Numerical integration of } e(t) \text{)} & p_{\mathrm{I}}^{\mathrm{d}}(k+1) = p_{\mathrm{I}}^{\mathrm{d}}(k) + \frac{T}{2} \big(e^{\mathrm{d}}(k+1) + e^{\mathrm{d}}(k) \big) \\ & \xrightarrow{\mathcal{Z}\text{-transform}} & z P_{\mathrm{I}}^{\mathrm{d}}(z) = P_{\mathrm{I}}^{\mathrm{d}}(z) + \frac{T}{2} \big(z E^{\mathrm{d}}(z) + E^{\mathrm{d}}(z) \big). \end{array}$$

The transfer function from $e^{\rm d}$ to $p_{\rm I}^{\rm d}$ is computed by

$$\frac{P_{\rm I}^{\sf d}(z)}{E^{\sf d}(z)} = \frac{T}{2} \frac{z+1}{z-1}.$$

The frequency response in w-domain:

$$\left. \frac{P_{\mathrm{I}}^{\mathrm{d}}(z)}{E^{\mathrm{d}}(z)} \right|_{z=\frac{1+(T/2)\mathrm{w}}{1-(T/2)\mathrm{w}}} = \frac{1}{\mathrm{w}}.$$

Frequency response of numerical differentiation via B.T.

8.9. PID Controllers

In a similar way, one can have

(Numerical differentiation of
$$e(t)$$
) $p_{\mathrm{D}}^{\mathsf{d}}(k) = -p_{\mathrm{D}}^{\mathsf{d}}(k-1) + \frac{2}{T} \left(e^{\mathsf{d}}(k) - e^{\mathsf{d}}(k-1) \right)$
$$\xrightarrow{\mathbf{Z}\text{-transform}} P_{\mathrm{D}}^{\mathsf{d}}(z) = -z^{-1} P_{\mathrm{D}}^{\mathsf{d}}(z) + \frac{2}{T} \left(E^{\mathsf{d}}(z) - z^{-1} E^{\mathsf{d}}(z) \right).$$

The transfer function from $e^{\mathbf{d}}(k)$ to $m_{\mathrm{D}}^{\mathbf{d}}(k)$ is computed by

$$\frac{P_{\mathrm{D}}^{\mathrm{d}}(z)}{E^{\mathrm{d}}(z)} = \frac{2}{T} \frac{1-z^{-1}}{1+z^{-1}} = \frac{2}{T} \frac{z-1}{z+1}.$$

The frequency response in w-domain:

$$\left. \frac{P_{\rm D}^{\rm d}(z)}{E^{\rm d}(z)} \right|_{z=\frac{1+(T/2){\rm w}}{1-(T/2){\rm w}}} = \frac{1}{{\rm w}}.$$

Frequency response of discrete-time PID control

8.9. PID Controllers

Thus, the discrete-time PID controller

$$D_{\text{PID}}^{\mathsf{d}}(z) = K_{\text{P}} + K_{\text{I}} \cdot \frac{T}{2} \frac{z+1}{z-1} + K_{\text{D}} \cdot \frac{2}{T} \frac{z-1}{z+1}$$

has the frequency response in w-domain as follows:

$$D_{\text{PID}}^{\mathsf{d}}(\mathbf{w}) = K_P + K_I \frac{1}{\mathbf{w}} + K_D \mathbf{w}.$$

Note: If we use different methods for numerical differentiation and/or integration, then the frequency response should be slightly modified.

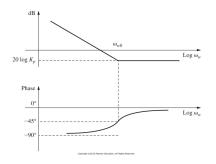
Frequency-domain design of PID control

8.9. PID Controllers

A PI controller has the frequency response

$$D_{\mathrm{PI}}^{\mathrm{w}}(\mathbf{w}) = K_{\mathrm{P}} + K_{\mathrm{I}} \frac{1}{\mathbf{w}} = K_{\mathrm{P}} \frac{1 + (\mathbf{w}/\omega_{\mathrm{w}0})}{\mathbf{w}} \quad \text{where } \omega_{\mathrm{w}0} := K_{\mathrm{I}}/K_{\mathrm{P}} > 0.$$

Bode plot of $D_{PI}^{w}(w)$:



Lesson:

- Negative phase angle
- Increase gain margin
- → ∴ PI controller works as a phase-lag controller.

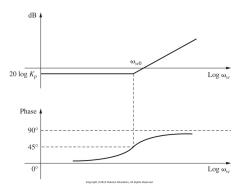
Frequency-domain design of PID control

8.9. PID Controllers

A PD controller has the frequency response

$$D_{\mathrm{PD}}^{\mathrm{w}}(\mathbf{w}) = K_{\mathrm{P}} + K_{\mathrm{D}} \mathbf{w} = K_{\mathrm{P}} \left(1 + \frac{\mathbf{w}}{\omega_{\mathrm{w}0}} \right) \quad \text{where } \omega_{\mathrm{w}0} := \frac{K_{\mathrm{D}}}{K_{\mathrm{P}}} > 0.$$

Bode plot of $D_{PD}^{W}(w)$:

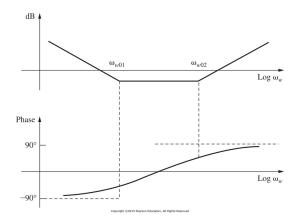


Lesson:

- Positive phase angle
- Increase phase margin
- .: PD controller works as a phase-lead controller.

Summarizing so far, PID pprox Lead-lag compensator

8.9. PID Controllers



 \therefore The PID controller can be constructed in a similar way of lead-lag compensator (in Sections 8.4 – 8.6).

Heuristic design of PID control: Ziegler-Nichols methods

8.9. PID Controllers

- \blacktriangleright A widely-used heuristic approach to selecting $K_{\rm P}$, $K_{\rm I}$, $K_{\rm D}$.
- ▶ Useful particularly when we do not have system model.
- ▶ Basic idea: Find the control gains via trial-and-error approach.
- ► Two methods are available (while the second one is usually employed...)

In the following, we will discuss the ZN method for a continuous-time version

$$\begin{split} D_{\mathrm{PID}}(s) &= K_{\mathrm{P}} + K_{\mathrm{I}} \frac{1}{s} + K_{\mathrm{D}} \frac{s}{T_{\mathrm{F}} s + 1} \\ &= K_{P} \left(1 + \frac{1}{T_{I} s} + \frac{T_{\mathrm{D}} s}{T_{\mathrm{F}} s + 1} \right), \quad \text{where } T_{\mathrm{I}} = \frac{K_{\mathrm{P}}}{K_{\mathrm{I}}} \text{ and } T_{\mathrm{D}} = \frac{K_{\mathrm{D}}}{K_{\mathrm{P}}} \end{split}$$

(while its discrete-time counterpart can be derived in the same way.)

For more details, refer to other references, including

► K. Aström, T. Hägglund, PID Controllers: Theory, Design, and Tuning (2nd ed.), ISA

Heuristic design of PID control: First ZN method

8.9. PID Controllers

Suppose that the plant to be controlled

- has overshoot-free step response, and
- ▶ has neither integrator nor complex-conjugate poles (in continuous time).

Then the first method suggests the PID gain as

$$K_{\rm P} = 1.2 \frac{t_r}{t_i \cdot c_{\rm ss}}, \quad T_{\rm I} = \frac{K_{\rm P}}{K_{\rm I}} = 2t_i, \quad T_{\rm D} = \frac{K_{\rm D}}{K_{\rm P}} = 0.5t_i.$$

where

- ▶ t_i : The time required for the response to rise from the initial value to 10~% of the final response
- $ightharpoonup t_r$: The rise time
- $ightharpoonup c_{
 m ss}$: The steady-state step response

Heuristic design of PID control: Second ZN method

8.9. PID Controllers

The second method suggests to follow the steps below.

- ▶ Step 1: Increase K_P from 0 to ∞ and apply the P control with no I and D terms.
- ▶ Step 2: Find a critical value $K_{\rm P} = K_{\rm P}^{\star}$, with which the output $c^{\rm d}(k)$ exhibits oscillation.
- ▶ Step 3: Let T^* be the period of oscillation that takes place with $K_P = K_P^*$.
- Step 4: Set the PID gains as

$$K_{\rm P} = 0.6K_{\rm P}^{\star}, \quad T_{\rm I} = 0.5T^{\star}, \quad T_{\rm D} = 0.125T^{\star}.$$

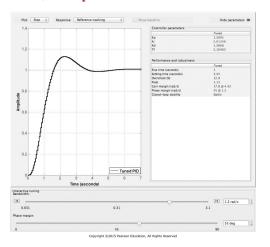
Note: The core of the second method is,

- ightharpoonup determine $K_{\rm P}$ first only with P control, and
- ▶ add I and D terms if needed.

Computer-aided design: MATLAB PID Toolbox

8.9. PID Controllers

- ► MATLAB provides a toolbox for frequency and/or time-domain designs.
- ► To run the toolbox, enter pidtool in Command Window



Root locus-based design of first-order compensator

8.11. Design by Root Locus

The root locus technique provides yet another method for constructing a first-order compensator

$$D^{\mathsf{d}}(z) = K_{\mathbf{C}} \frac{z - z_0}{z - z_{\mathbf{D}}}.$$

For ease of explanation, we assume that the plant to be controlled has a discrete-time transfer function

$$G^{d}(z) = K_{G} \frac{z - z_{1}}{(z - z_{2})(z - 1)}.$$

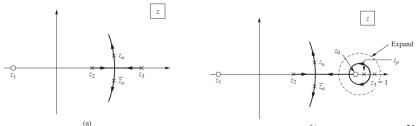
Design objective is to

- \triangleright place the closed-loop pole to a desired location z_a , and
- ▶ set the control gain $K_{\rm C}K_{\rm G}$ as the desired value K^{\star} .

Key idea of root locus-based design

8.11. Design by Root Locus

- ▶ First, find $K_{\rm u}$ such that the poles of $\frac{K_{\rm u}G^{\rm d}(z)}{1+K_{\rm u}G^{\rm d}(z)}$ are located at the desired values z_a . (Fig. (a))
- ▶ Second, add 1 zero $z=z_0$ and 1 pole $z=z_{\rm p}<1$ to the open-loop transfer function. (Fig. (b)) (Roughly speaking, this leads to a virtual shift of the open-loop pole z=1 inside the unit circle.)
- Adjust the design parameters properly so that the design objectives are satisfied.



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