

# Computer Structure and Language

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Hamid Sarbazi-Azad

Department of Computer Engineering  
Sharif University of Technology (SUT)  
Tehran, Iran

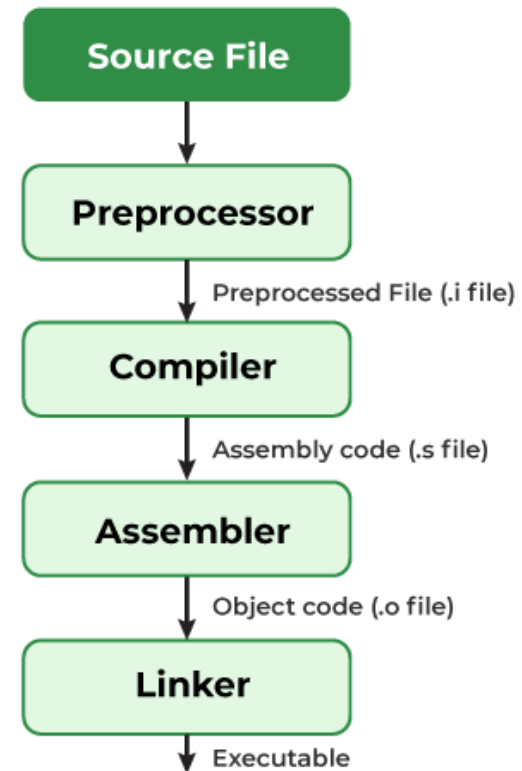


# Agenda

- From High level code to executable / compilation process
- Data Representation in Assembly / Registers, Memory & Variables
- Program Sections in Memory
- Implementing Logic in Assembly / What is different?
- Implementing If & Loop
- Talking About Stack
- Functions & Macros
- Writing Sample Programs

# Compilation Process

- **Compiling:**
  - High Level Code to Assembly
- **Assembling:**
  - Assembly Code to Machine (Object) Code
  - No Address Translations
  - Some symbols remain unresolved.
  - Can not be executed.
- **Linking:**
  - Resolving symbols in object code & creating executables.



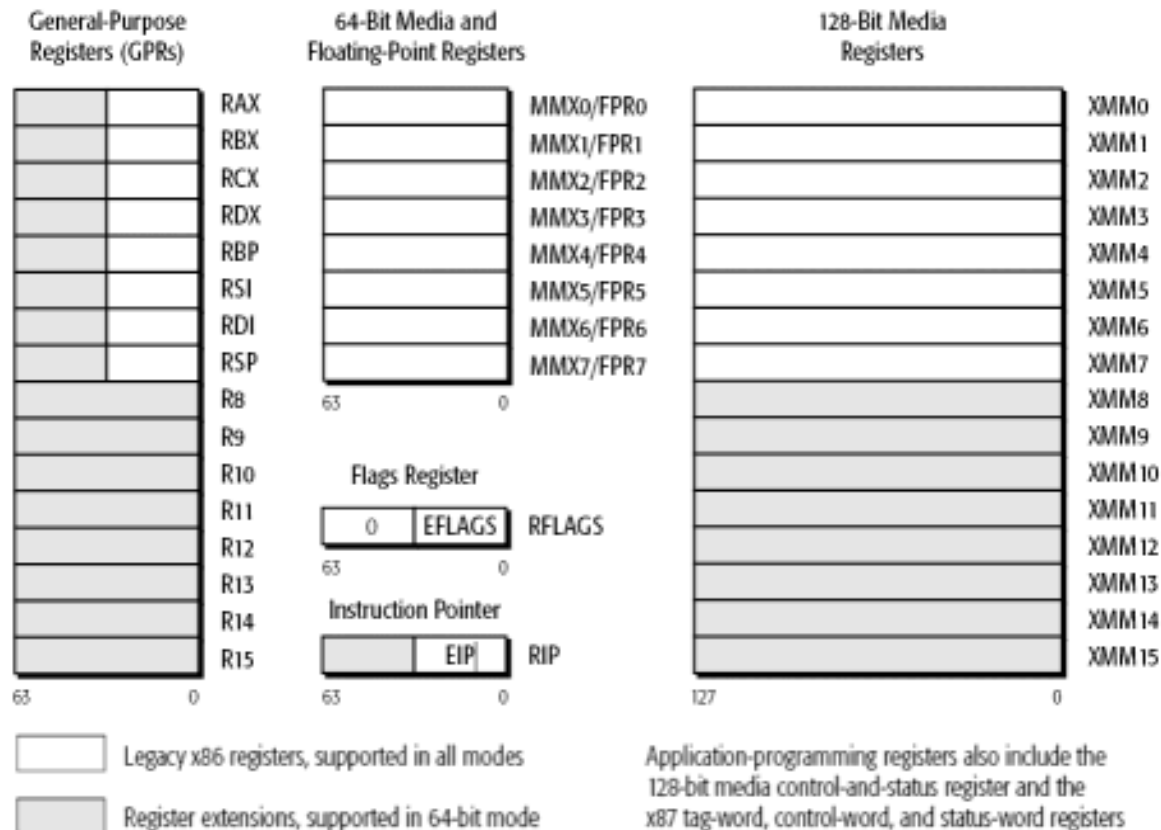
# DATA REPRESENTATION

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# Registers

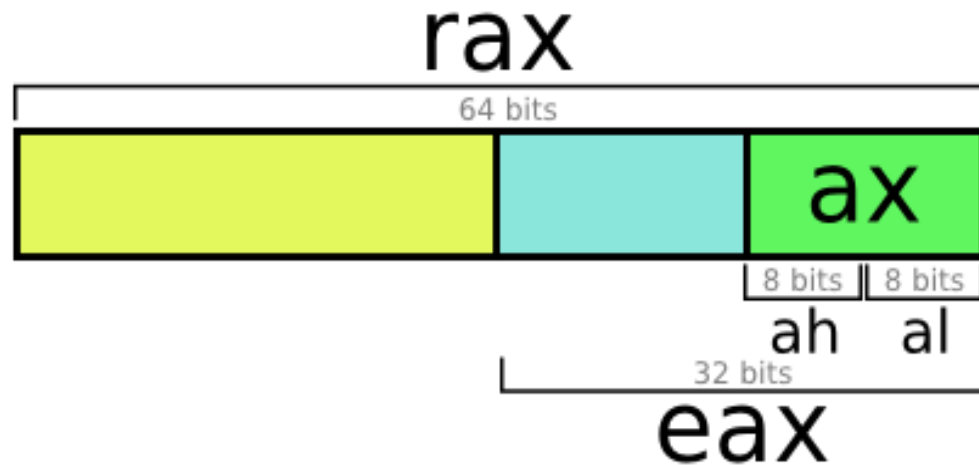
- High speed data stores built into the processor
- Limited in number, can not store all of the data required for the program
- Data has to move between them and memory
- Some of the registers have special purpose

# Registers (x86\_64)



<https://pvs-studio.com/en/blog/posts/a0029/>

# Registers (x86\_64)



<https://nullprogram.com/blog/2015/05/15/>

# Memory – Data vs Address

```
segment .data  
  
l1:      dd 1234
```

```
mov eax, l1  
call print_int  
call print_nl  
  
mov eax, [l1]  
call print_int  
call print_nl
```



# Memory – data segment

```
segment .data
```

```
11: db 123                ; one byte
12: dw 1000               ; one word - two bytes
13: db 11010b
14: db 12o
16: dd 1A92h             ; one double word - four bytes
17: dd 0x1A92
18: db 'A'
19: db "AB"              ; two bytes
```

**; remember we specify size, not type!**

# Memory – data segment

```
segment .data

b:    db 1           ; byte
w:    dw 1           ; word - 2 bytes
d:    dd 1           ; double word - 4 bytes
q:    dq 1           ; quad word - 8 bytes
t:    dt 1           ; 10 bytes

; (without initializing)
rb:   resb 4          ; reserve 4 bytes
rw:   resw 2          ; reserve 2 words
rd:   resd 5          ; reserve 5 double words
rq:   resq 10         ; reserve 10 quad words

id:   dd 1, 2, 3, 4, 5, 6 ; 6 double words with values (1, 2, 4, 5, 6)
tb:   times 9 db 1     ; 9 bytes all with value 1
```

# Memory – address

```
; for specifying memory address  
[reg1 + m*reg2 + offset]  
; m can be 1, 2, 4 or 8  
; offset has to be a literal
```

# Memory – When size matters

```
mov byte [11], 5  
mov word [12], 3  
inc dword [13]  
add rax, qword [14 + 4]
```

```
; We have to specify memory size for the operation  
; To do this we use keywords: byte, word, dword, qword
```

# Memory – Invalid Memory Operations

```
mov [11], [12]
add [11], [12]
sub [11], [12]
adc [11], [12]
sbb [11], [12]
cmp [11], [12]
and [11], [12]
or [11], [12]
xor [11], [12]
```

; Only one operand can be Memory

```
mov [11], 44
```

; Size is ambiguous

# Memory – Extending

```
; Move Zero Extend
movzx rax, word [11]
movzx rbx, bx
movzx rbx, cl

; Move Sign Extend
movsx rax, dword [11]
movsx rbx, bx
movsx rbx, cl
```

# PROGRAM SECTIONS

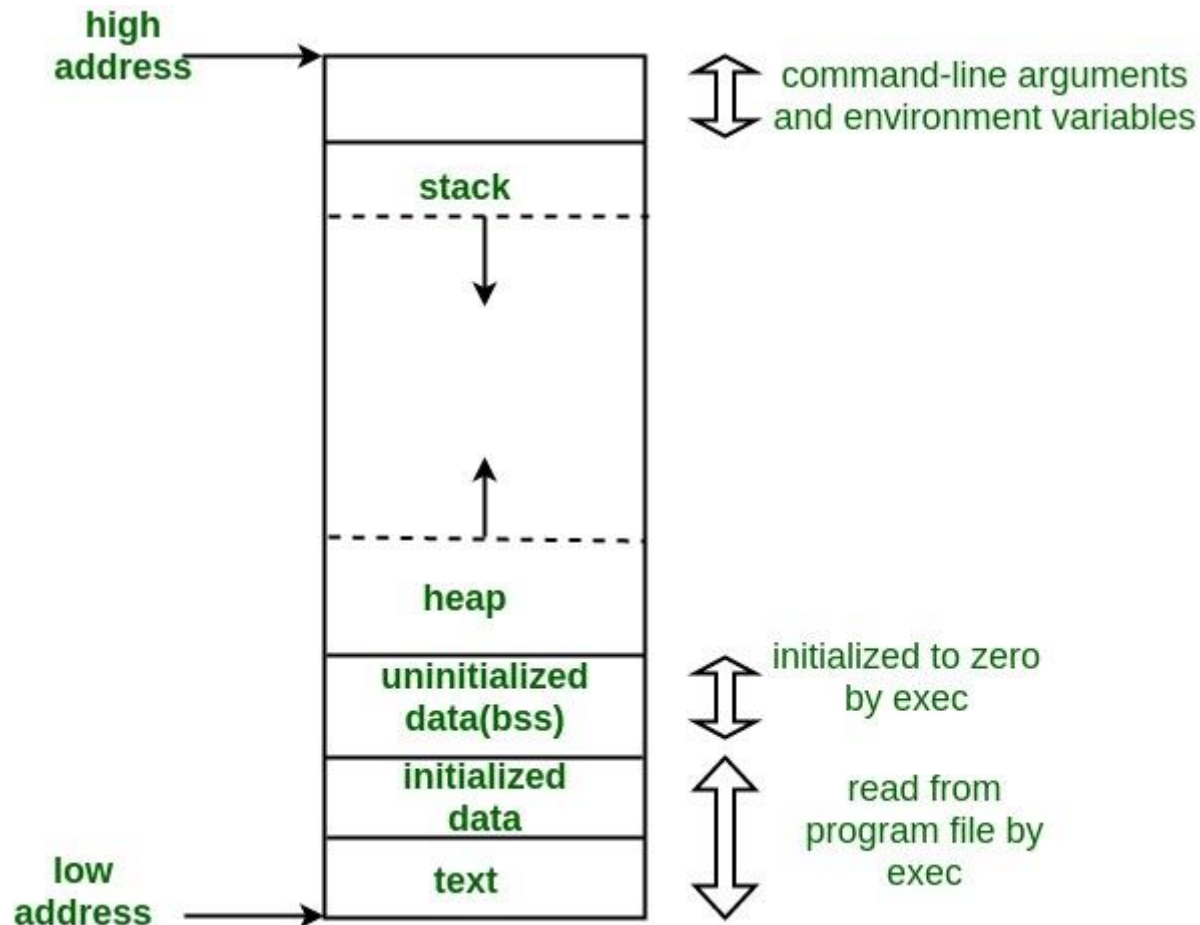
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# Program Sections

- Text (Instructions)
- Data (Global Variables)
- BSS (Global Variables)
- Stack (Return Address, Local Variables, Saved Registers)
- Heap



# Program Sections



<https://www.geeksforgeeks.org/memory-layout-of-c-program/>

# Program Sections

- Modern Programs Still have those five sections but, they are not stored in memory like this.
- You will learn about it in Operating Systems Course.

# LOGIC

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# What is different from High Level Languages?

- Variables
- If & Loop
- Function Calling, Struct, High-Level Programming

# Implementing If

- If is implemented using conditional jumps (branches) and non conditional jumps in all assembly languages
- In amd64 assembly branches depend on side effect of previous instructions
- Implementing if, else, or, and
  - You might want to use logical not of the original condition

# Implementing If

```
        ; if (rax > rbx)
        ;   inc rax
        ; inc rbx

        cmp rax, rbx
        jle end_if
if_cond: inc rax
end_if:  inc rbx
```

# Implementing If, Else

```
        ; if (rax > bax)
        ;   inc rbx
        ; else
        ;   inc rax
        ; dec rcx

        cmp rax, rbx
        jle else_cond
if_cond:    inc rbx
           jmp end_if
else_cond:  inc rax
end_if:    dec rcx
```

# Implementing If (&&)

```
        ; if (rax > rbx && rcx > rdx)
        ;   inc rdx
        ;   dec rcx

        cmp rax, rbx
        jle end_if
        cmp rcx, rdx
        jle end_if
if_cond:    inc rdx
end_if:    dec rcx
```



# Implementing If (||)

```
        ; if (rax > rbx || rcx > rdx)
        ;   inc rdx
        ;   dec rcx

        cmp rax, rbx
        jg if_cond
        cmp rcx, rdx
        jg if_cond
        jmp end_if
if_cond:    inc rdx
end_if:    dec rcx
```

# Implementing Loop

- While loop is the fundamental loop, all others can be created using while
- While Loop: If + Jump

# Implementing Loop

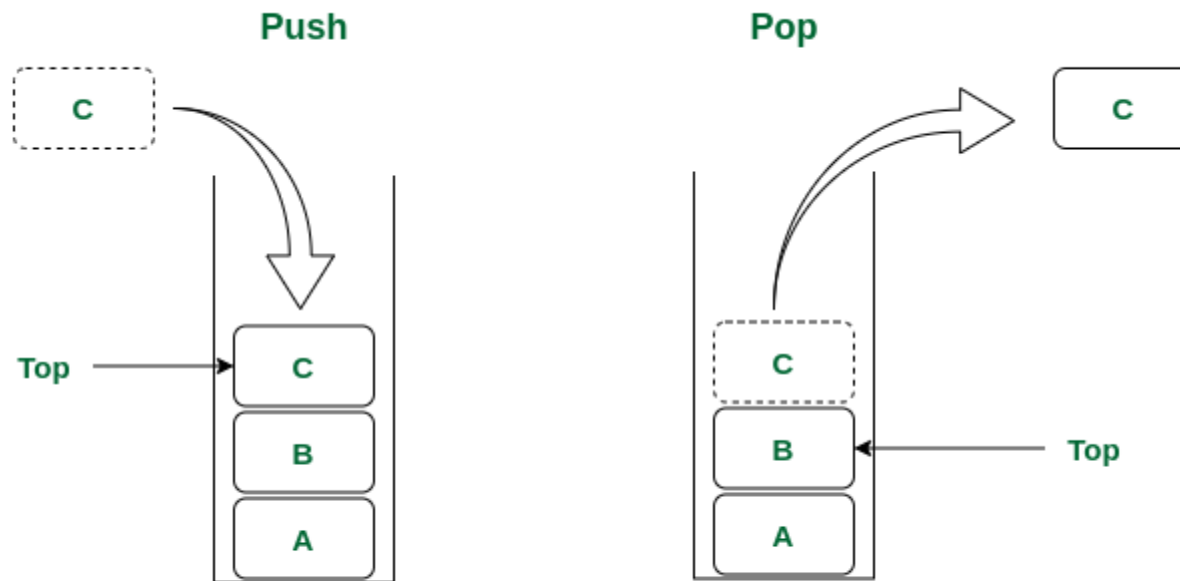
```
        ; while (rax > bax)
        ;   inc rbx
        ; dec rcx

loop_cond: cmp rax, rbx
           jle end_loop
in_loop:   inc rbx
           jmp  loop_cond
end_loop:  dec rcx
```

# STACK

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# Stack



**Stack Data Structure**

# Stack

- Remember stack grows in reverse order in x86
  - Stack size increases when stack pointer (rsp) decreases
- Stack is used to store function return address (Next PC)
  - **call** instruction automatically pushes next PC on top of stack
  - **ret** instructions automatically pops top value of the stack and jumps to it
- Stack is used to store and later load values of registers
  - Using **push** and **pop** instructions
- Stack is used to store local variables
  - By manipulating stack pointer (rsp)
- Stack is used to pass parameters to functions

# MACROS & FUNCTIONS

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# Macros

- Pieces of code that were written before and are added to program
- Like copy and pasting
- Like `#define` functions in C
- Macros are replaced in place



# Functions

- When calling a function program jumps to a different address (call) and later return to it's original execution path (ret)
- Each programming language implements them slightly different from others (calling conventions)
- Functions could be located in different memory locations from the calling program (shared libraries)
- Functions often declare local variables on top of the stack

# Functions

```
swap_function:
    push rcx
    push rdx
    mov rcx, qword [rax]
    mov rdx, qword [rbx]
    mov qword [rax], rdx
    mov qword [rbx], rcx
    pop rdx
    pop rcx
    ret

mov rax, 11
mov rbx, 12
call swap_function
```

# System Calls

- Requests to the operating system
- Execution stops, OS performs requested operation, then return to the program and execution continues

**END OF SLIDES**