

Computer Structure and Language

The 8086/8088 Assembly Language

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1

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Computer Structure & Language, Lecture#2: Data transfer instructions

2

8086/88 has 7 types of instructions:

- 1. Data Transfer Instructions**
- 2. Arithmetic Instructions**
- 3. Bit Manipulation Instructions**
- 4. String Instructions**
- 5. Program Execution Transfer Instructions**
- 6. Processor Control Instructions**
- 7. Interrupt Instructions**

2

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3

The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF

DF

IF

TF

SF

ZF

AF

PF

CF

-

-

-

-

-

-

-

-

-

- Register/Memory to/from Register**

mov

reg1,reg2

≡

reg1 ← (reg2);

mov

reg,memory

≡

reg1 ← (EA);

mov

memory,reg

≡

EA ← (reg);

100010

d w

Md

Reg

R/M

Disp. Low-byte

Disp. High-byte

for 16-bit displacement

Example 1:

mov

ax,si

≡

ax ← (si);

word transfer

Machine code:

10001011

dw

Md

Reg

R/M

11

000

110

≡

8BC6h

dw

Md

Reg

R/M

3

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4

Memory address calculation using Md and R/M fields

Memory mode				Register mode			
Md = 00		Md = 01		Md = 10		Md = 11	
SR : EA		SR : EA		SR : EA		W = 0	W = 1
R/M	000	DS : (BX)+(SI)	DS : (BX)+(SI)+d8	DS : (BX)+(SI)+d16	AL	AX	Reg
	001	DS : (BX)+(DI)	DS : (BX)+(DI)+d8	DS : (BX)+(DI)+d16	CL	CX	
	010	SS : (BP)+(SI)	SS : (BP)+(SI)+d8	SS : (BP)+(SI)+d16	DL	DX	
	011	SS : (BP)+(DI)	SS : (BP)+(DI)+d8	SS : (BP)+(DI)+d16	BL	BX	
	100	DS : (SI)	DS : (SI)+d8	DS : (SI)+d16	AH	SP	
	101	DS : (DI)	DS : (DI)+d8	DS : (DI)+d16	CH	BP	
	110	DS : d16	SS : (BP)+d8	SS : (BP)+d16	DH	SI	
	111	DS : (BX)	DS : (BX)+d8	DS : (BX)+d16	BH	DI	

SR: Segment Register

EA: Effective Address

4

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The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Register/Memory to/from Register**
 - mov reg1,reg2 $\equiv \text{reg1} \leftarrow (\text{reg2});$
 - mov reg,memory $\equiv \text{reg1} \leftarrow (\text{EA});$
 - mov memory,reg $\equiv \text{EA} \leftarrow (\text{reg});$

100010 d w Md Reg R/M Disp. Low-byte Disp. High-byte
for 16-bit displacement

Example 1:

mov ax,si $\equiv \text{ax} \leftarrow (\text{si});$ word transfer

Machine code: 10001011 11 000 110 $\equiv 8\text{BC6h}$

Example 2:

mov cl,ah $\equiv \text{cl} \leftarrow (\text{ah});$ byte transfer

Machine code: 10001000 11 100 001 $\equiv 88\text{E1h}$

5

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The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Register/Memory to/from Register (cont.)**
 - 100010 d w Md Reg R/M Disp. Low-byte Disp. High-byte
for 16-bit displacement

Example 3:

mov cx,word ptr [si-5] $\equiv \text{cx} \leftarrow (\text{M}_{(\text{si}-5)});$ word transfer

Machine code: 10001011 01 001 100 11111011 $\equiv 8\text{B4CFBh}$

Example 4:

mov array[bx+2][di-6],dx $\equiv \text{M}_{\text{array}+(\text{bx})+(\text{di})-4} \leftarrow (\text{dx});$
@array = 260 of word type

Machine code:

10001001 10 010 001 00000000 00000001 $\equiv 89910001\text{h}$

6

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Memory address calculation using Md and R/M fields

		Memory mode			Register mode		
		Md = 00	Md = 01	Md = 10	Md = 11		
		SR : EA	SR : EA	SR : EA	W = 0	W = 1	
R/M	000	DS : (BX)+(SI)	DS : (BX)+(SI)+d8	DS : (BX)+(SI)+d16	AL	AX	000
	001	DS : (BX)+(DI)	DS : (BX)+(DI)+d8	DS : (BX)+(DI)+d16	CL	CX	001
	010	SS : (BP)+(SI)	SS : (BP)+(SI)+d8	SS : (BP)+(SI)+d16	DL	DX	010
	011	SS : (BP)+(DI)	SS : (BP)+(DI)+d8	SS : (BP)+(DI)+d16	BL	BX	011
	100	DS : (SI)	DS : (SI)+d8	DS : (SI)+d16	AH	SP	100
	101	DS : (DI)	DS : (DI)+d8	DS : (DI)+d16	CH	BP	101
	110	DS : d16	SS : (BP)+d8	SS : (BP)+d16	DH	SI	110
	111	DS : (BX)	DS : (BX)+d8	DS : (BX)+d16	BH	DI	111

SR: Segment Register EA: Effective Address

Reg

7

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The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF DF IF TF SF ZF AF PF CF

-	-	-	-	-	-	-	-
---	---	---	---	---	---	---	---

- Immediate to Register/Memory

1100011 w	Md	000 R/M	Disp. Low-byte	Disp. High-byte	Imm. Low-byte	Imm. High-byte
			for 16-bit displacement		for 16-bit (word) data	

Example 1:

mov cx,256h \equiv $cx \leftarrow 256h$; i.e. (ch) = 2 (cl) = 56h

Machine code:

11000111	w	Md	R/M	Data Low	Data High	
11000111		11	000 001	01010110	00000010	\equiv C7C15602h

Example 2:

mov byte ptr [bx+2][si],-2 \equiv $M_{(bx)+(si)+2} \leftarrow -2$; byte transfer

Machine code:

11000110	w	Md	R/M	Disp. Low	Data Low	
11000110		01	000 000	00000010	11111110	\equiv C64002FEh

8

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The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Immediate to Register**

1011 w reg Imm. Low-byte Imm. High-byte
If w=1

Example 1:

```
mov dx,-1    ≡ dx ← FFFFh;    word transfer
```

Machine code:

w	reg	Data Low	Data High	
1011	1 010	11111111	11111111	≡ BAFFFFh

Example 2:

```
mov bh,100   ≡ bh ← 100;    byte transfer
```

Machine code:

w	reg	Data Low	
1011	0 111	01100100	≡ B764h

9

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The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Memory to Accumulator**

```
mov al,address    ≡ al ← (Maddress)byte;    if w=0
mov ax,address    ≡ ax ← (Maddress)word;    if w=1
```

1010000 w Address Low Address High

Example 1:

```
mov ax,array+10   ≡ ax ← (Marray+10)word; @array = 1000h
```

Machine code:

w	Addr. Low	Addr. High	
10100001	00001010	00010000	≡ A10A10h

10

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The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Accumulator to Memory**

$\text{mov address, al} \equiv M_{\text{address}} \leftarrow (\text{al}); \quad \text{if } w=0$
 $\text{mov address, ax} \equiv M_{\text{address}} \leftarrow (\text{ax}); \quad \text{if } w=1$

1010001 w Address Low Address High

Example 1:

$\text{mov array-2, al} \equiv M_{\text{array-2}} \leftarrow (\text{al}); \quad @\text{array} = 1000\text{h}$

Machine code: 10100010 11111110 00001111 $\equiv \text{A2FE0Fh}$

11

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The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Register/Memory to Segment register**

10001110 Md 0SR R/M Disp. Low-byte Disp. High-byte
for 16-bit displacement

Segment Register	SR
ES	00
CS	01
SS	10
DS	11

Example 1:

$\text{mov ES, AX} \equiv \text{ES} \leftarrow (\text{AX});$

Machine code: 10001110 11 000 000 $\equiv \text{8EC0h}$

Example 2:

$\text{mov SS, array[bx]} \equiv \text{SS} \leftarrow (M_{\text{array}+(\text{bx})}); \quad @\text{array} = 100$

Machine code: 10001110 01 010 111 01100100 $\equiv \text{8E5764h}$

12

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The 8086/88's Data Transfer Instructions:

Move Instructions: mov

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Segment Register to Register/Memory**

10001100	Md	0SR	R/M	Disp. Low-byte	Disp. High-byte
for 16-bit displacement					

Segment Register	SR
ES	00
CS	01
SS	10
DS	11

Example 1:

mov AX,ES \equiv AX \leftarrow (ES);

Machine code: 10001100 ^{Md SR R/M} 11 000 000 \equiv 8CC0h

Example 2:

mov myarr+3[di],DS \equiv M_{myarr+(di)+3} \leftarrow (DS); @myarr = 500h

Machine code:

10001100 ^{Md SR R/M Disp. Low Disp. High} 10 011 101 00000011 00000101 \equiv 8C9D0305h

13

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The 8086/88's Data Transfer Instructions:

Exchange Instructions: xchg

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Register/Memory with Register**

1000011 w	Md	Reg	R/M	Disp. Low-byte	Disp. High-byte
for 16-bit displacement					

Example 1:

xchg si,bx \equiv (si) \leftrightarrow (bx);

Machine code: ^{w Md Reg R/M} 10000111 11 110 011 \equiv 87F3h

Example 2:

xchg byte ptr cam+30[si],dl \equiv (M_{cam+(si)+30}) \leftrightarrow (dl);
@cam = 500h

Machine code:

^{w Md Reg R/M Disp. Low Disp. High} 10000110 10 010 100 00011110 00000101 \equiv 86941E05h

14

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The 8086/88's Data Transfer Instructions:

Exchange Instructions: xchg

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- Register with Accumulator

10010 Reg

Example:

xchg ax,bx $\equiv (ax) \leftrightarrow (bx);$

Machine code: 10010 011 \equiv 93h

15

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The 8086/88's Data Transfer Instructions:

Special Instructions:

- Translate Byte to AL: XLAT

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

11010111

xlat $\equiv al \leftarrow (M_{(bx)+(al)});$

- Load Effective Address: LEA

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

10001101 Md Reg R/M Disp. Low-byte Disp. High-byte
for 16-bit displacement

Example:

lea ax,[di][bx-1] $\equiv ax \leftarrow (di)+(bx)-1;$

Machine code:

10001101 01 000 001 11111111 \equiv 8D41FFh

16

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The 8086/88's Data Transfer Instructions:

Special Instructions:

- Load Pointer to DS: LDS**

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

11000101 Md Reg R/M Disp. Low-byte Disp. High-byte
for 16-bit displacement

Example 1:

$$\text{l ds} \quad \text{bx}, \text{p1}[\text{si}] \quad \equiv \quad \text{bx} \leftarrow (\text{M}_{\text{p1}+(\text{si})}); \text{DS} \leftarrow (\text{M}_{\text{p1}+(\text{si})+2});$$

@p1 = 1234h

Machine code:

	Md	Reg	R/M	Disp. Low	Disp. High	
11000101	10	011	100	00110100	00010010	\equiv C59C3412h

17

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The 8086/88's Data Transfer Instructions:

Special Instructions:

- Load Pointer to ES: LES**

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

11000100 Md Reg R/M Disp. Low-byte Disp. High-byte
for 16-bit displacement

Example 1:

$$\text{les} \quad \text{bx}, \text{pip}+10[\text{di}] \quad \equiv \quad \text{bx} \leftarrow (\text{M}_{\text{pip}+(\text{di})+10}); \text{ES} \leftarrow (\text{M}_{\text{pip}+(\text{di})+2+10});$$

@pip = 1234h

Machine code:

	Md	Reg	R/M	Disp. Low	Disp. High	
11000100	10	011	101	00111110	00010010	\equiv C49D3E12h

18

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The 8086/88's Data Transfer Instructions:

Special Instructions:

- **Load AH with Flags: LAHF**

10011111

lahf $\equiv ah \leftarrow (F_{7..0});$

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- **Store AH into Flags: SAHF**

10011110

sahf $\equiv F_{7..0} \leftarrow (ah);$

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	r	r	r	r	r

r: restored from prev. saved value

19

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The 8086/88's Data Transfer Instructions:

Push Instructions: PUSH

OF	DF	IF	TF	SF	ZF	AF	PF	CF
-	-	-	-	-	-	-	-	-

- **Push Register/Memory into Stack**

11111111 Md 110 R/M Disp. Low-byte Disp. High-byte
for 16-bit displacement

Example 1:

push dx $\equiv sp \leftarrow (sp)-2; M_{(sp)} \leftarrow (dx);$

Machine code: 11111111 ^{Md} 11 ^{R/M} 110 010 \equiv FFF2h

Example 2:

push array+2 $\equiv sp \leftarrow (sp)-2; M_{(sp)} \leftarrow (M_{array+2});$
@array = 11h of word type

Machine code:

11111111 ^{Md} 00 ^{R/M} 110 ^{Disp. Low} 110 ^{Disp. High} 00010011 00000000 \equiv FF361300h

20

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The 8086/88's Data Transfer Instructions:

Push Instructions: PUSH (cont.)

OF DF IF TF SF ZF AF PF CF
 - - - - - - - -

- Push Register into Stack** 01010 Reg

Example: `push dx` \equiv $sp \leftarrow (sp)-2; M_{(sp)} \leftarrow (dx);$

Machine code: `01010 010` \equiv 52h

- Push Segment Register into Stack** 000 SR 110

Example: `push DS` \equiv $sp \leftarrow (sp)-2; M_{(sp)} \leftarrow (DS);$

Machine code: `000 11 110` \equiv 1Eh

Segment Register	SR
ES	00
CS	01
SS	10
DS	11

- Push Flags into Stack: PUSHF** 10011100

`pushf` \equiv $sp \leftarrow (sp)-2; M_{(sp)} \leftarrow (F_{15..0});$

21

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The 8086/88's Data Transfer Instructions:

Pop Instructions: POP

OF DF IF TF SF ZF AF PF CF
 - - - - - - - -

- Pop Register/Memory from Stack**

10001111 Md 000 R/M Disp. Low-byte Disp. High-byte
for 16-bit displacement

Example 1:

`pop dx` \equiv $dx \leftarrow (M_{(sp)}); sp \leftarrow (sp)+2;$

Machine code: `10001111 11 000 010` \equiv 8FC2h

Example 2:

`pop arc-2` \equiv $M_{arc-2} \leftarrow (M_{(sp)}); sp \leftarrow (sp)+2;$
 @arc = 100h of word type

Machine code:

`10001111 00 000 110 11111110 00000000` \equiv 8F06FE00h

22

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23

The 8086/88's Data Transfer Instructions:

Pop Instructions: POP (cont.)

• Pop Register from Stack

01011 Reg

OF

DF

IF

TF

SF

ZF

AF

PF

CF

-

-

-

-

-

-

-

-

-

Example:

pop dx ≡ dx ← (M_(sp)); sp ← (sp)+2;

Machine code: 01011 010^{Reg} ≡ 5Ah

• Pop Segment Register from Stack

000 SR 111

Segment Register	SR
ES	00
CS	01
SS	10
DS	11

Example:

pop DS ≡ DS ← (M_(sp)); sp ← (sp)+2;

Machine code: 000 11 111^{SR} ≡ 1Fh

• Pop Flags from Stack: POPF

10011101

OF

DF

IF

TF

SF

ZF

AF

PF

CF

r

r

r

r

r

r

r

r

popf ≡ F_{15..0} ← (M_(sp)); sp ← (sp)+2;

23

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24

End of Slides

24

12