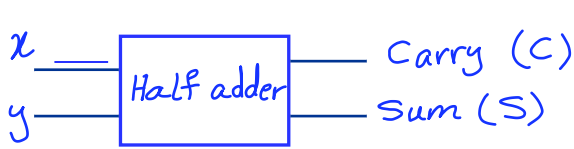


Adders and Subtractors

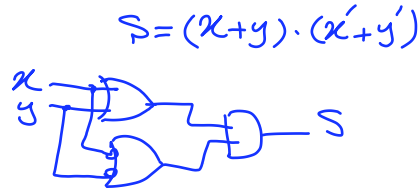
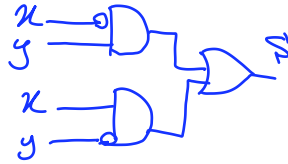
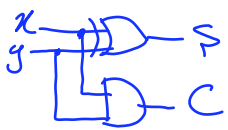
جمع کننده (Adder)



x	y	C	S
0	0	0	0
0	1	0	1
1	0	0	1
1	1	1	0

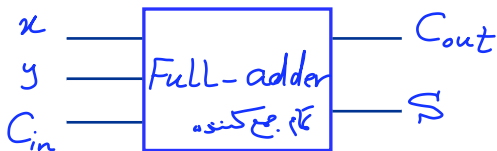
$$S = x'y + xy' = x \oplus y$$

$$C = x \cdot y$$



$$S = (x+y) \cdot (x'+y')$$

$$\begin{array}{r} C_2 \quad C_1 \\ a_1 a_0 \\ + b_1 b_0 \\ \hline S_1 S_0 \end{array}$$



x	y	Cin	Cout	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

xy	00	01	11	10
Cin	0	1	0	1
1	1	0	1	0

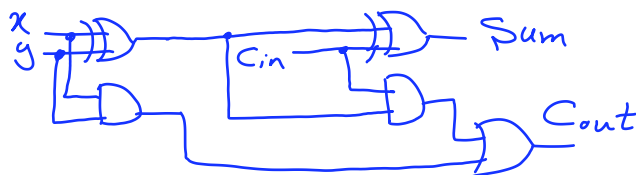
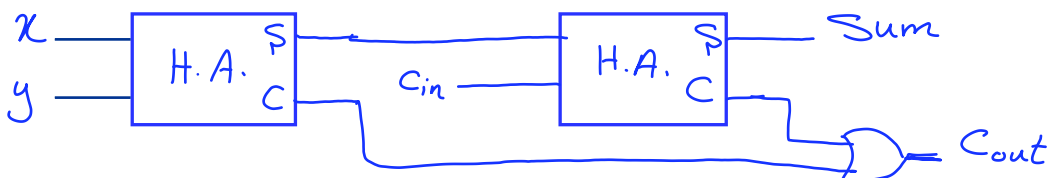
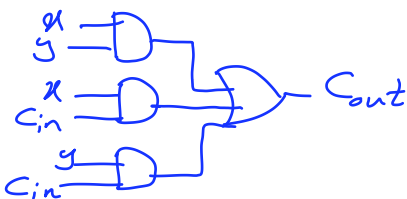
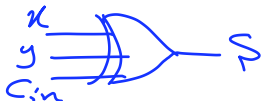
$$S = x'y'C_{in} + x'yc'_{in} + xyC_{in} + xy'c'_{in}$$

$$S = x \oplus y \oplus C_{in} \quad (\text{تعداد فرد "1"})$$

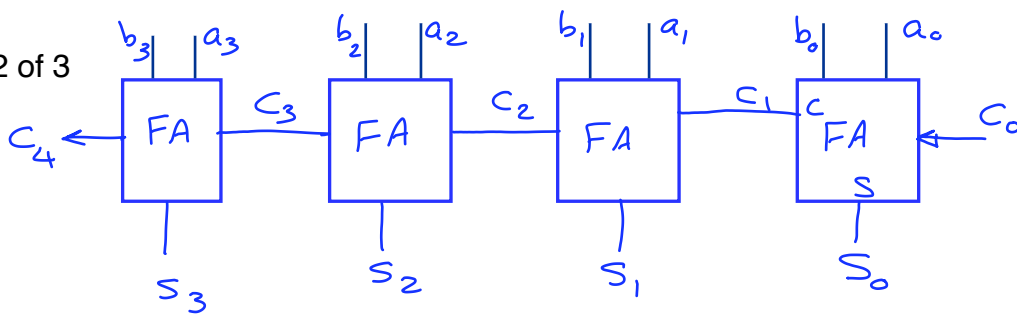
xy	00	01	11	10
Cin	0	0	1	0
1	0	1	1	1

$$C_{out} = yC_{in} + xC_{in} + xy \quad + \cancel{xyC_{in}}$$

تابع اکثریت = majority function



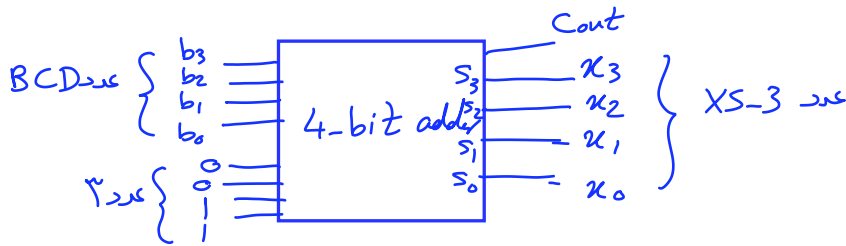
Full-Adder 7480
7482
4-bit ← 74283
IC: Integrated circuit



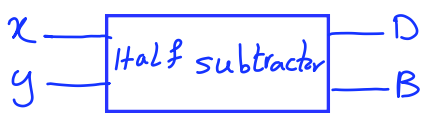
$$\begin{array}{r} c_1 c_0 \\ a_3 a_2 a_1 a_0 \\ + b_3 b_2 b_1 b_0 \\ \hline c_4 s_3 s_2 s_1 s_0 \end{array}$$

4-bit adder

مثال: مداری طرح کنید که BCD را به کد مازاد (Excess-3) تبدیل کند.

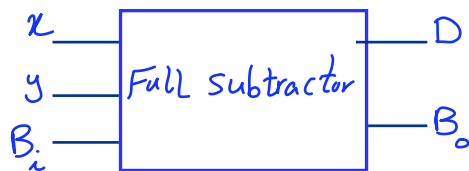


$$BCD + "3" = XS-3$$



$$D = x'y + xy' = x \oplus y$$

$$B = xy$$



xy	B	D
00	0	0
01	1	1
10	0	1
11	0	0

تفریق کتبه (subtractor)

$$\begin{array}{r} B \\ x \\ - y \\ \hline \text{Difference} \end{array}$$

borrow

$$\begin{array}{r} B \\ a_1 a_0 \\ - b_1 b_0 \\ \hline d_0 \end{array}$$

xy Bi	Bo	D
000	0	0
001	1	1
010	1	1
011	1	0
100	0	1
101	0	0
110	0	0
111	1	1

$$D = \bar{x}\bar{y}B_i + \bar{x}y\bar{B}_i + x\bar{y}\bar{B}_i + xyB_i$$

$$D = x \oplus y \oplus B_i$$

$$B_o = \bar{x}y + \bar{x}B_i + yB_i$$

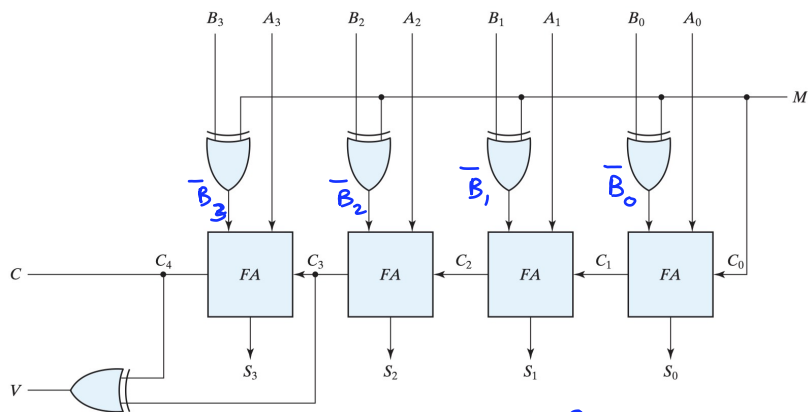


FIGURE 4.13
 Four-bit adder-subtractor (with overflow detection)

$$\begin{array}{r} -3 \\ -6 \\ \hline -9 \end{array}$$

$$\begin{array}{r} 0000 \\ 1101 \\ \hline 1010 \\ 0111 \end{array}$$

$$\begin{array}{r} c_4 \ c_3 \\ 0011 \\ 0010 \\ + 0110 \\ \hline 1000 \end{array}$$

$$\begin{array}{r} +2 \\ +6 \\ \hline +8 \end{array}$$

$$M = \begin{cases} 0 \rightarrow \text{عمل جمع} \\ 1 \rightarrow \text{عمل تفریق} \end{cases}$$

$$a \oplus 1 = a'$$

$$a' \oplus 1 = a$$

مکمل ۲ : تمام بیت ها NOT می شود + 1

$$\begin{array}{r} 1 \\ 1001 \\ + 1011 \\ \hline 10100 \end{array} \quad \begin{array}{r} 9 \\ + 11 \\ \hline 20 \end{array}$$

overflow = سرریز

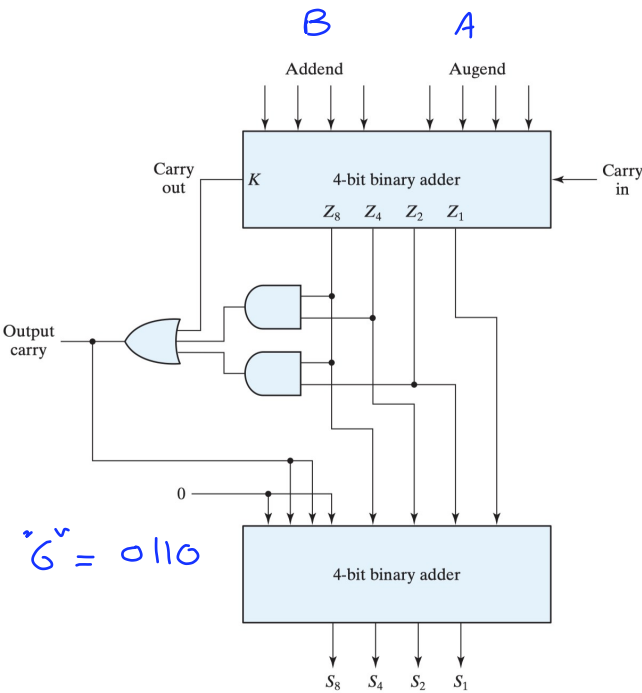


FIGURE 4.14
 Block diagram of a BCD adder

$$\begin{array}{r} z_8 z_4 z_2 z_1 \\ 1001 \\ 1010 \\ 1011 \\ 1100 \\ 1101 \\ 1110 \\ 1111 \end{array} \left| \begin{array}{l} 9 \\ 10 \\ 11 \\ 12 \\ 13 \\ 14 \\ 15 \end{array} \right. \quad z_8 z_4 + z_8 z_2$$