

## Digital System Design

**Hajar Falahati** 

hfalahati@ipm.ir
hfalahati@ce.sharif.edu

## Design Session 3

## Carry Look Ahead (CLA) Adder

- Steps:
  - Sketch block diagram and implement 1-bit full adder
  - Sketch block diagram and implement 4-bit CLA adder
- Model level
  - Behavioral
  - Dataflow
- How to deliver
  - Submit at CW



## Thank You

