

12V SLA BATTERY CHARGER

I/O TERMINAL

U4
1-215307-0

1	1	20	20
VUSB	2	19	VIN
A11	3	18	VEXT
A10	4	17	
A9	5	16	VINSTR
A8	6	15	
A6	7	14	A7
A4	8	13	A5
A2	9	12	A3
A0	10	11	A1
	10		

U5
1-215307-0

TX1	1	20	20
TX2	2	19	RX2
TX3	3	18	RX3
D12	4	17	D42
D10	5	16	D11
D8	6	15	D9
D6	7	14	D7
D4	8	13	D5
D2	9	12	D3
RX0	10	11	TX0
	10		

U6
1-215307-0

SDA	1	20	20
D34	2	19	D35
D32	3	18	D33
D30	4	17	D31
D28	5	16	D29
D26	6	15	D27
D24	7	14	D25
D22	8	13	D23
SCK	9	12	SS
MISO	10	11	MOSI
	10		

H5
MOUNT-PAD-ROUND2.8

H6
MOUNT-PAD-ROUND2.8

H7
MOUNT-PAD-ROUND2.8

H8
MOUNT-PAD-ROUND2.8

GND

PROTOTYPE

U1
1-215307-0

GND ← 1
VUSB 2
A11 3
A10 4
A9 5
A8 6
A6 7
A4 8
A2 9
A0 10

20 20
19 19
18 18
17 17
16 16
15 15
14 14
13 13
12 12
11 11

VIN_ → GND
VEXT
VINSTR → +5V
A7
A5
A3
A1

U2
1-215307-0

TX1 1
TX2 2
TX3 3
D12 4
D10 5
D8 6
D6 7
D4 8
D2 9
RX0 10

20 20
19 19
18 18
17 17
16 16
15 15
14 14
13 13
12 12
11 11

RX1
RX2
RX3
D42
D11
D9
D7
D5
D3
TX0

U3
1-215307-0

SDA 1
D34 2
D32 3
D30 4
D28 5
D26 6
D24 7
D22 8
SCK 9
MISO 10

20 20
19 19
18 18
17 17
16 16
15 15
14 14
13 13
12 12
11 11

SCL
D35
D33
D31
D29
D27
D25
D23
SS
MOSI

POWER VOLTMETERS

The image displays two circuit diagrams for power voltmeters on the M3_Shield_Solar_V1.2 board.

Top Diagram (TP4): This circuit is powered by an 18V-CELL. The signal path includes a resistor R11 (1.5M) and a test point TP4. The output is taken from node A11, which is connected to a resistor R12 (300K) and a capacitor C18 (1nF) to ground. The output is connected to a voltmeter (V).

Bottom Diagram (TP5): This circuit is powered by a 12V-BAT. The signal path includes a resistor R13 (1M) and a test point TP5. The output is taken from node A10, which is connected to a resistor R14 (510K) and a capacitor C19 (1nF) to ground. The output is connected to a voltmeter (V).