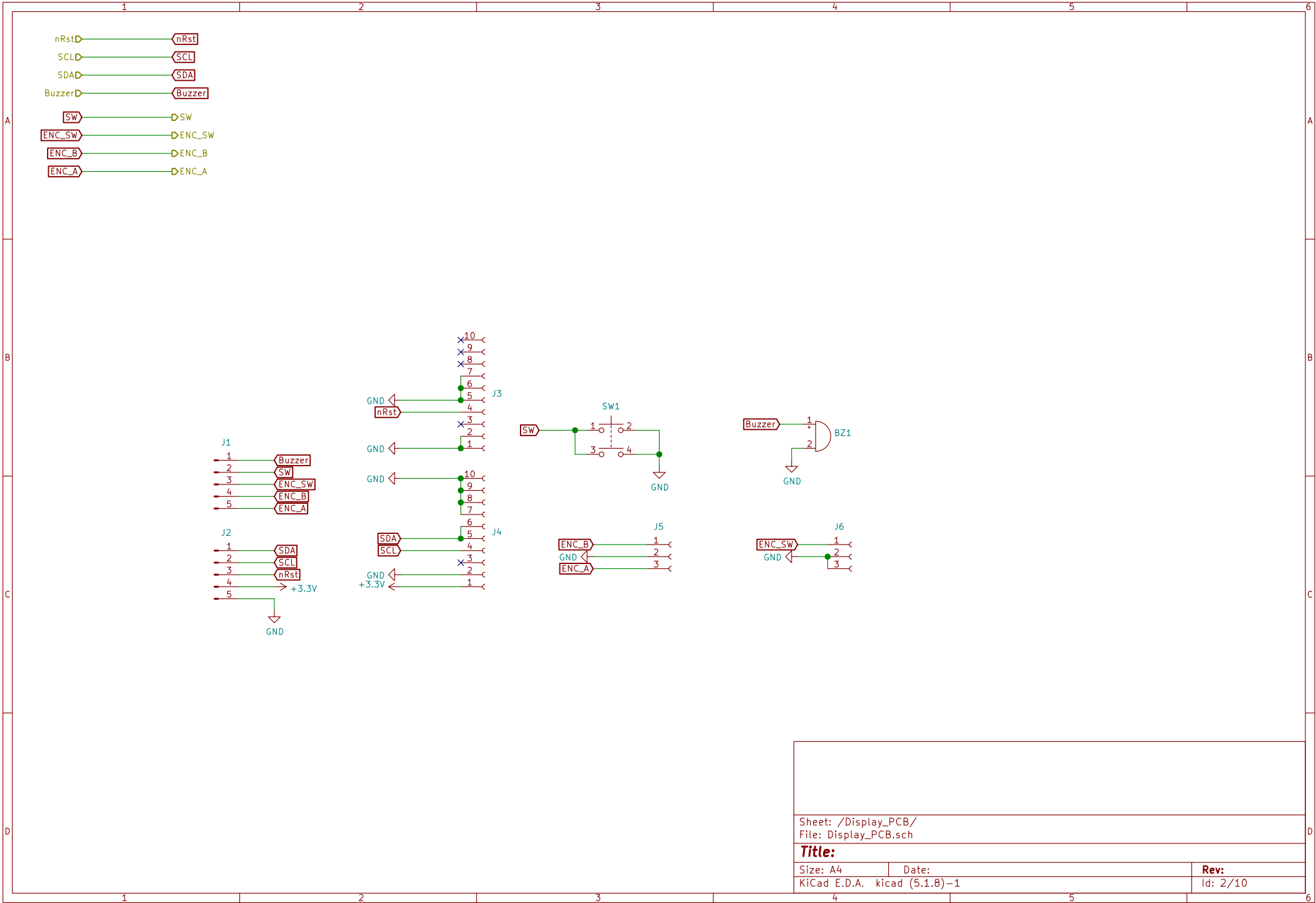


Sheet: /		
File: Gustl_sum_eck.sch		
Title:		
Size: A4	Date: 2021-01-11	Rev:
KiCad E.D.A. kicad (5.1.8)-1		Id: 1/10



Sheet: /Display_PCB/
File: Display_PCB.sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad (5.1.8)-1

Rev:

Id: 2/10

Sheet: /ESC_left/ File: ESC.sch	
Title:	
Size: A4	Date:
KiCad E.D.A. kicad (5.1.8)–1	Rev: Id: 3/10

The image shows a blank sheet of paper with a grid. The grid has columns labeled 1 through 6 at the top and rows labeled A, B, C, and D on the left side. In the bottom right corner, there is a title block containing the following information:

Sheet: /BLDC_left/ File: BLDC.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.8)-1	Id: 4/10	



	1	2	3	4	5	6
A						
B						
C						
D						
	1	2	3	4	5	6

Sheet: /Sheet5FF84A43/ File: Master_Bec_Boy.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.8)-1		Id: 6/10



Sheet: /ESC_right/
File: ESC.sch

Title:

Size: A4

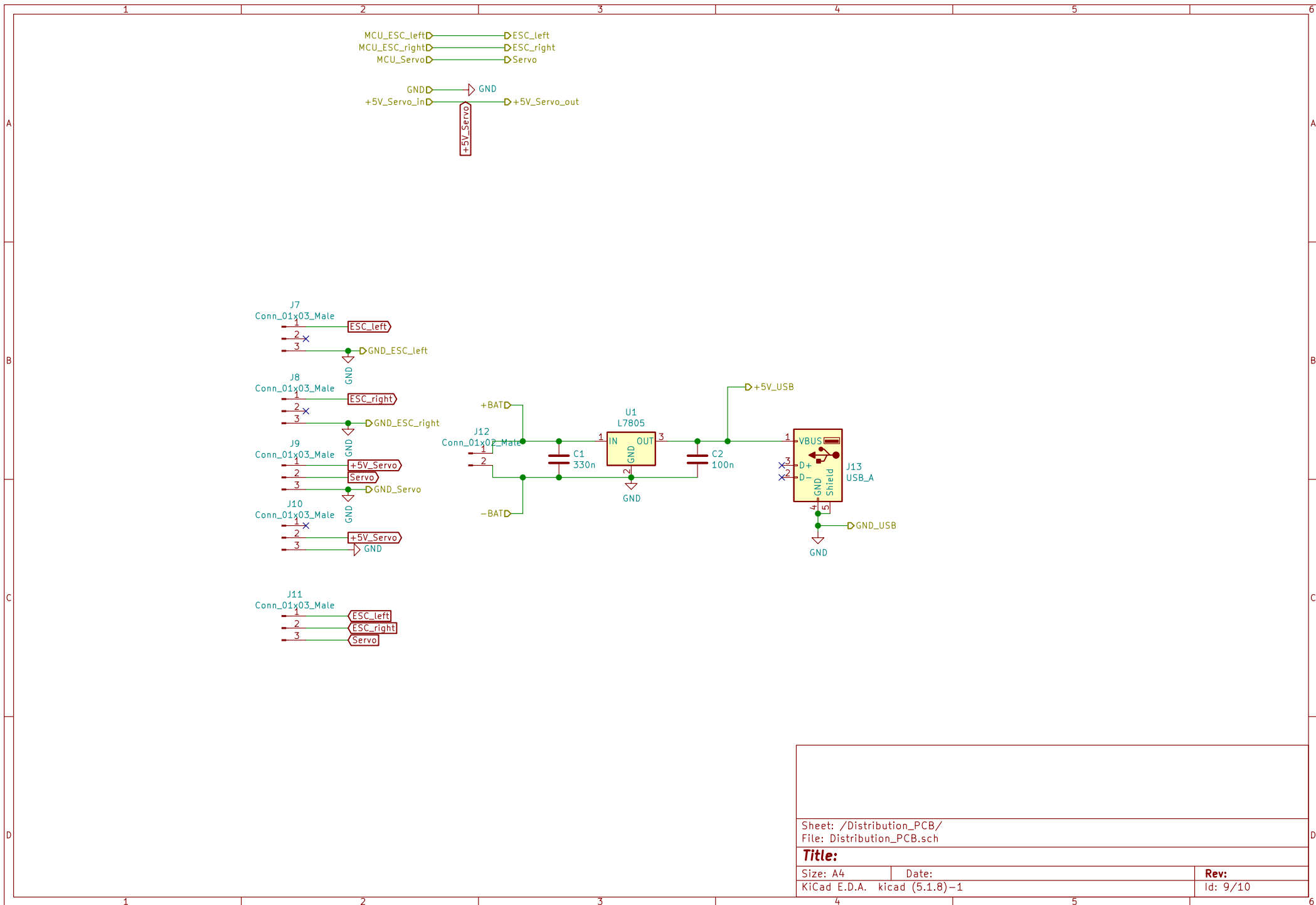
Date:

KiCad E.D.A. kicad (5.1.8)-1

Rev:

Id: 7/10

[illegible]



Sheet: /Distribution_PCB/
File: Distribution_PCB.sch

Title:

Size: A4
KiCad E.D.A. kicad (5.1.8)-1

Date:

Rev:
Id: 9/10

1	2	3	4	5	6
A					A
B					B
C					C
D					D
1	2	3	4	5	6

Sheet: /MCU/ File: LPCXpresso.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.8)-1		Id: 10/10