**Comparisons:**

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
|  | **A 7T Security Oriented SRAM Bit cell** | | | | | |
|  | 6T SRAM (Single Bit)  Conventional | 7T SRAM (Single Bit)  existing | 6T SRAM (Single Bit)  Conventional | 7T SRAM (Single Bit)  Proposed | 6T SRAM (8-Bit)  Conventional | 7T SRAM (8-Bit)  Proposed |
| MOSFETS | 8 | 12 | 8 | 12 | 64 | 96 |
| Delay (ns) | - | - | 21 | 20.1 | 81 | 81 |
| Power (uW) | - | - | 1.33 | 2.06 | 10.6 | 4.7 |
| Technology (nm) | 28 | 28 | 22 | 22 | 22 | 22 |
| Input Voltage (V) | 0.9 | 0.9 | 0.4 | 0.4 | 0.4 | 0.4 |
| Area(mm) | 0.224 | 0.336 | 0.176 | 0.264 | 1.408 | 2.112 |