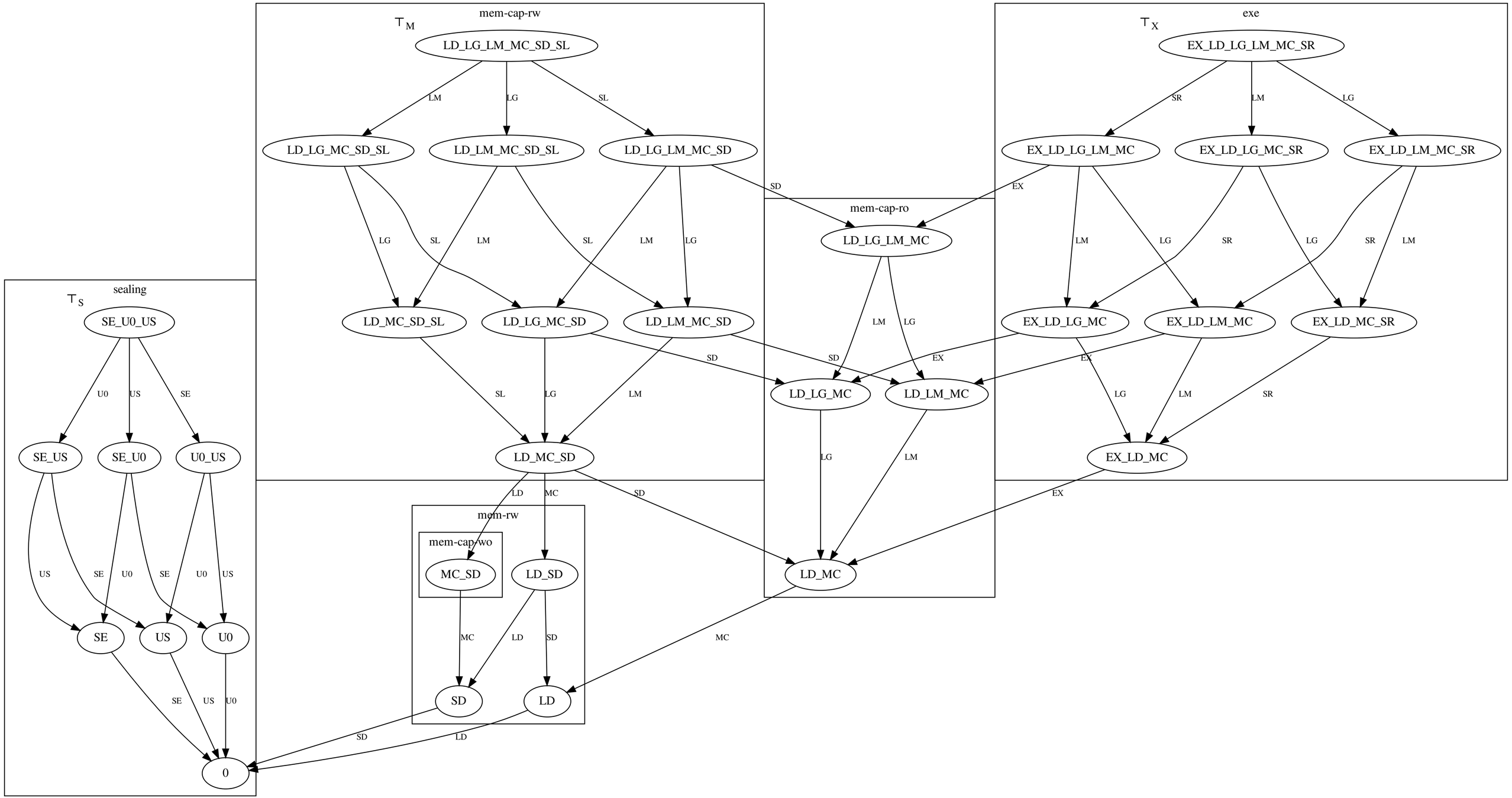
mem-cap-rw
$\top_M$
LD_LG_LM_MC_SD_SL
LM
LG
SL
LD_LG_MC_SD_SL
LD_LM_MC_SD_SL
LD_LG_LM_MC_SD
SD
LG
SL
LM
SL
LM
LG
LD_MC_SD_SL
LD_LG_MC_SD
LD_LM_MC_SD
SD
SL
LG
LM
LD_MC_SD
exe
$\top_X$
EX_LD_LG_LM_MC_SR
SR
LM
LG
EX_LD_LG_LM_MC
EX_LD_LG_MC_SR
EX_LD_LM_MC_SR
EX
LM
LG
SR
LG
SR
LM
EX_LD_LG_MC
EX_LD_LM_MC
EX_LD_MC_SR
EX
EX
LG
LM
SR
EX_LD_MC
EX
mem-cap-ro
LD_LG_LM_MC
LM
LG
SD
EX
LD_LG_MC
LD_LM_MC
LG
LM
LD_MC
MC
sealing
$\top_S$
SE_U0_US
U0
US
SE
SE_US
SE_U0
U0_US
US
SE
U0
SE
U0
US
SE
US
U0
SE
US
U0
0
LD
MC
SD
mem-rw
mem-cap-wo
MC_SD
LD_SD
MC
LD
SD
SD
LD
SD
LD