

**ARM<sup>®</sup> Cortex<sup>®</sup>-M4**  
**32-bit Microcontroller**

**M480 Series CMSIS BSP**  
**Revision History**

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**Revision 3.01.000** (Released 2017-09-29)

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1. Reorganized register definition header files.
2. Added new standard driver reference samples..
3. Added Eclipse project support.
4. Renamed SPI0 to QSPI0, SPI1 to SPI0, SPI2 to SPI1, SPI3 to SPI2, SPI4 to SPI3, ACMP to ACMP01.
5. Rewrote USBH library to optimize memory usage.
6. Minor bug fix.

**Revision 3.00.000** (Released 2017-06-15)

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1. Initial release.

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