

ARM[®] Cortex[®]-M4
32-bit Microcontroller

M480 Series CMSIS BSP
Revision History

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Revision 3.02.000 (Released 2018-03-20)

1. Updated sample codes to use PB12(RX) and PB13(TX) instead of PD2(RX) and PD3(TX) as console pins for NuMaker-PFM-M487 v3.0 board.
2. Updated FatFs to r0.13a.
3. Updated FreeRTOS to v10.0.0.
4. Updated lwIP to v2.0.3.
5. Added a USBH CDC class driver and a CDC class VCOM sample.
6. Added mbed TLS and test suites using crypto accelerator.
7. Minor bug fix.

Revision 3.01.000 (Released 2017-09-29)

1. Reorganized register definition header files.
2. Added new standard driver reference samples.
3. Added Eclipse project support.
4. Renamed SPI0 to QSPI0, SPI1 to SPI0, SPI2 to SPI1, SPI3 to SPI2, SPI4 to SPI3, ACMP to ACMP01.
5. Rewrote USBH library to optimize memory usage.
6. Minor bug fix.

Revision 3.00.000 (Released 2017-06-15)

1. Initial release.

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