**Homework 8**

**Page 161. Chinese textbook**

**Question 6.1**

A lamp is supplied by a single-phase AC voltage controller. Suppose this lamp is a resistor load, the output power reaches maximum value when Try to find the delay angle  when the output power is 80% and 50% of maximum output power.

**Question 6.2**

An AC voltage controller is supplied by a 50Hz-220V voltage source, and its load is serried resistor and inductor where *R* = 0.5 and *L*=2mH. Try to derive:

1. Available range of delay angle **
2. Maximum value of RMS value of load current,
3. Maximum value output power together with power factor on the source side,
4. When /2, the RMS value of current in the thyristor, the conduction angle of thyristor and power factor on the source side.

**Question 6.4**

What is the highest output frequency of an AC-AC converter? Which factor limits the output frequency?

**Answer 6.1**

When phase angle **°, the output voltage is at its maximum value.

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Meanwhile, the current of load is at its maximum value.



Therefore, the maximum value of the output power is



When the output power is 80%,



The solution is



When the output power is 50%,



The solution is



**Answer 6.2**

(1)



The solution is



(2)

When the phase angle ****the current is continuous at its maximum and the conduction angle ***π*.



(3)





(4)

When the phase angle **



The derivation of the above formula is



With the above two equations, sum of square of each of them are



Then we get the value of **



The RMS value of current in the thyristor is



The power factor on the source side is



**Answer 6.4**

The more pulse AC to AC converter has, the higher frequency of output signal has. For the 6-pulse 3-phase bridge converter, the maximum frequency is below 1/3~1/2 of the grid frequency. When the grid frequency is 50Hz, the maximum value of output frequency is about 20Hz.

When the output frequency increases, the number of grid voltage segments decreases in the single cycle of output voltage, so the waveform distortion is serious. And the waveform distortion of voltage and the waveform distortion of current due to the waveform distortion of voltage is the main reason to limit the output frequency.