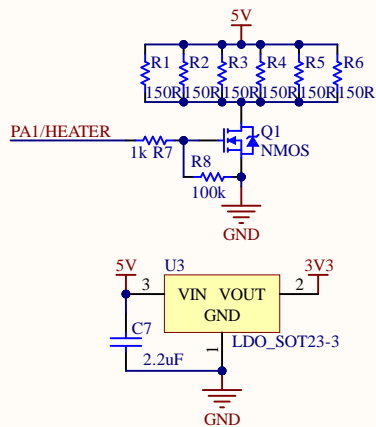
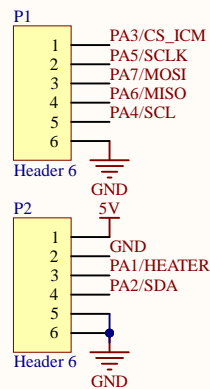


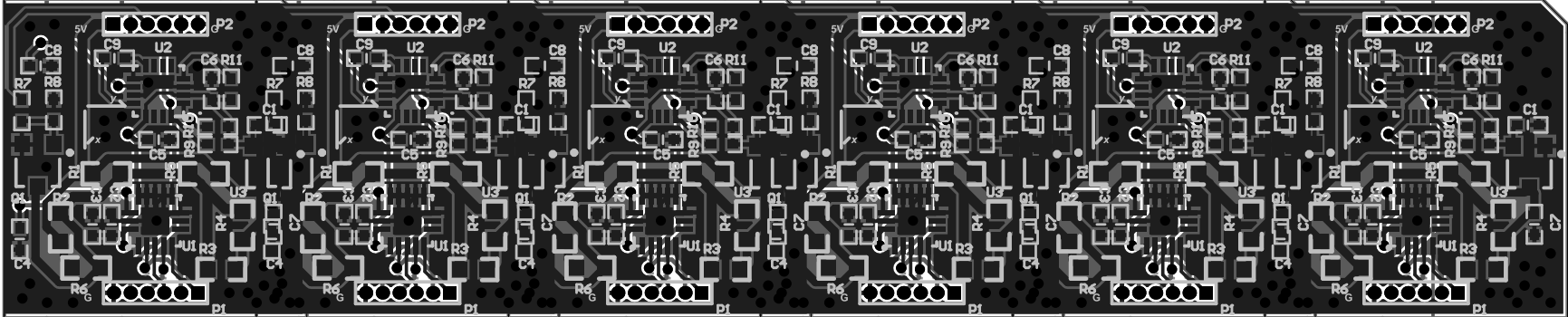
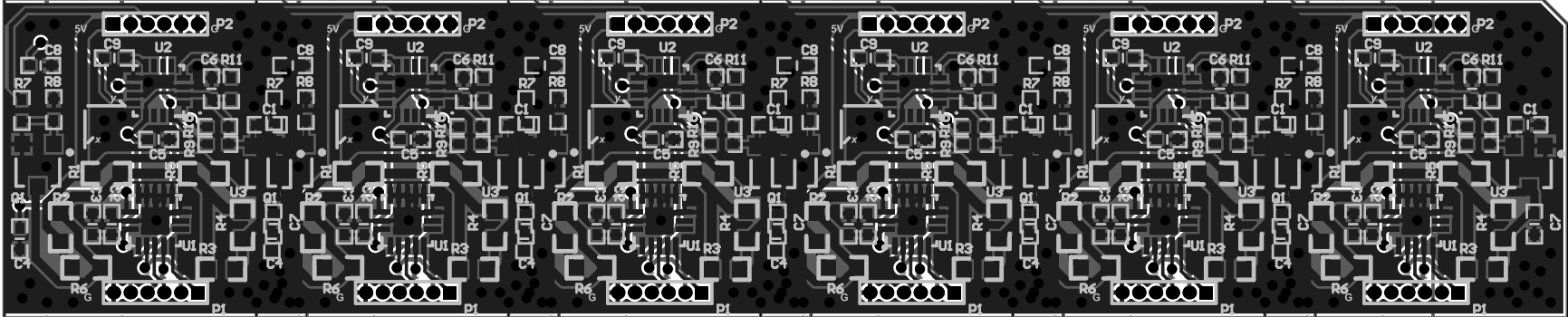
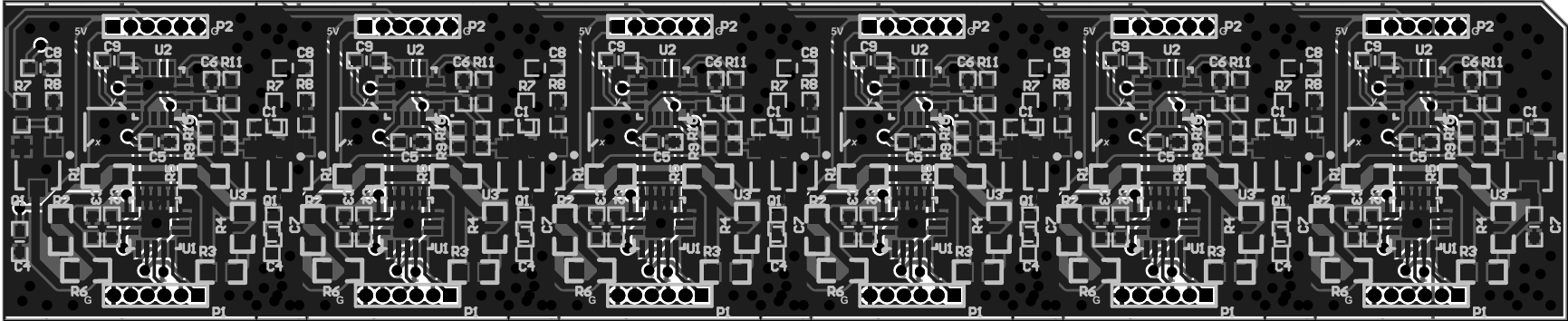
Slave Address Select			
CAD1	CAD0	Address (7-bit)	Address (8-bit)
VSS	VSS	0CH	18H
VSS	VDD	0DH	1AH
VDD	VSS	0EH	1CH
VDD	VDD	0FH	1EH

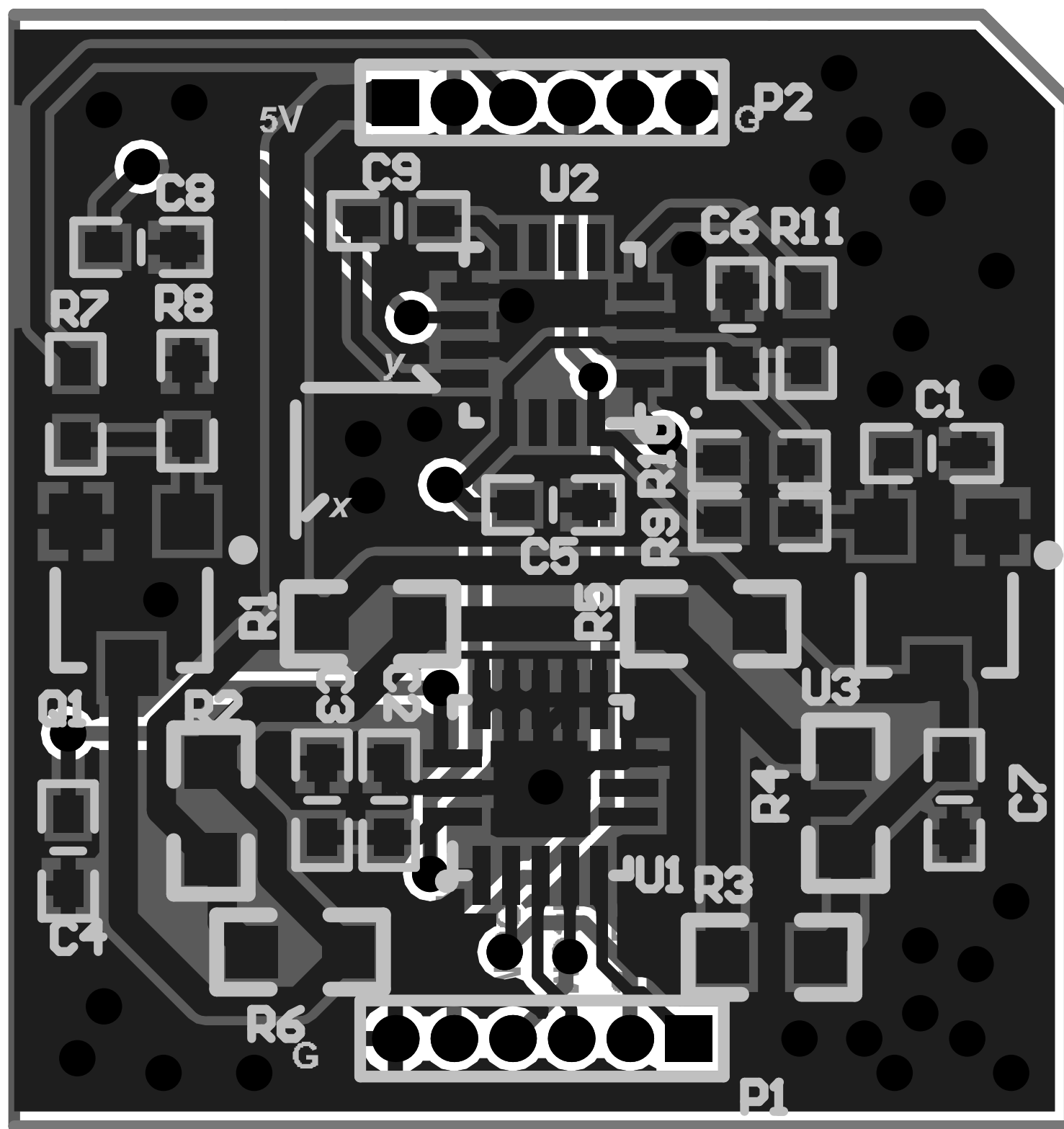
*if CAD1 and CAD0 are floating, I²C address will be 0EH/1CH.

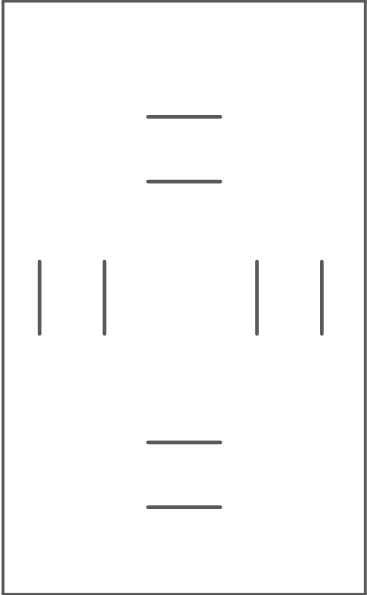
R11、C9: QMC芯片上要焊



Title		
Size	Number	Revision
A4		
Date:	10/11/2021	Sheet of
File:	E:\personal\...\Sensor.SchDoc	Drawn By:







Comment	Description	Designator	Footprint	LibRef	Quantity
100nF	Capacitor	C1, C2, C4, C5, C6	0402C	Cap	5
2.2uF	Capacitor	C3, C7	0402C	Cap	2
4.7uF	Capacitor	C8	0402C	Cap	1
220nF	Capacitor	C9	0402C	Cap	1
Header 6	Header, 5-Pin	P1, P2	HEADER 6x1 – 1.0mm	Header 6	2
NMOS	N-Channel Power MOSFET	Q1	SOT-23-3	NMOS(SOT23)	1
150R	Resistor	R1, R2, R3, R4, R5, R6	0603R	Res1	6
1k	Resistor	R7, R11	0402R	Res1	2
100k	Resistor	R8	0402R	Res1	1
4.7k	Resistor	R9, R10	0402R	Res1	2
ICM20602		U1	LGA-16	ICM20602	1
IST8310		U2	LGA16	IST8310	1
LDO_SOT23-3		U3	SOT-23-3	LDO_SOT23-3	1