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RTL Report

Testbench	Pass
Clock Cycle	10 (ns)
Total Time	10840 (ns)

Synthesis Report

Testbench	Pass
Clock Cycle	10 (ns)
Cell Area	237349.274729
Total Time	10840 (ns)
Area*Time	2572866138.06

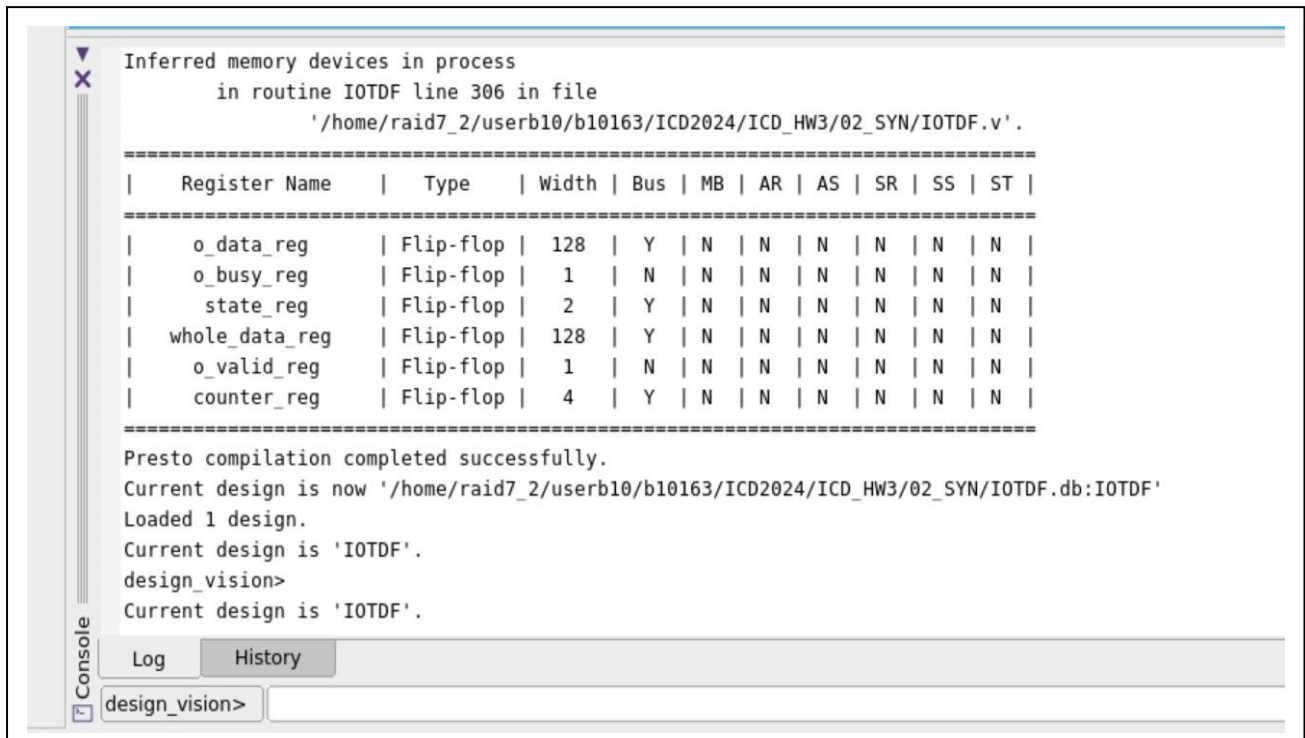
APR Report

Testbench	Pass
Clock Cycle	10 (ns)
Cell Area	47157.167 (um^2)
Total Time	10840 (ns)
Area*Time	511183690(ns*um^2)

How to report area in Innovus?

➔ File > Report > Summary > choose Text only, file name: **summaryReport.rpt**>OK, find **Total area of Core.**

Synthesis No latch



APR NanoRoute Innovus Result (截圖)

