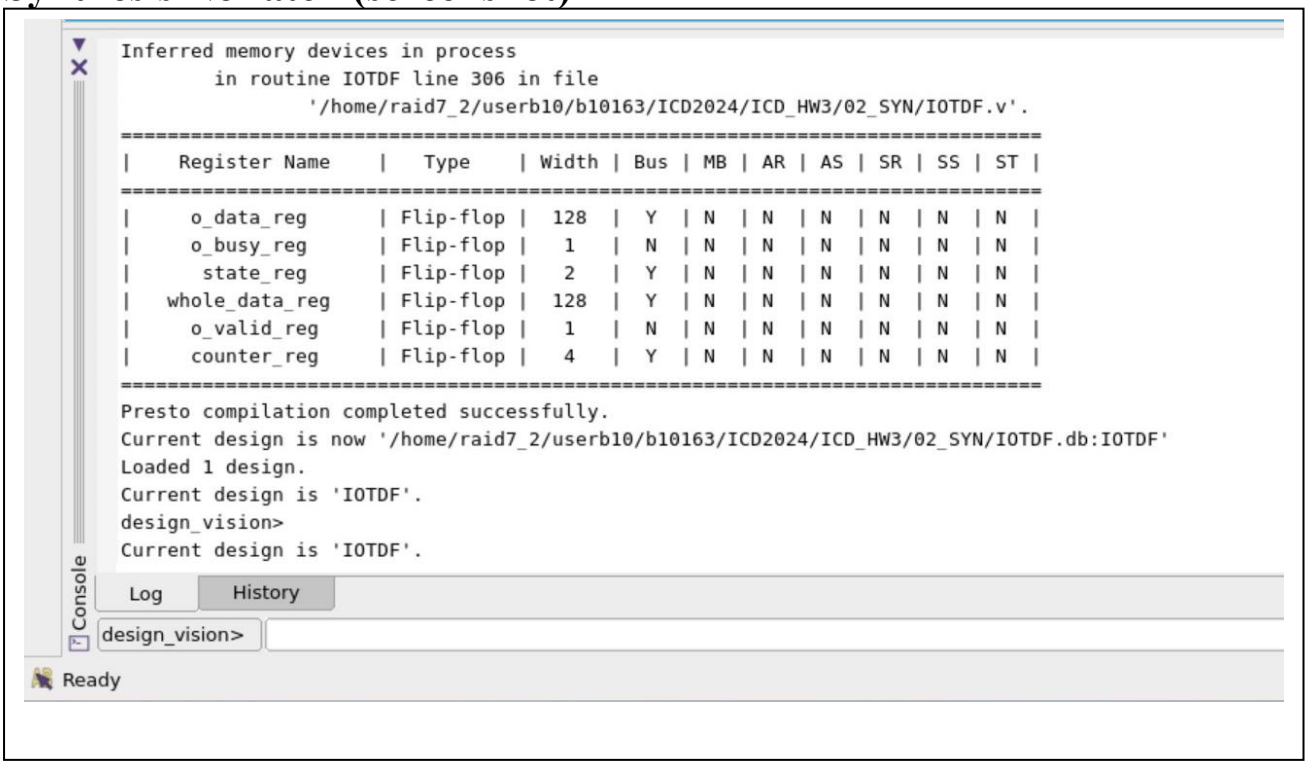
Synthesis Report

Testbench	Pass
Clock Cycle	10 (ns)
Cell Area	237349.274729
Total Time	10840 (ns)
Area*Time	2572866138.06

Synthesis No-latch (screenshot)



Synthesis slack (screenshot)