Charles Preston Hill III

(469) 503-8539 | chillpreston3@gmail.com | http://linkedin.com/in/charleshillthree | https://chill-three.github.io/home

EDUCATION

The University of Texas at Dallas, Richardson, TX - BSEE — August 2020 – December 2024

- ★ Graduated: Fall 2024
- ★ Relevant Coursework: Signals and Systems, Electronic Devices, Electromagnetic Engineering, RF Design Principles, Systems and Controls, Telecommunications Networks, Intro to Power, Integrated Circuit Technology

Collin College, Frisco, TX - Associates of Science in Electrical Engineering — May 2019 – June 2022

- ★ Phi Theta Kappa (PTK) Honor Society Member
- ★ Collin College's 2019 Dean's List

TECHNICAL SKILLS

Languages: C# Language, Java, Python, 8051 Assembly Language, VHDL, Verilog Programming and Shell Scripts

Programs:

- ✪ AWR Microwave Office and PathWave ADS
- ✪ Digilent WaveForms Instrument Suite
- ✪ KiCAD EDA Suite
- ✪ National Instruments LabVIEW & Design Suite
- ✪ Onshape and Rhinoceros 3D CAD Software
- ✪ Cadence Allegro PSpice Analysis Utilities Software
- ✪ Keil MDK-Arm Development Tools
- ✪ MATLAB and MATLAB App Designer
- ✪ QT Design Studio
- ✪ Xilinx Vivado Design Suite

EXPERIENCE

Allegro MicroSystems — January 2024 – December 2024

Software Engineer & Technical Project Lead (Senior UTDesign Project)

- ★ Managed and coordinated a diverse team to develop the Capacitive Coupling Analyzer, a Python-based tool that identifies and summarizes circuit design issues by analyzing top-level layouts and Excel-based noise data, outperforming traditional simulators like SPICE in efficiency

Natural Science and Engineering Research Laboratory (UT Dallas) — August 2023 – December 2024

Undergraduate Research Assistant

- ★ Aided cutting-edge research in MEMS, nanotechnology, liquid metals, and strain sensors, contributing to innovative developments within the NSERL Micro/Nano Devices and Systems (MiNDs) Laboratory
- ★ Coordinated material procurement with UT Dallas vendors and analyzed data for reports presented to researchers

SELECTED PROJECTS

Single-Stage RF Amplifiers (Lumped Element & Distributed TX Lines) | AWR Microwave Office

- ★ Designed two single-stage RF amplifiers utilizing lumped-element components and distributed TX lines
- ★ Successfully engineered RF amplifiers to surpass design specifications, achieving over 10 dB gain, input and output return losses above 20 dB, and bandwidth exceeding 10% at the target center frequency

Full-Wave Bridge Diode Rectifier PCB Design | Cadence Allegro PSpice PCB Design

- ★ Designed a minimal full-wave bridge diode rectifier, which smoothly converts AC signals into DC signals
- ★ Employed PSpice simulations of multiple PCB layouts to ensure optimal and efficient designs

FPGA Moore Designed Finite State Machine | Xilinx Vivado (Artix-7 FPGA Family), Verilog

- ★ Wrote and designed Verilog code to implement a rising edge detector state machine and testbench scenarios

AFFILIATIONS

IntelliChoice (Non-profit Organization) | The University of Texas at Dallas — November 2023 – Present

Volunteer Tutor

- ★ Aided K-12 students with tailored STEM education ensuring inclusive and differentiated support

PROFESSIONAL DEVELOPMENT

Radio Spectrum Conference | Baylor University & National Science Foundation (NSF) — Summer 2024

- ★ Participated in Spectrum Policy discussions and rulemaking; analyzed passive systems and frequency allocations; studied communication systems, modulation, and coexistence; explored wireless circuits and radar systems