

Figure 2

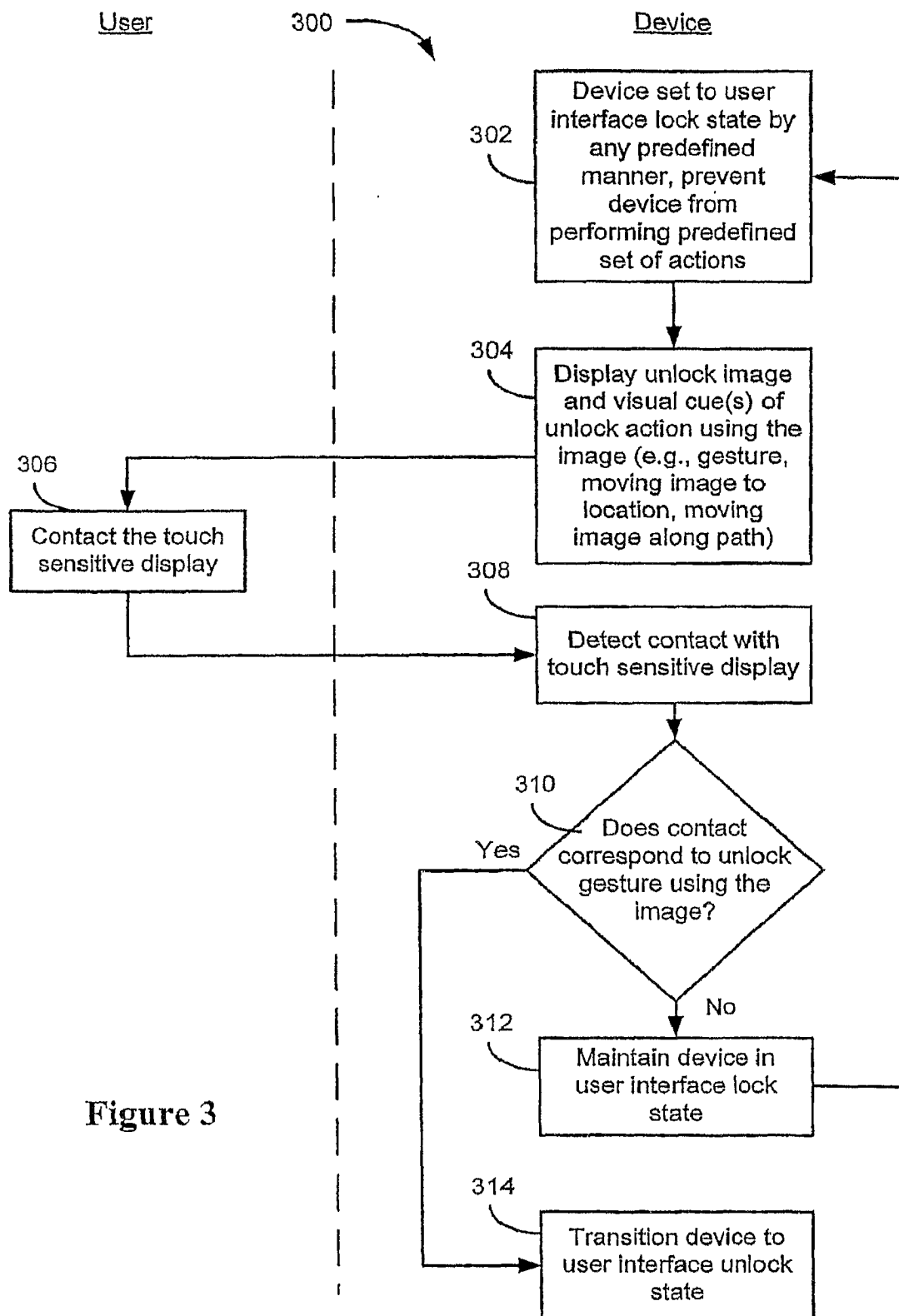


Figure 3

Device
400

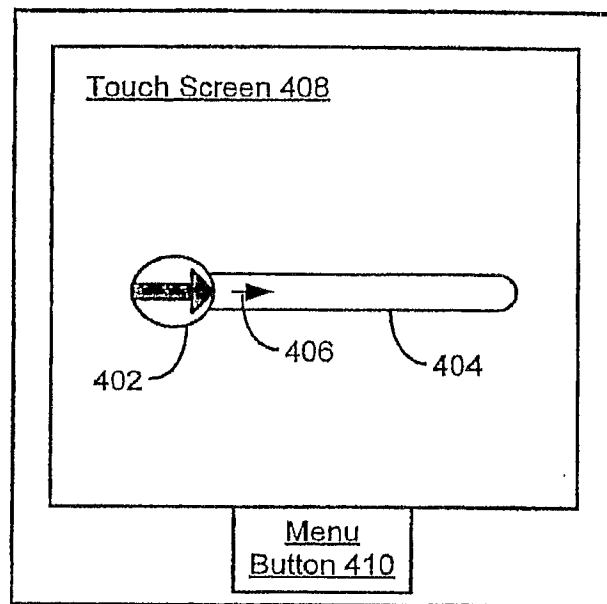


Figure 4A

Device
400

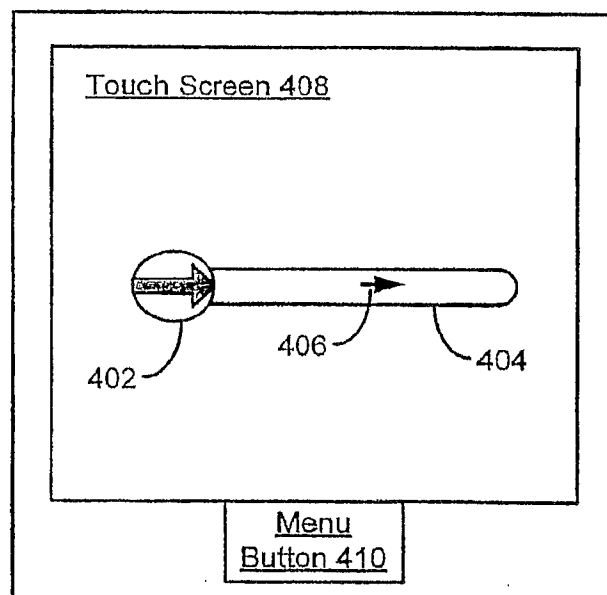


Figure 4B

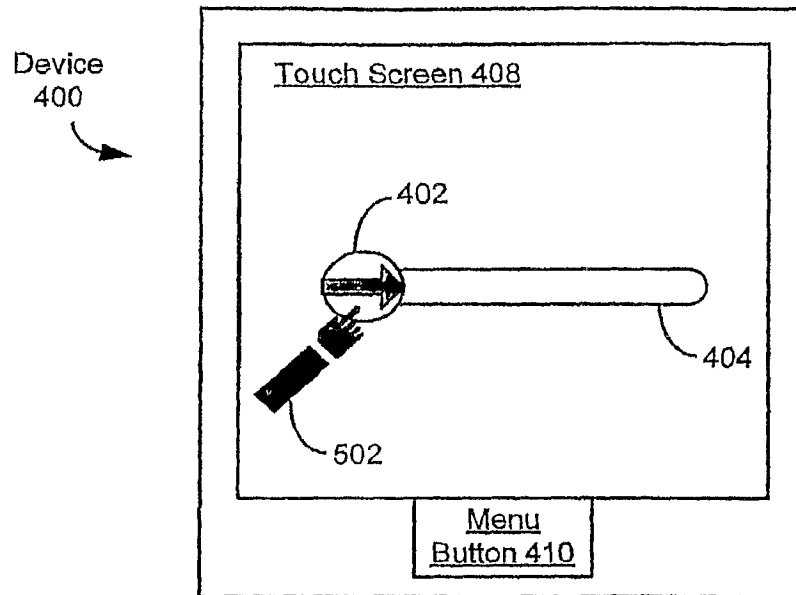


Figure 5A

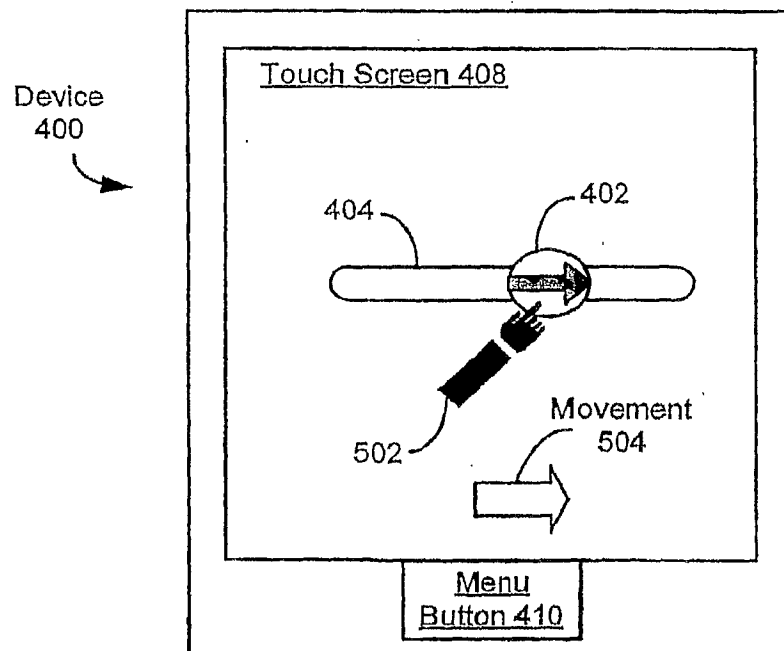


Figure 5B

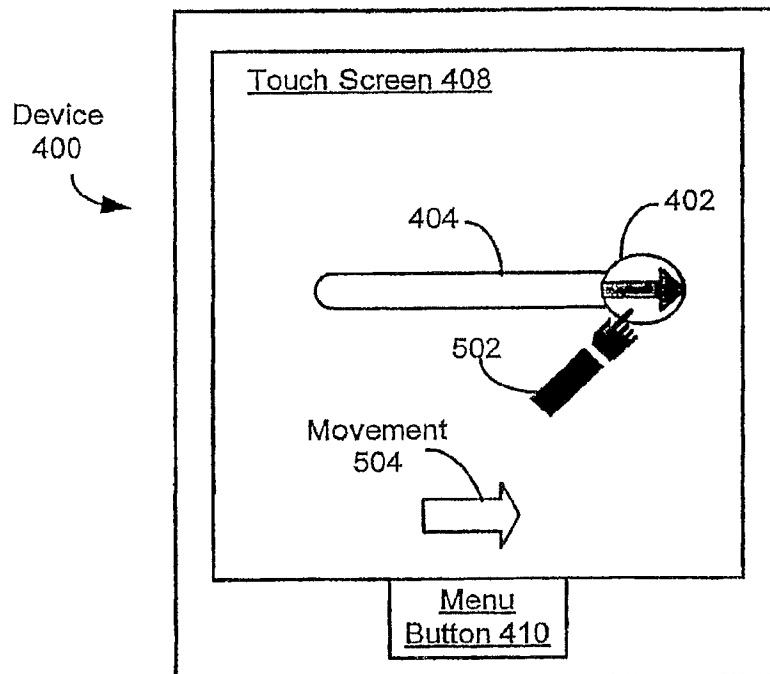


Figure 5C

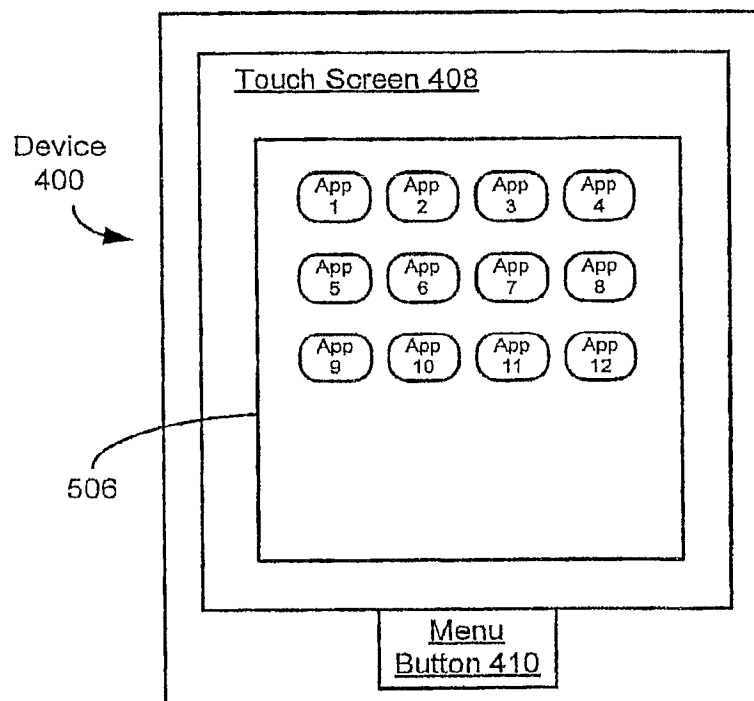


Figure 5D

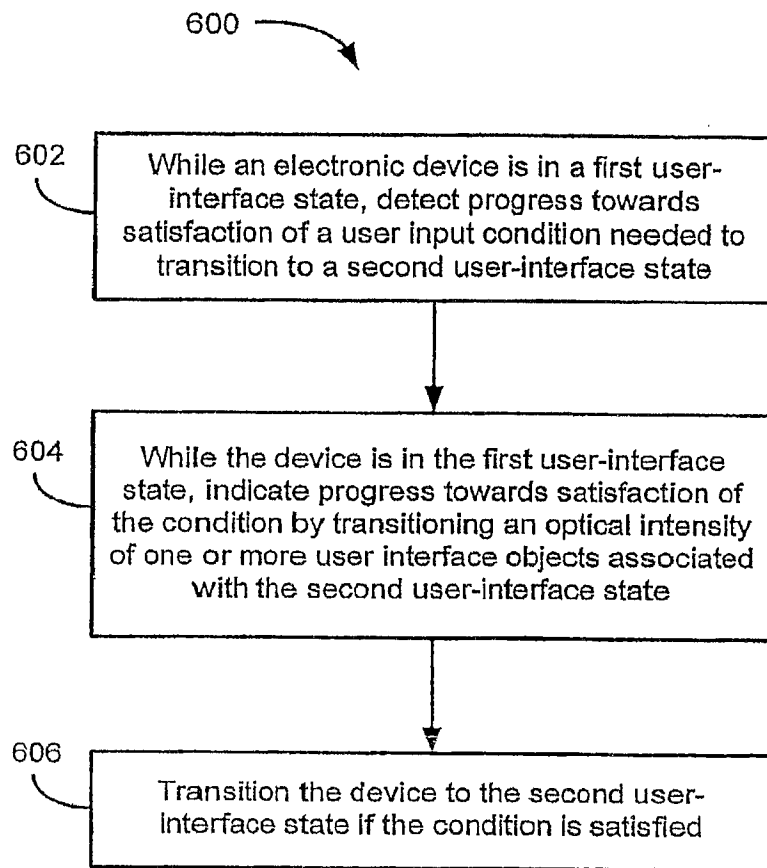


Figure 6

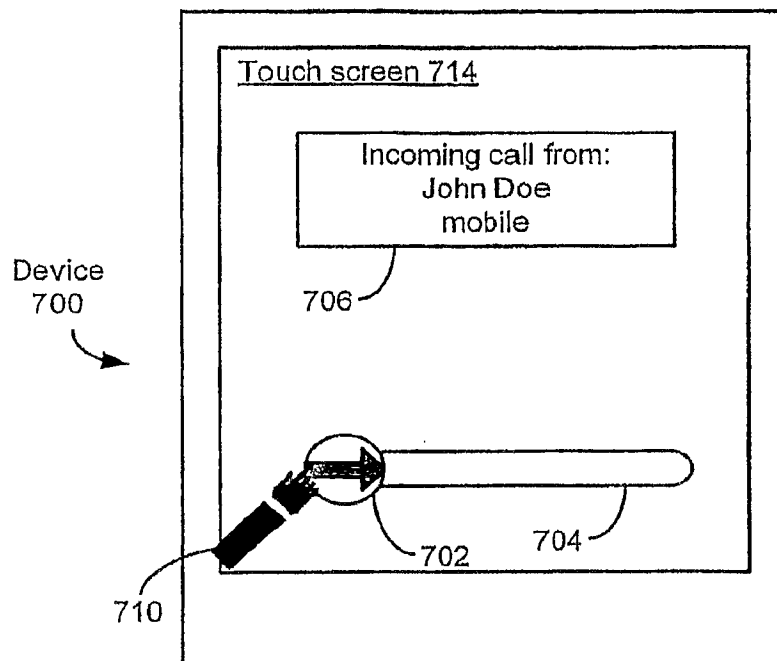


Figure 7A

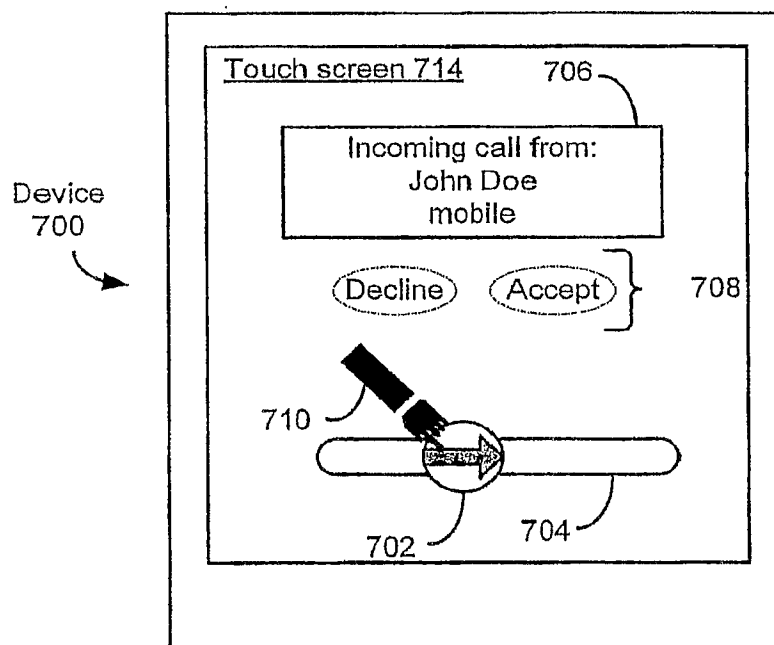


Figure 7B

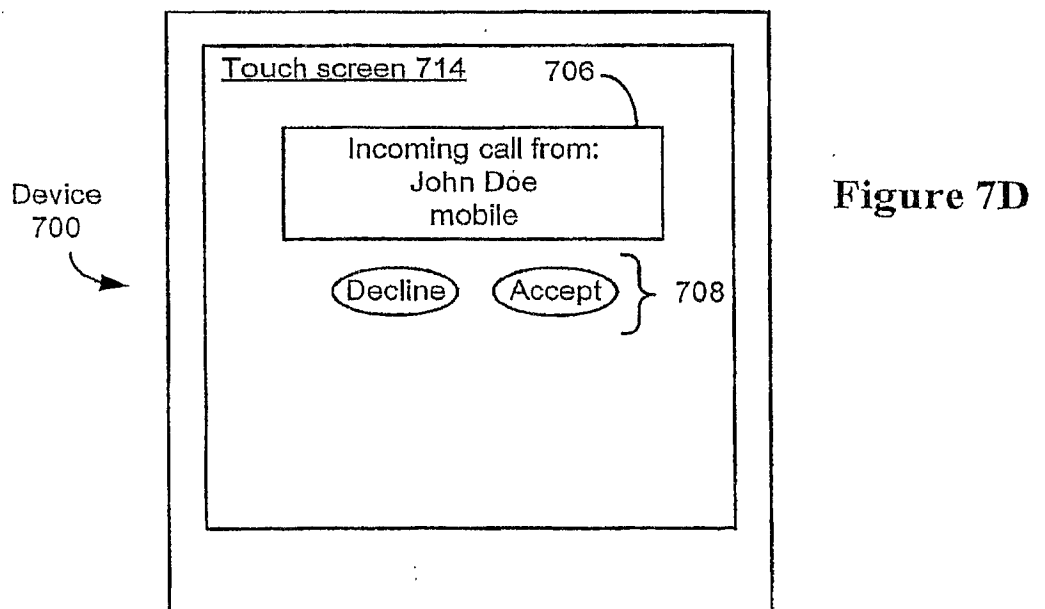
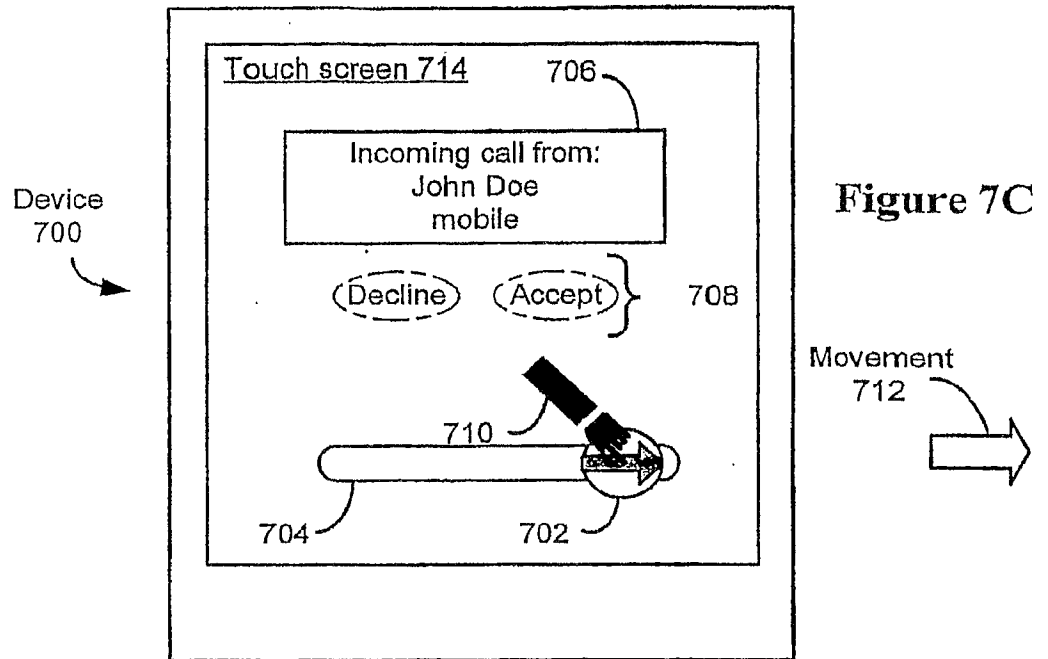


Figure 8A

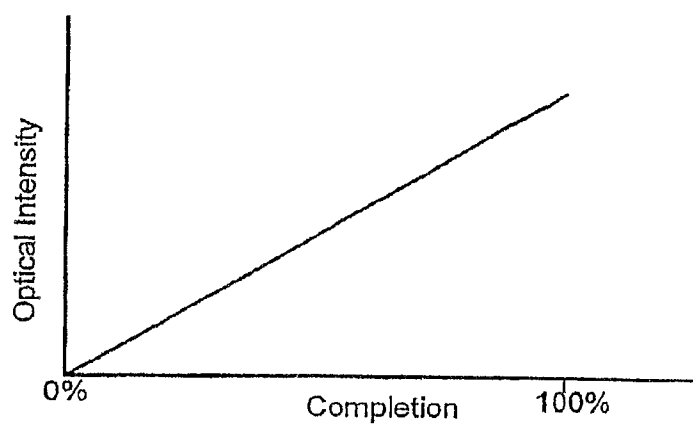


Figure 8B

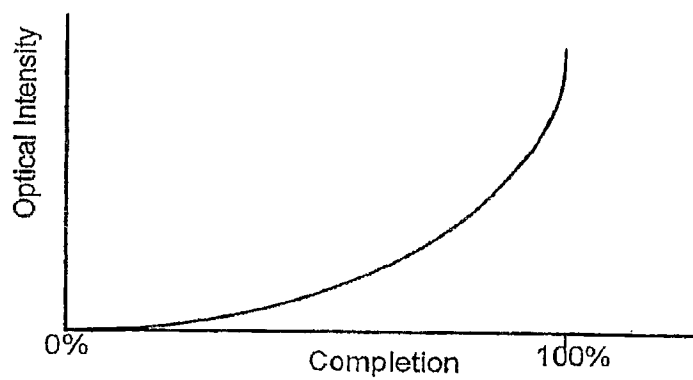
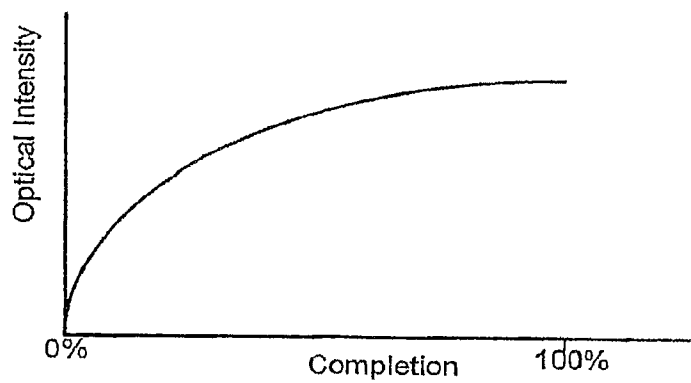


Figure 8C



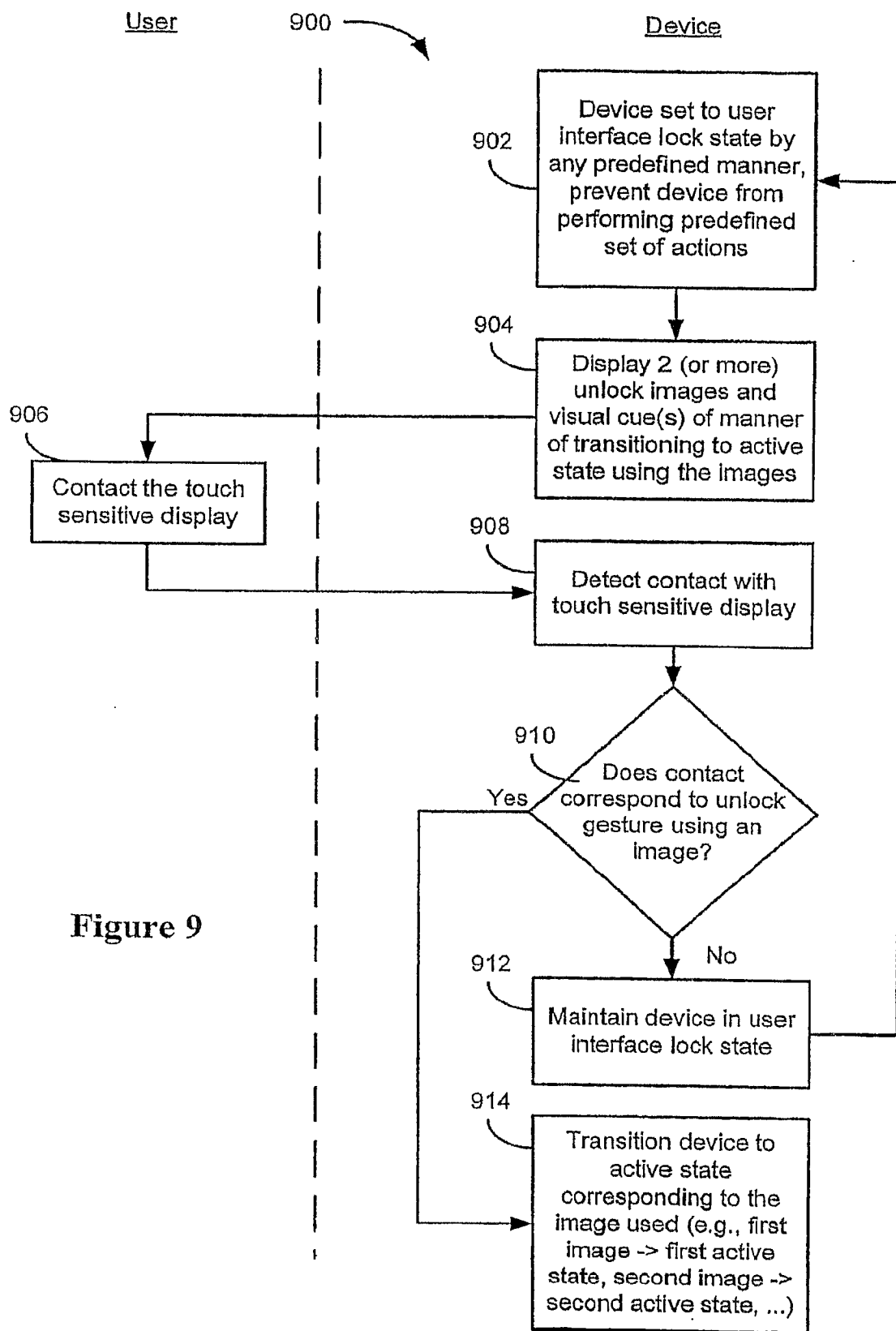


Figure 9

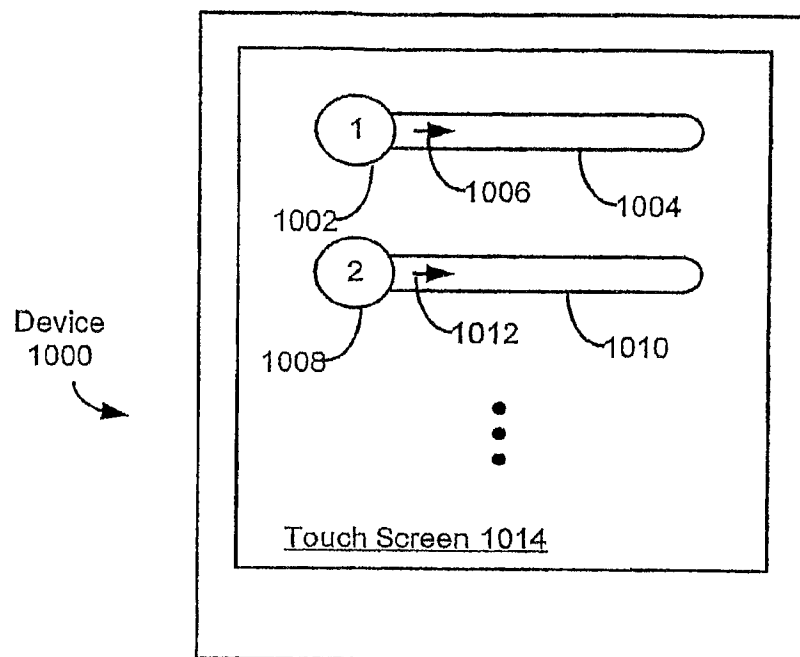


Figure 10

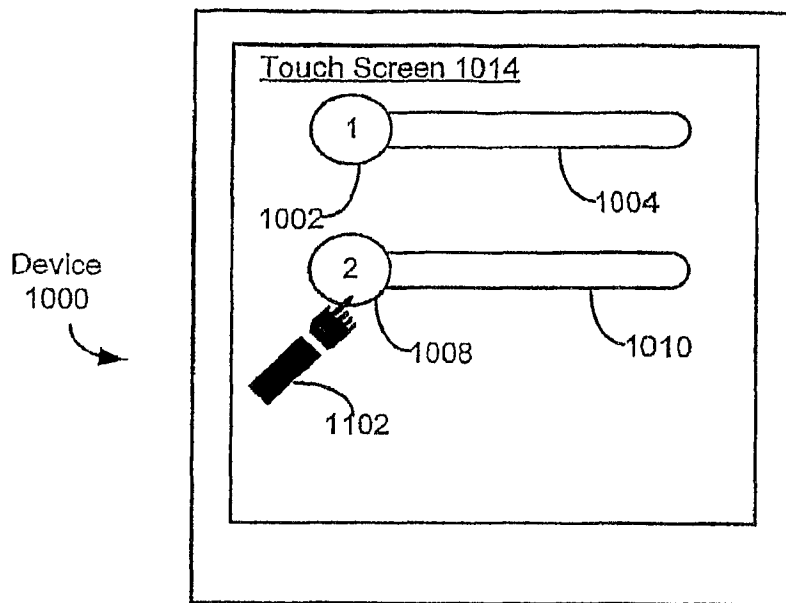


Figure 11A

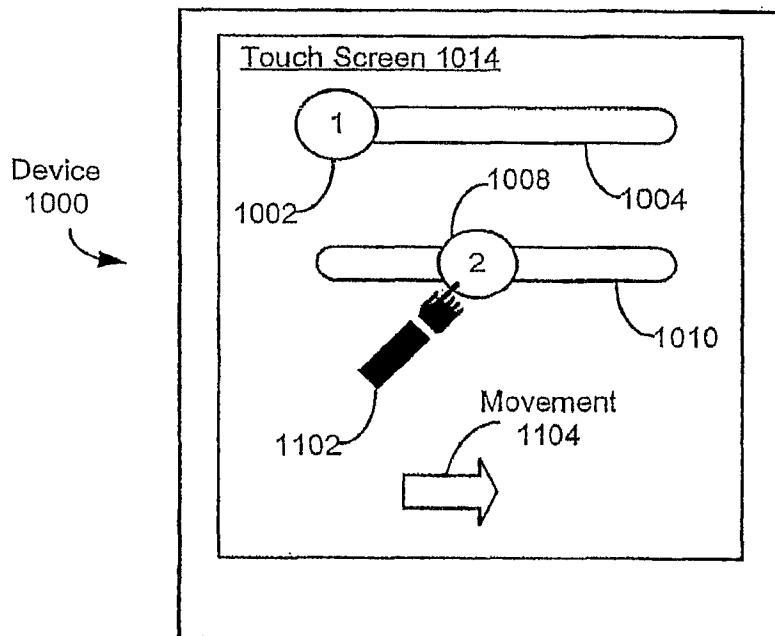


Figure 11B

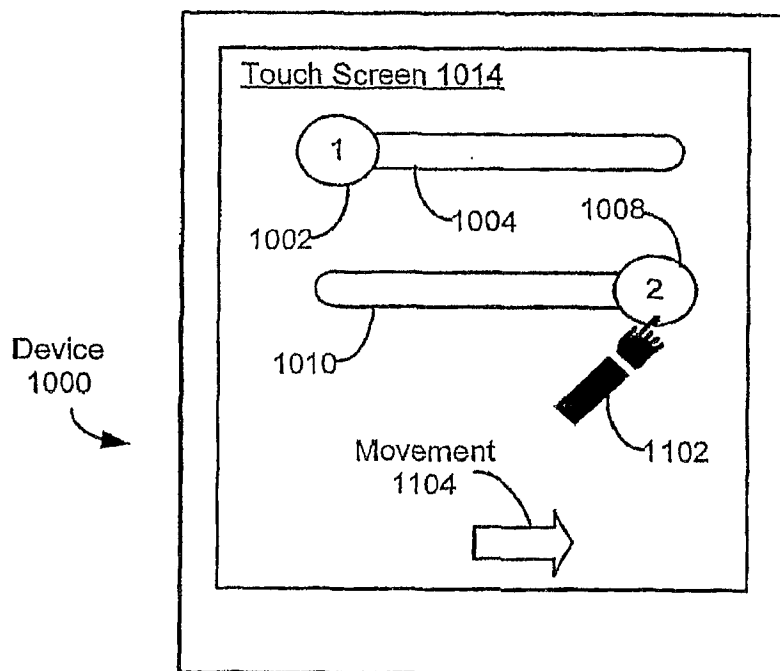


Figure 11C

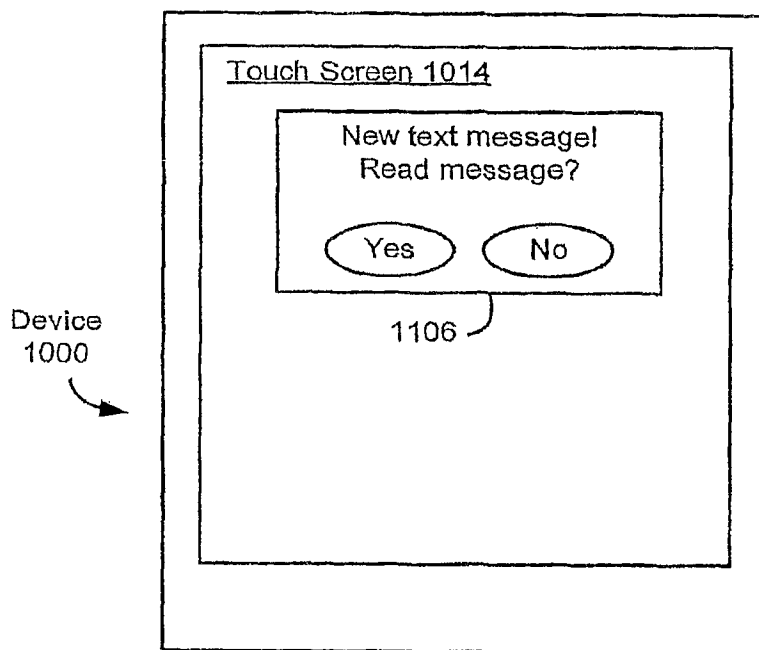


Figure 11D

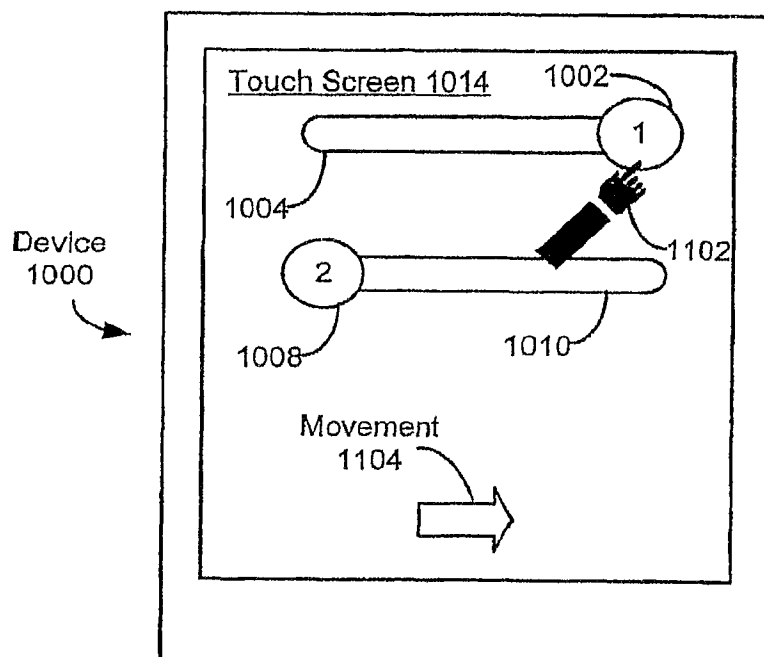


Figure 11E

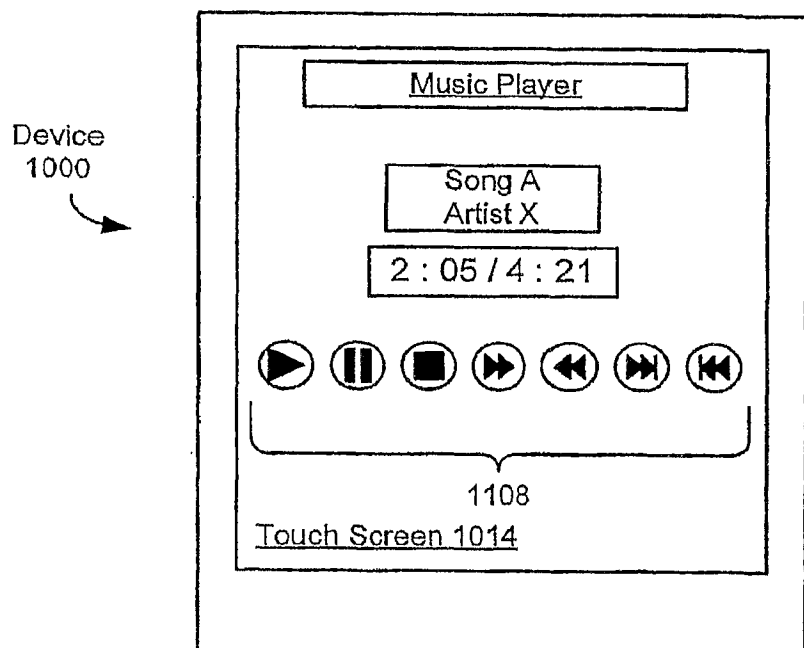


Figure 11F