

Layer Name	Туре	Material	Thickness (mm)	Color	Epsilon R	Loss Tangent
F.Silkscreen	Top Silk Screen	Not specified	0 mm	White	1	0
F.Paste	Top Solder Paste		0 mm		1	0
F.Mask	Top Solder Mask	Not specified	0.01 mm	Blue	3.3	0
F.Cu	copper		0.035 mm		1	0
Dielectric	core	FR4	1.51 mm	FR4 natural	4.5	0.02
B.Cu	copper		0.035 mm		1	0
B.Mask	Bottom Solder Mask	Not specified	0.01 mm	Blue	3.3	0
B.Paste	Bottom Solder Paste		0 mm		1	0
B.Silkscreen	Bottom Silk Screen	Not specified	0 mm	White	1	0

## **BOARD CHARACTERISTICS**

Copper Layer Count: 2 Board Thickness: 1.000 mm

Board overall dimensions: 51.0000 mm x 21.0000 mm

Min track/spacing: 0.1500 mm / 0.1500 mm Min hole diameter: 0.2000 mm

Copper Finish: None Impedance Control: No Castellated pads: Yes Plated Board Edge: No

Edge card connectors: No

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CIRCUITSTATE Electronics LLP (www.circuitstate.com)
Sheet:
File: Mitayi-Pico-D1.kicad\_pcb

Title: Mitayi-Pico-D1

Size: A4 Date: 2025-04-27 Rev: 0.6

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