CLJD

Progress Report

<2019-10-17>

Draft Description

Draft Member Tasks

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  | Charles | Liora | Jacob | Dylan |
| Updated Report | | | | |
| Title Page | \* |  |  | \* |
| Updated Report |  |  |  | \* |
| Updated Description |  |  |  | \* |
| Updated Member Task List | \* | \* | \* | \* |
| Updated Software Discovery |  | \* |  |  |
| Updated Participation  Census |  | \* |  |  |
| System Design | | | | |
| Title Page | \* |  |  |  |
| Parts List |  |  |  | \* |
| Input List |  |  |  | \* |
| Output List |  |  |  | \* |
| Interface List |  |  |  | \* |
| Module List | \* |  |  |  |
| Mode List |  | \* |  |  |
| Circuit Diagram |  |  | \* |  |
| State Machine Diagram |  |  | \* |  |
| Code and Output | | | | |
| Add | \* |  |  |  |
| Subtract |  | \* |  |  |
| Multiply |  |  |  | \* |
| Divide |  |  | \* |  |
| Bit Shift | \* |  |  |  |

Draft Software Discovery

Participation Census