CLJD

System Design

<2019-11-22>

Parts List

Input List

\*The following inputs are organized by module:

Mux2:

signal:

in1:

in2:

Mux4:

a3:

a2:

a1:

a0:

s:

Decoder4:

n:

ARA:

in:

AddHalf:

a:

b:

AddFull:

a:

b:

c\_in:

Add:

a:

b:

Partial:

a:

b:

Mult:

a:

b:

Sub:

a:

b:

ShiftLeft:

num:

shift:

ShiftRight:

num:

shift: