
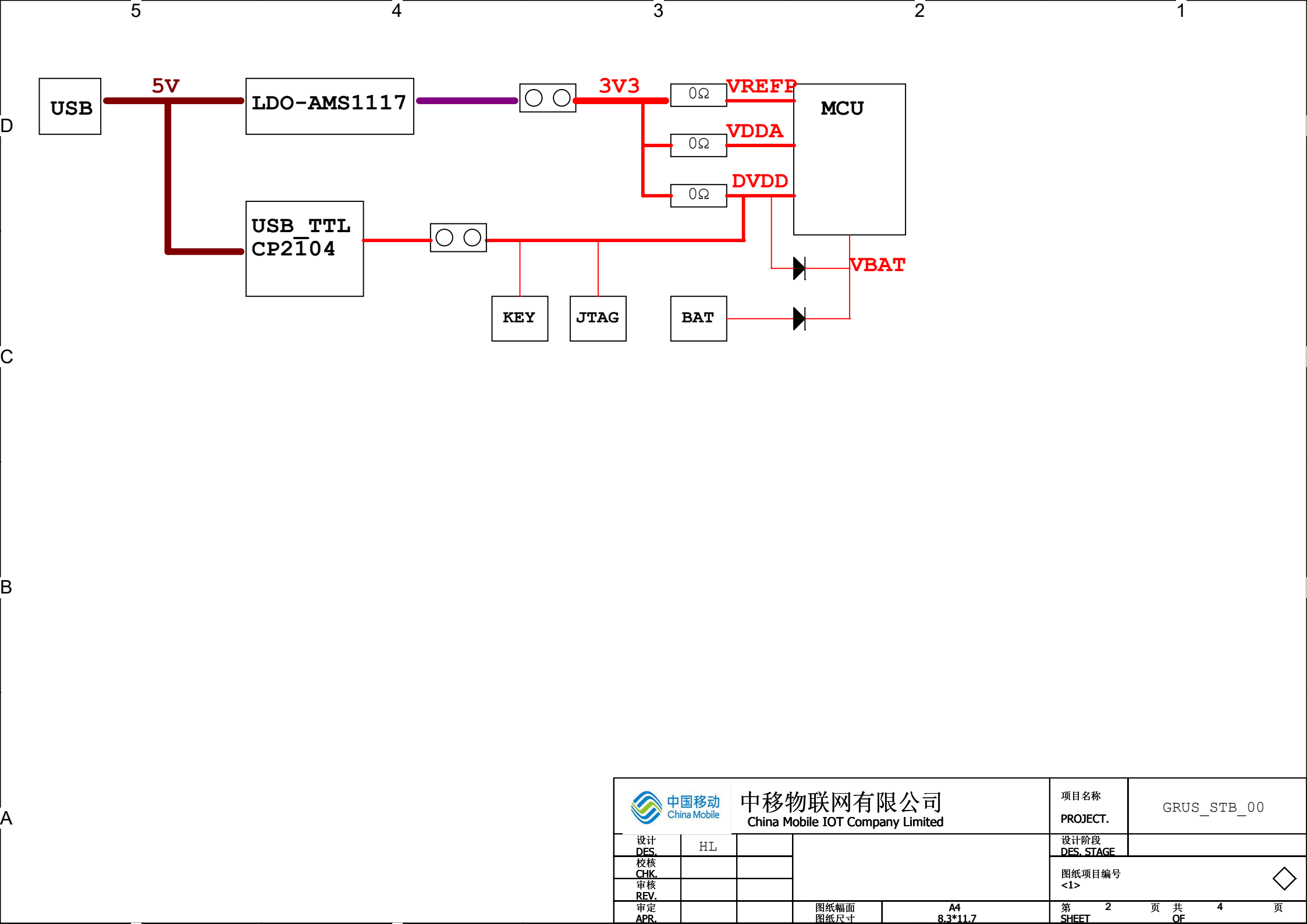




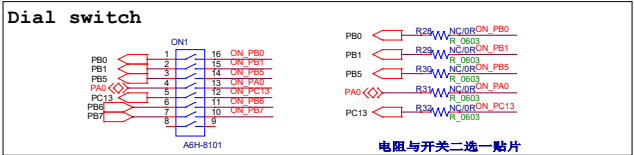
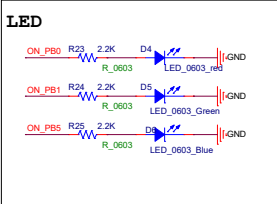
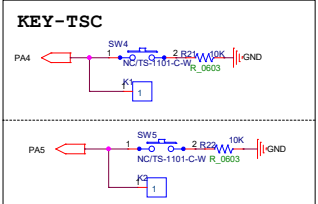
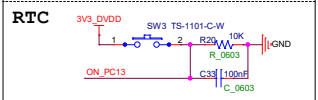
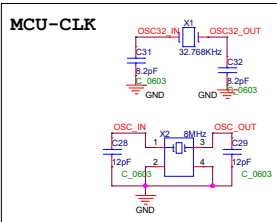
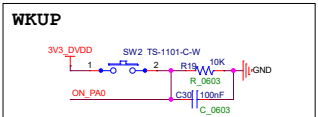
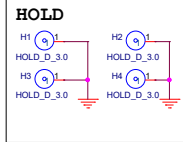
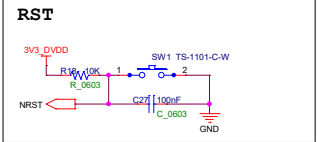
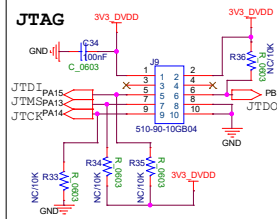
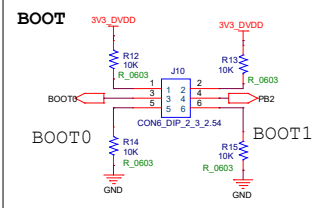
MCU_FPGA BOARD Design

Rev	Date	Description	Designer
00	2021-07-06	initial	HUANGLUN

		中移物联网有限公司 China Mobile IOT Company Limited		项目名称 PROJECT.	
设计 DES.				设计阶段 DES. STAGE	
校核 CHK.				图纸项目编号 <1>	
审核 REV.					
审定 APR.			图纸幅面 图纸尺寸	A4 8.3*11.7	第 1 页 共 4 页 SHEET OF

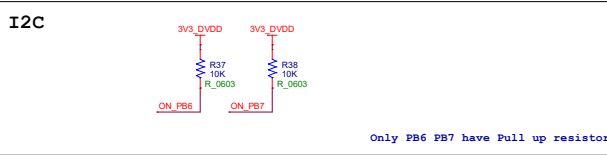


		中移物联网有限公司 China Mobile IOT Company Limited		项目名称 PROJECT.	GRUS_STB_00
设计 DES.	HL			设计阶段 DES. STAGE	
校核 CHK.				图纸项目编号 <1> 	
审核 REV.					
审定 APR.			图纸幅面 图纸尺寸	A4 8.3*11.7	第 2 页 共 4 页 SHEET OF



The diagram illustrates the pinout of the ATmega328P microcontroller. The pins are numbered 1 to 40, and the package is labeled 'ATmega328P'. The pins are organized into several groups:

- Power Pins:** VDD, GND, VBAT, VREF, VREF+, VREF-, VREFP, VREFM, VREFN, VREFP+, VREFM+, VREFN+.
- Digital I/O Pins:** PORTA (PA0-PA7), PORTB (PB0-PB7), PORTC (PC0-PC7), PORTD (PD0-PD7), PORTF (PF0-PF7), PORTK (PK0-PK7).
- Analog Pins:** AD_CONVERTER (AD0-AD7), COMPARATOR (COMP0-COMP7), OPERATOR (OP0-OP7).
- Other Pins:** RESET, PC13-TAMPER-RTC, PC14-OSC32_IN, PC15-OSC32_OUT, PC16-OSC32_IN, PC17-OSC32_OUT, PC18-OSC32_IN, PC19-OSC32_OUT, PC20-OSC32_IN, PC21-OSC32_OUT, PC22-OSC32_IN, PC23-OSC32_OUT, PC24-OSC32_IN, PC25-OSC32_OUT, PC26-OSC32_IN, PC27-OSC32_OUT, PC28-OSC32_IN, PC29-OSC32_OUT, PC30-OSC32_IN, PC31-OSC32_OUT, PC32-OSC32_IN, PC33-OSC32_OUT, PC34-OSC32_IN, PC35-OSC32_OUT, PC36-OSC32_IN, PC37-OSC32_OUT, PC38-OSC32_IN, PC39-OSC32_OUT, PC40-OSC32_IN, PC41-OSC32_OUT, PC42-OSC32_IN, PC43-OSC32_OUT, PC44-OSC32_IN, PC45-OSC32_OUT, PC46-OSC32_IN, PC47-OSC32_OUT, PC48-OSC32_IN, PC49-OSC32_OUT, PC50-OSC32_IN, PC51-OSC32_OUT, PC52-OSC32_IN, PC53-OSC32_OUT, PC54-OSC32_IN, PC55-OSC32_OUT, PC56-OSC32_IN, PC57-OSC32_OUT, PC58-OSC32_IN, PC59-OSC32_OUT, PC60-OSC32_IN, PC61-OSC32_OUT, PC62-OSC32_IN, PC63-OSC32_OUT, PC64-OSC32_IN, PC65-OSC32_OUT, PC66-OSC32_IN, PC67-OSC32_OUT, PC68-OSC32_IN, PC69-OSC32_OUT, PC70-OSC32_IN, PC71-OSC32_OUT, PC72-OSC32_IN, PC73-OSC32_OUT, PC74-OSC32_IN, PC75-OSC32_OUT, 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The figure consists of three pin connection diagrams for the PE10000, each showing a 54-pin package with pins 1-14 on the left and 21-54 on the right. The diagrams are labeled COM40_SIP_2_14_2_54, COM25_DIP_2_14_2_54, and COM25_DIP_2_20_2_54. Each diagram includes a V_3V3 supply and a GND connection.

COM40_SIP_2_14_2_54 Pin Connections:

Pin	Signal	Pin	Signal
1	V_3V3	21	PE10
2	V_3V3	22	PE11
3	PEB_UARTSTX	23	PE12
4	PEB_UARTSTX	24	PE13
5	PE10	25	PE14
6	PE11	26	PE15
7	PE12	27	PE16
8	PE13	28	PE17
9	PE14	29	PE18
10	PE15	30	PE19
11	PE16	31	PE20
12	PE17	32	PE21
13	PE18	33	PE22
14	PE19	34	PE23
		35	PE24
		36	PE25
		37	PE26
		38	PE27
		39	PE28
		40	PE29
		41	PE30
		42	PE31
		43	PE32
		44	PE33
		45	PE34
		46	PE35
		47	PE36
		48	PE37
		49	PE38
		50	PE39
		51	PE40
		52	PE41
		53	PE42
		54	PE43

COM25_DIP_2_14_2_54 Pin Connections:

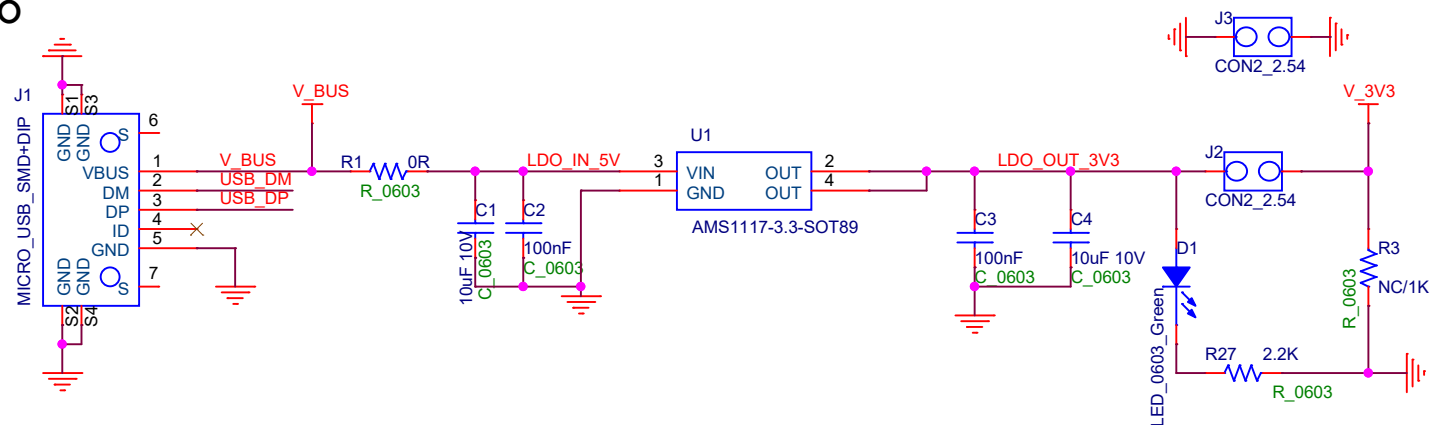
Pin	Signal	Pin	Signal
1	V_3V3	21	PE10
2	V_3V3	22	PE11
3	PEB_UARTSTX	23	PE12
4	PEB_UARTSTX	24	PE13
5	PE10	25	PE14
6	PE11	26	PE15
7	PE12	27	PE16
8	PE13	28	PE17
9	PE14	29	PE18
10	PE15	30	PE19
11	PE16	31	PE20
12	PE17	32	PE21
13	PE18	33	PE22
14	PE19	34	PE23
		35	PE24
		36	PE25
		37	PE26
		38	PE27
		39	PE28
		40	PE29
		41	PE30
		42	PE31
		43	PE32
		44	PE33
		45	PE34
		46	PE35
		47	PE36
		48	PE37
		49	PE38
		50	PE39
		51	PE40
		52	PE41
		53	PE42
		54	PE43

COM25_DIP_2_20_2_54 Pin Connections:

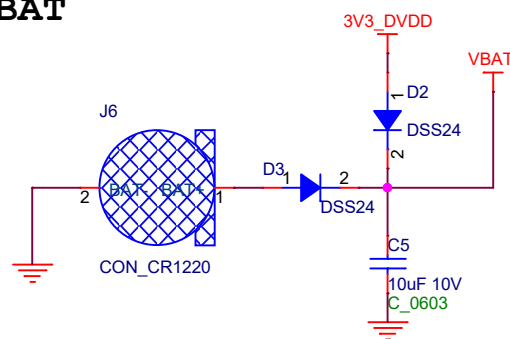
Pin	Signal	Pin	Signal
1	V_3V3	21	PE10
2	V_3V3	22	PE11
3	PEB_UARTSTX	23	PE12
4	PEB_UARTSTX	24	PE13
5	PE10	25	PE14
6	PE11	26	PE15
7	PE12	27	PE16
8	PE13	28	PE17
9	PE14	29	PE18
10	PE15	30	PE19
11	PE16	31	PE20
12	PE17	32	PE21
13	PE18	33	PE22
14	PE19	34	PE23
		35	PE24
		36	PE25
		37	PE26
		38	PE27
		39	PE28
		40	PE29
		41	PE30
		42	PE31
		43	PE32
		44	PE33
		45	PE34
		46	PE35
		47	PE36
		48	PE37
		49	PE38
		50	PE39
		51	PE40
		52	PE41
		53	PE42
		54	PE43

		中移物联网有限公司 China Mobile IOT Company Limited		项目名称 PROJECT.		GRUS_STB_00	
设计 DES.	HL		设计阶段 DES. STAGE				
校核 CHK.			图纸项目编号 <1>		◇		
审核							
审定 REV.							
审批 APP.							
		图纸幅面	A4	第 3 页 共 3 页			
			8.5*11.7	SHEET			

USB-LDO



BAT



USB_TTL

