	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
PeriCfg0	FBDiv_skip	clkTreeDisable	CLKSel 0	disVCO	disSER	disEOM	disDES 0	disCLK 0
PeriCfg1		PLI	I		-	PLL_E	BiasGen	
PeriCfg2	0 0 0 0				0	0 0 0 0 0		
	0	0	0	0 PLL_vcoRailMode	0	0	0	0
PeriCfg3	VRefGen 0	0	ENABLEPLL 0	PLL_vcoRailMode 0	0	0 PLL_v	codac 0	0
PeriCfg4	TS_PD 0	PS_ForceDown	PS_Enable 0	PS_CapRst	0	PS_CP	Current 0	0
PeriCfg5	0	0	0	PS_Ph	naseAdj 0	0	0	0
PeriCfg6	-		-	RefS	trSel			
	0 GRO_TOARST_N	0 GRO_Start	0 CLK40_SetCM	U CLK40_InvData	0	CLK40_Equ	0 CLK40_EnTer	0 CLK40_EnRx
PeriCfg7	0	0	0	0	0	0	0	0
PeriCfg8	GRO_TOA_Latch	GRO_TOA_CK 0	CLK1280_SetCM 0	CLK1280_InvData	0	CLK1280_Equ 0	CLK1280_EnTer	CLK1280_EnRx 0
PeriCfg9	GRO_TOT_CK_Latch	GRO_TOTRST_N 0	FC_SetCM	FC_InvData	0	FC_Equ 0	FC_EnTer	FC_EnRx
PeriCfg10	0	0	0	BCIDof		0	0	0
PeriCfg11		emptySlot	BCID[3:0]			BCIDoff	Set[11:8]	
	0	0	0	0 emptySlot	0 BCID[11:4]	0	0	0
PeriCfg12	0	0	0	0	0	0	0	0
PeriCfg13	asyPLLReset ()	asyLinkReset ()	asyAlignFastcommand 0	0	0	readoutClockDelayPixel	0	0
PeriCfg14	asyResetGlobalRead- out	asyResetFastcommand	asyResetChargeInj			readoutClockWidthPixel		
	0	0	0	0	0	0	0	0
PeriCfg15	0	asyStartCalibration	asyResetLockDetect 0	0	0	readoutClockDelayGlobal	0	0
PeriCfg16	0	LTx_AmplSel	0	0	0	readoutClockWidthGlobal	0	0
PeriCfg17	0	RTx_AmplSel	0	0	0	chargeInjectionDelay	0	0
PeriCfg18	disLTx ()	onChipL	JAConf ()	fcDataDelayEn	fcClkDelayEn	fcSelfAlignEn	softBoot	disPowerSequence
PeriCfg19	disRTx	singlePort	serRate			serRateLeft	linkResetTestPattern	disScrambler
	0	0 0 0 eFuse_TCKHP			0 0 0 0 triggerGranularity mergeTriggerData			
PeriCfg20	0	0	0	0	0	0	0	0
PeriCfg21	0	0	eFuse_Bypass	eFuse_Start	eFuse_Rstn	eFuse_ 0	_Mode 0	eFuse_EnClk
PeriCfg22	0	0	0	eFuse_F	Prog[7:0]	0	0	0
PeriCfg23	0	0	0	eFuse_P		0	0	0
PeriCfg24		-		eFuse_P	rog[23:16]			
	0	0	0	0 eFuse_Pi	og[31:24]	0	0	0
PeriCfg25	0	0	0	0	0	0	0	0
PeriCfg26	0	0	0	0	edPattern[7:0]	0	0	0
PeriCfg27	0	0	0	linkResetFixe	dPattern[15:8]	0	0	0
PeriCfg28	0	0	0	linkResetFixed	lPattern[23:16]	0	0	0
PeriCfg29			<u> </u>	linkResetFixed	lPattern[31:24]		l	
PeriCfg30	0	0 lfReLockT		0	0	0 lfLockTh		0
	<u> </u>	0	0 TDCStrobeTest	0 TDCClockTest	0	0 lfUnLockT	hrCounter	0
PeriCfg31	0	0	0	0	0	0	0	0