Bare Chip 01E2 #52, Pixel (3,3), Qinj S-Curve, PA Low Bias Current Mode THCal BL = 255 DAC LSB THCal NW = ± 3 DAC LSB DAC TH = $(8.582\pm0.321 [1/fC])\cdot Q + (231.621\pm5.834)$ 95% Confidence Interval on Linear Fit Threshold [LSB] Charge Injected [fC]