

| | Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 |
|-----------|-----------------------------|--------------------------|-----------------------------------|----------------------|------------------------------|---------------------------|-------------------|-----------------------|
| PeriCfg0 | FBDiv_skip 0 | clkTreeDisable 0 | CLKSel 0 | disVCO 0 | disSER 0 | disEOM 0 | disDES 0 | disCLK 0 |
| PeriCfg1 | | PLL_I 0 | | | | PLL_BiasGen 0 | | |
| PeriCfg2 | | PLL_R 0 | | | | PLL_P 0 | | |
| PeriCfg3 | VRefGen 0 | - 0 | ENABLEPLL 0 | PLL_vcoRailMode 0 | | PLL_vcoDAC 0 | | |
| PeriCfg4 | TS_PD 0 | PS_ForceDown 0 | PS_Enable 0 | PS_CapRst 0 | | PS_CPCurrent 0 | | |
| PeriCfg5 | | | | PS_PhaseAdj 0 | | | | |
| PeriCfg6 | | | | RefStrSel 0 | | | | |
| PeriCfg7 | GRO_TOARST_N 0 | GRO_Start 0 | CLK40_SetCM 0 | CLK40_InvData 0 | CLK40_Equ 0 | CLK40_EnTer 0 | CLK40_EnRx 0 | |
| PeriCfg8 | GRO_TOA_Latch 0 | GRO_TOA_CK 0 | CLK1280_SetCM 0 | CLK1280_InvData 0 | CLK1280_Equ 0 | CLK1280_EnTer 0 | CLK1280_EnRx 0 | |
| PeriCfg9 | GRO_TOT_CK_Latch 0 | GRO_TOTRST_N 0 | FC_SetCM 0 | FC_InvData 0 | FC_Equ 0 | FC_EnTer 0 | FC_EnRx 0 | |
| PeriCfg10 | | | BCIDOffset[7:0] 0 | | | | | |
| PeriCfg11 | | emptySlotBCID[3:0] 0 | | | | BCIDOffset[11:8] 0 | | |
| PeriCfg12 | | | emptySlotBCID[11:4] 0 | | | | | |
| PeriCfg13 | asyPLLReset 0 | asyLinkReset 0 | asyAlignFastcommand 0 | | readoutClockDelayPixel 0 | | | |
| PeriCfg14 | asyResetGlobalRead-out 0 | asyResetFastcommand 0 | asyResetChargeInj 0 | | readoutClockWidthPixel 0 | | | |
| PeriCfg15 | - 0 | asyStartCalibration 0 | asyResetLockDetect 0 | | readoutClockDelayGlobal 0 | | | |
| PeriCfg16 | | LTx_AmplSel 0 | | | readoutClockWidthGlobal 0 | | | |
| PeriCfg17 | | RTx_AmplSel 0 | | | chargeInjectionDelay 0 | | | |
| PeriCfg18 | disLTx 0 | onChipL1AConf 0 | | fcDataDelayEn 0 | fcClkDelayEn 0 | fcSelfAlignEn 0 | softBoot 0 | disPowerSequence 0 |
| PeriCfg19 | disRTx 0 | singlePort 0 | serRateRight 0 | | serRateLeft 0 | linkResetTestPattern 0 | disScrambler 0 | |
| PeriCfg20 | | eFuse_TCKHP 0 | | | triggerGranularity 0 | | | mergeTriggerData 0 |
| PeriCfg21 | - 0 | - 0 | eFuse_Bypass 0 | eFuse_Start 0 | eFuse_Rstn 0 | eFuse_Mode 0 | | eFuse_EnClk 0 |
| PeriCfg22 | | | eFuse_Prog[7:0] 0 | | | | | |
| PeriCfg23 | | | eFuse_Prog[15:8] 0 | | | | | |
| PeriCfg24 | | | eFuse_Prog[23:16] 0 | | | | | |
| PeriCfg25 | | | eFuse_Prog[31:24] 0 | | | | | |
| PeriCfg26 | | | linkResetFixedPattern[7:0] 0 | | | | | |
| PeriCfg27 | | | linkResetFixedPattern[15:8] 0 | | | | | |
| PeriCfg28 | | | linkResetFixedPattern[23:16] 0 | | | | | |
| PeriCfg29 | | | linkResetFixedPattern[31:24] 0 | | | | | |
| PeriCfg30 | | IfReLockThrCounter 0 | | | | IfLockThrCounter 0 | | |
| PeriCfg31 | - 0 | - 0 | TDCStrobeTest 0 | TDCClockTest 0 | | IfUnLockThrCounter 0 | | |