

	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
PixCfg0	–	RfSel		0	IBSel		0	CLSel
PixCfg1	–	0	QInjEn	1	0	QSel	0	0
PixCfg2	–	0	–	DACDiscri	0	HysSel	0	0
PixCfg3	–	0	–	ScanStart	CLKEn	Bypass	BufEn	RSTn
PixCfg4	0	0	0	0	DAC[7:0]	0	0	0
PixCfg5	0	0	0	0	0	0	0	DAC[9:8]
PixCfg6	enable_TDC	resetrn_TDC	autoReset_TDC	testMode_TDC	level_TDC			–
PixCfg7	–	–	–	workMode	disTrigPath	disDataReadout	addrOffset	0
PixCfg8	L1ADelay[0]	0	0	0	selfTestOccupancy	0	0	0
PixCfg9	0	0	0	0	L1ADelay[8:1]	0	0	0
PixCfg10	0	0	0	0	lowerCal[7:0]	0	0	0
PixCfg11	0	0	0	0	upperCal[5:0]	0	0	lowerCal[9:8]
PixCfg12	0	0	0	0	lowerTOA[3:0]	0	0	0
PixCfg13	0	0	0	0	0	0	0	0
PixCfg14	0	0	0	0	upperTOA[9:2]	0	0	0
PixCfg15	0	0	0	0	lowerTOT[7:0]	0	0	0
PixCfg16	0	0	0	0	upperTOT[6:0]	0	0	lowerTOT[8]
PixCfg17	0	0	0	0	lowerCalTrig[5:0]	0	0	upperTOT[8:7]
PixCfg18	0	0	0	0	0	0	0	0
PixCfg19	0	0	0	0	0	0	0	0
PixCfg20	0	0	0	0	0	0	0	0
PixCfg21	0	0	0	0	0	0	0	0
PixCfg22	0	0	0	0	0	0	0	0
PixCfg23	0	0	0	0	0	0	0	0
PixCfg24	–	–	–	–	0	0	0	0
PixCfg25	0	0	0	0	0	0	0	0
PixCfg26	0	0	0	0	0	0	0	0
PixCfg27	0	0	0	0	0	0	0	0
PixCfg28	0	0	0	0	0	0	0	0
PixCfg29	0	0	0	0	0	0	0	0
PixCfg30	0	0	0	0	0	0	0	0
PixCfg31	0	0	0	0	0	0	0	0