Bare Chip 01E2 #52, Pixel (3,3), Qinj S-Curve, PA Low Bias Current Mode THCal BL = 255 DAC LSB THCal NW = ± 3 DAC LSB DAC TH = $(8.362\pm0.268 [1/fC]) \cdot Q + (229.688\pm4.866)$ 95% Confidence Interval on Linear Fit DAC Threshold [LSB]

Charge Injected [fC]