

|           | Bit 7                       | Bit 6                    | Bit 5                             | Bit 4                | Bit 3                        | Bit 2                     | Bit 1             | Bit 0                 |
|-----------|-----------------------------|--------------------------|-----------------------------------|----------------------|------------------------------|---------------------------|-------------------|-----------------------|
| PeriCfg0  | FBDiv_skip<br>0             | clkTreeDisable<br>0      | CLKSel<br>0                       | disVCO<br>0          | disSER<br>0                  | disEOM<br>0               | disDES<br>0       | disCLK<br>0           |
| PeriCfg1  |                             | PLL_I<br>0               |                                   |                      |                              | PLL_BiasGen<br>0          |                   |                       |
| PeriCfg2  |                             | PLL_R<br>0               |                                   |                      |                              | PLL_P<br>0                |                   |                       |
| PeriCfg3  | VRefGen<br>0                | -<br>0                   | ENABLEPLL<br>0                    | PLL_vcoRailMode<br>0 |                              | PLL_vcoDAC<br>0           |                   |                       |
| PeriCfg4  | TS_PD<br>0                  | PS_ForceDown<br>0        | PS_Enable<br>0                    | PS_CapRst<br>0       |                              | PS_CPCurrent<br>0         |                   |                       |
| PeriCfg5  |                             |                          |                                   | PS_PhaseAdj<br>0     |                              |                           |                   |                       |
| PeriCfg6  |                             |                          |                                   | RefStrSel<br>0       |                              |                           |                   |                       |
| PeriCfg7  | GRO_TOARST_N<br>0           | GRO_Start<br>0           | CLK40_SetCM<br>0                  | CLK40_InvData<br>0   | CLK40_Equ<br>0               | CLK40_EnTer<br>0          | CLK40_EnRx<br>0   |                       |
| PeriCfg8  | GRO_TOA_Latch<br>0          | GRO_TOA_CK<br>0          | CLK1280_SetCM<br>0                | CLK1280_InvData<br>0 | CLK1280_Equ<br>0             | CLK1280_EnTer<br>0        | CLK1280_EnRx<br>0 |                       |
| PeriCfg9  | GRO_TOT_CK_Latch<br>0       | GRO_TOTRST_N<br>0        | FC_SetCM<br>0                     | FC_InvData<br>0      | FC_Equ<br>0                  | FC_EnTer<br>0             | FC_EnRx<br>0      |                       |
| PeriCfg10 |                             |                          | BCIDOffset[7:0]<br>0              |                      |                              |                           |                   |                       |
| PeriCfg11 |                             | emptySlotBCID[3:0]<br>0  |                                   |                      |                              | BCIDOffset[11:8]<br>0     |                   |                       |
| PeriCfg12 |                             |                          | emptySlotBCID[11:4]<br>0          |                      |                              |                           |                   |                       |
| PeriCfg13 | asyPLLReset<br>0            | asyLinkReset<br>0        | asyAlignFastcommand<br>0          |                      | readoutClockDelayPixel<br>0  |                           |                   |                       |
| PeriCfg14 | asyResetGlobalRead-out<br>0 | asyResetFastcommand<br>0 | asyResetChargeInj<br>0            |                      | readoutClockWidthPixel<br>0  |                           |                   |                       |
| PeriCfg15 | -<br>0                      | asyStartCalibration<br>0 | asyResetLockDetect<br>0           |                      | readoutClockDelayGlobal<br>0 |                           |                   |                       |
| PeriCfg16 |                             | LTx_AmplSel<br>0         |                                   |                      | readoutClockWidthGlobal<br>0 |                           |                   |                       |
| PeriCfg17 |                             | RTx_AmplSel<br>0         |                                   |                      | chargeInjectionDelay<br>0    |                           |                   |                       |
| PeriCfg18 | disLTx<br>0                 | onChipL1AConf<br>0       |                                   | fcDataDelayEn<br>0   | fcClkDelayEn<br>0            | fcSelfAlignEn<br>0        | softBoot<br>0     | disPowerSequence<br>0 |
| PeriCfg19 | disRTx<br>0                 | singlePort<br>0          | serRateRight<br>0                 |                      | serRateLeft<br>0             | linkResetTestPattern<br>0 | disScrambler<br>0 |                       |
| PeriCfg20 |                             | eFuse_TCKHP<br>0         |                                   |                      | triggerGranularity<br>0      |                           |                   | mergeTriggerData<br>0 |
| PeriCfg21 | -<br>0                      | -<br>0                   | eFuse_Bypass<br>0                 | eFuse_Start<br>0     | eFuse_Rstn<br>0              | eFuse_Mode<br>0           |                   | eFuse_EnClk<br>0      |
| PeriCfg22 |                             |                          | eFuse_Prog[7:0]<br>0              |                      |                              |                           |                   |                       |
| PeriCfg23 |                             |                          | eFuse_Prog[15:8]<br>0             |                      |                              |                           |                   |                       |
| PeriCfg24 |                             |                          | eFuse_Prog[23:16]<br>0            |                      |                              |                           |                   |                       |
| PeriCfg25 |                             |                          | eFuse_Prog[31:24]<br>0            |                      |                              |                           |                   |                       |
| PeriCfg26 |                             |                          | linkResetFixedPattern[7:0]<br>0   |                      |                              |                           |                   |                       |
| PeriCfg27 |                             |                          | linkResetFixedPattern[15:8]<br>0  |                      |                              |                           |                   |                       |
| PeriCfg28 |                             |                          | linkResetFixedPattern[23:16]<br>0 |                      |                              |                           |                   |                       |
| PeriCfg29 |                             |                          | linkResetFixedPattern[31:24]<br>0 |                      |                              |                           |                   |                       |
| PeriCfg30 |                             | IfReLockThrCounter<br>0  |                                   |                      |                              | IfLockThrCounter<br>0     |                   |                       |
| PeriCfg31 | -<br>0                      | -<br>0                   | TDCStrobeTest<br>0                | TDCClockTest<br>0    |                              | IfUnLockThrCounter<br>0   |                   |                       |