Bare Chip 01E2 #52, Pixel (15,9), Qinj S-Curve, PA Low Bias Current Mode THCal BL = 63 DAC LSB ---- THCal NW =  $\pm 3$  DAC LSB 200 DAC\_TH =  $(5.129\pm0.117 [1/fC])\cdot Q + (41.817\pm2.127)$ 95% Confidence Interval on Linear Fit DAC Threshold [LSB] 125 100 50 10 15 20 25 30 35 Charge Injected [fC] Bare Chip 01E2 #52, Pixel (14,9), Qinj S-Curve, PA Low Bias Current Mode THCal BL = 57 DAC LSB ---- THCal NW =  $\pm 7$  DAC LSB DAC\_TH =  $(1.412\pm0.128 [1/fC])\cdot Q + (57.604\pm2.328)$ 100 95% Confidence Interval on Linear Fit DAC Threshold [LSB] 90 70 60 10 15 25 30 35 Charge Injected [fC]