CMSC 417 Spring 2016 Lecture #3 2/3/2016

Agendal

The tentative exam/ploject schedule

Project O (how people are doing?) Gitting names

Gitting wires

Intro to routing protocols

Intro to routing protocols

Mead

CMSC419 Spling 2016 Lecture #3 2/3/2016 Scaling Wires) when does this fail? > > too many hosts (Ethernet is 1024?) these are => Wires need to be too long (Ethernet is 500 m) really the same so not enough capacity > have to solder or otherwise physically top into the wire Repeaters of two wires and they repeat everything gotten on one out the other Resenter | P Repeaters can be put on another wire This is all at the physical layer & talking signals & voltages - not frames

Mead

CMSC417 Spring 2016 Lecture # 3 2/3/2016 Like a repeater but with more than 2 wires

D D Hub D

OR

D Hub THI Problems => too many hosts -> still there => too many nosis => wires can't be long enough -> still there (now 2500m) > not enough capacity > still there => have to physically top the wire > fixed real problem is everyone hears every message ? To enable efficient callision detection, Ethernet regulies at most 4 repeators in any given path between 2 hosts

CMSC 417 Splins 2016 Lecture #3 2/3/2016 Switching => a switch is a hot that tries to forward a frame out only the "right" link (actually the interface on the sultch, which is called a port) DD D Switch D D butter, operates on frames more commonly today

D-[switch] -D

Switch -D

D-[switch] -D

D-[switch] -D

D-[switch] -D

D-[switch] -D

D-[switch] -D How do we fisher out what port is "right"? => Assuming only that addresses on the netroit are unique. side-note: this is free in Ethernet b/c Netvolk could rendols get as hinique profix and can then legg

from repeating what comes after

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Switching Contid!

finding the "right" port

static	tables [D]			
	[C]=[SI]-		E	-
	A		3 52	F
	To	5) = [S3] 1 Y	RI	
	ganda	2 1 -	an and a second	
		LH		

51	52	S 3
A >2	A -> 3	A ->
B -> 1	B -2	B -> 1
C-33	C = 3	C -> 0
D -> 0	D->3	D-50
モラト	F > 0	E > 0
F->1	F ->	F-70
6 31	6-2	G->3
H -> 1	11-72	H -> 2

build them by hand knowing the network

Why is this bad?

> hosts move

> people make mistakes typing

=) i , o

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Learning Bridges

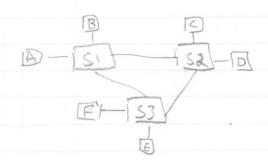
3) start with an empty table

- son a miss, send ont every port but the
- => record the source of every frame as being out that purt
- Dantometically populate tables as
- = allows for more capacity

A>C, D>E, F>B, and G-> H can all send at full speed assuming Switches support It

if A > E and C > F at the same time, buffers fill at SI and eventually both can only send at half rate

What if there is a loop?



=> tables start empy

=> A sends to D

=> SI sends to S2 \$ S3, S2 \$ S3 send to each other, 2 frames will exist forever